University of Mumbai

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विद्याविषयक प्राधिकरणे
सभा आणि सेवा विभाग(ए.ए.एम.एस)
रूम नं. १२८ एम.जी.रोड, फोर्ट,
मुंबई – ४०० ०३२
टेलिफोन नं. - ०२२ - ६८३२००३३

(नॅक पुनर्मूल्यांकनाद्वारे ३.६५ (सी.जी.पी.ए.) सह अ++ श्रेणी
विद्यापीठ अनुदान आयोगाद्वारे श्रेणी १ विद्यापीठ दर्जा)

क्र.वि.प्रा.स.से./आयसीडी/२०२५-२६/३७ दिनांक : २७ मे, २०२५

परिपत्रक:-

सर्व प्राचार्य/संचालक, संलग्नित महाविद्यालये/संस्था, विद्यापीठ शैक्षणिक विभागांचे संचालक/ विभाग प्रमुख यांना कळविण्यात येते की, राष्ट्रीय शैक्षणिक धोरण २०२० च्या अमंलबजावणीच्या अनुषंगाने **शैक्षणिक वर्ष २०२५-२६** पासून **पदवी व पदव्युत्तर अभ्यासक्रम** विद्यापरिषदेच्या दिनांक २८ मार्च २०२५ व २० मे, २०२५ च्या बैठकीमध्ये मंजूर झालेले सर्व अभ्यासक्रम मुंबई विद्यापीठाच्या www.mu.ac.in या संकेत स्थळावर NEP २०२० या टॅब वर उपलब्ध करण्यात आलेले आहेत.

मुंबई – ४०० ०३२
२७ मे, २०२५

(डॉ. प्रसाद कारंडे)
कुलसचिव

क्र.वि.प्रा.स.से.वि/आयसीडी/२०२५-२६/३७ दिनांक : २७ मे, २०२५
Desktop/ Pritam Loke/Marathi Circular/NEP Tab Circular

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3	The Deputy Registrar, Marks and Certificate Unit,. Vidyanagari dr.verification@mu.ac.in
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7	The Deputy Registrar, PRO, Fort, (Publication Section), Pro@mu.ac.in
8	The Deputy Registrar, Executive Authorities Section (EA) eau120@fort.mu.ac.in He is requested to treat this as action taken report on the concerned resolution adopted by the Academic Council referred to the above circular.
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4	P.A to all Deans of all Faculties
5	P.A to Finance & Account Officers, (F & A.O), camu@accounts.mu.ac.in

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1	The Chairman, Board of Deans pvc@fort.mu.ac.in
2	**Faculty of Humanities,** **Offg. Dean** 1. Prof.Anil Singh Dranilsingh129@gmail.com **Offg. Associate Dean** 2. Prof.Manisha Karne mkarne@economics.mu.ac.in 3. Dr.Suchitra Naik Naiksuchitra27@gmail.com
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AC – 20/5/2025
Item No. – 6.31 (N)

As Per NEP 2020

University of Mumbai

Syllabus for Major Vertical – 1, 4, 5 & 6		
Name of the Programme – B.E. (Electronics and Telecommunication Engineering)		
Faculty of Engineering		
Board of Studies in Electronics and Telecommunication Engineering		
U.G. Second Year Programme	**Exit Degree**	**U.G. Diploma in Electronics and Telecommunication Engineering**
Semester		**III & IV**
From the Academic Year		**2025-26**

University of Mumbai

(As per NEP 2020)

Sr. No.	Heading	Particulars
1	**Title of program** **O:____________________**	**B.E. (Electronics and Telecommunication Engineering)**
2	**Exit Degree**	**U.G. Diploma in Electronics and Telecommunication Engineering.**
3	**Scheme of Examination** **R: ____________**	NEP 40% Internal 60% External, Semester End Examination Individual Passing in Internal and External Examination
4	**Standards of Passing** **R: ______________**	**40%**
5	**Credit Structure** **R. TEU-560C** **R. TEU-560D**	**Attached herewith**
6	**Semesters**	**Sem. III & IV**
7	**Program Academic Level**	**5.00**
8	**Pattern**	**Semester**
9	**Status**	**New**
10	**To be implemented from Academic Year**	**2025-26**

Sd/-
Dr. Faruk Kazi
BoS-Chairman- Electronics and Telecommunication Engineering
Faculty of Technology

Sd/-
Dr. Deven Shah
Associate Dean
Faculty of Science & Technology

Sd/-
Prof. Shivram S. Garje
Dean
Faculty of Science & Technology

Preamble

To meet the challenge of ensuring excellence and NEP 2020 policy in engineering education, the issue of quality needs to be addressed, debated, and taken forward systematically. Accreditation is the principal means of quality assurance in higher education. The major emphasis of the accreditation process is to measure the outcomes of the program that is being accredited. In line with this, the Faculty of Science and Technology (in particular Engineering) of the University of Mumbai has taken the lead in incorporating the philosophy of NEP 2020 education in the process of curriculum development.

The second-year engineering course is a core training program to impart scientific and logical thinking training to learners in general, with a choice of course selection from the program core course, multidisciplinary minor, and vocational skill-enhanced course. Simultaneously, the objectives of NEP 2020 demand nurturing the core program and skills required for the Electronics and Telecommunication Engineering Branch of the learner. Keeping this in view, a pool of courses is offered in Core Courses covering fundamentals required to understand core and modern engineering practices and emerging trends in technology. Considering the shift in pedagogy and the convenience of a stress-free learning process, a choice-based subject pool is offered in the coursework under the heads of Electronics and Telecommunication Engineering for open electives and multidisciplinary minor courses in the third and fourth semesters. Essentially, to give a glimpse of trends in the industry under vocational and enhanced skill practices, the pool is offered to nurture and develop creative skills in contemporary industrial practices. Criteria met in the structure is the opportunity for learners to choose the course of their interest in all disciplines.

The Program Core Course Covers Electronics and Telecommunication engineering core courses. Also, OE and MDM where a pool of subjects are given for selection. Considering the present scenario, diverse choices need to be made available to fulfill the expectation of a learner to aspire for a career in the field of current trends of Technology and interdisciplinary research. Ability enhancement can be achieved in Undergraduate training by giving an objective viewpoint to the learning process and transitioning a learner from a rote learner to a creative professional. for the purpose Design Thinking is introduced in the First Semester to orient a journey learner to become a skilled professional. Considering the NEP-2020 structure of award of Certificate & Diploma at multiple exit-point pools of Vocational skills is arranged for giving exposure to the current Industry practices.

The faculty resolved that course objectives and course outcomes are to be clearly defined for every course so that all faculty members in affiliated higher education institutes understand the depth and approach of the course to be taught, which will enhance the learner's learning process. NEP 2020 grading system enables a much-required shift in focus from teacher-centric to continuous-based learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation, which will enhance the quality of education. Credit assignment for courses is based on a 15-week teaching-learning process for NEP 2020, however, the content of courses is to be taught in 12-13 weeks, and the remaining 2-3 weeks are to be utilized for revision, tutorial, guest lectures, coverage of content beyond the syllabus, etc.

There was a concern that in the present system, the second-year syllabus must not be heavily loaded to the learner and it is of utmost importance that the learner entering into the second year of an engineering course should feel at ease by lowering the burden of syllabus and credits. This is necessary for a learner to get accustomed to the new environment of a college and to create a bond between the teacher and the learner. The present curriculum will be implemented for the Second Year of Engineering from the academic year 2025-26. Subsequently, this system will be carried forward for Third Year and Final Year Engineering in the academic years 2026-27, and 2027-28, respectively.

Sd/-	Sd/-	Sd/-
Dr. Faruk Kazi	**Dr. Deven Shah**	**Prof. Shivram S. Garje**
BoS-Chairman-	**Associate Dean**	**Dean**
Electronics and Telecommunication Engineering	**Faculty of Science & Technology**	**Faculty of Science & Technology**
Faculty of Technology		

Under Graduate Diploma in Engineering- Electronics and Telecommunication.

Credit Structure (Sem. III & IV)

Level	Semester	Major: Mandatory	Major: Electives	Minor	OE	VSC, SEC (VSEC)	AEC, VEC, IKS	OJT, FP, CEP, CC,RP	Cum. Cr. / Sem.	Degree/ Cum. Cr.
	R. TEU-560C									
5.0	III	PCC301:3 PCC302:3 PCC303:3 PCC304:3 PCL301: 1 PCL302:1	--	--	OE:2	--	VEC: 2 HSL: 2	CEP: 2	22	UG Diploma 45
	R. TEU-560D									
	IV	PCC401:3 PCC402:3 PCC403:3 PCL401:1 PCL402:1	--	MDM: 4	OE:2	VSEC:2	VEC: 2 EEM:2	--	23	
	Cum Cr.	25	--	4	4	2	*2+2+2+2*	2	45	

Exit option: Award of UG Diploma in Major and MDM with 90 credits and additional 4 credits core **one** theory subject with 3 credits and **one** lab with 1 credit from one third year from where they want to take Exit degree. Along with theory and practical course student must compulsory do internship for **one month or 160 hours** which internship is equal to 4 credits.

[Abbreviation - OE – Open Electives, VSC – Vocation Skill Course, SEC – Skill Enhancement Course, (VSEC), AEC – Ability Enhancement Course, VEC – Value Education Course, IKS – Indian Knowledge System, OJT – on Job Training, FP – Field Project, CEP – Continuing Education Program, CC – Co-Curricular, RP – Research Project]

S.E. Electronics and Telecommunication Engineering Scheme

Semesters III and IV

Program Structure for Second Year of Electronics and Telecommunication Engineering

UNIVERSITY OF MUMBAI (With Effect from 2025-2026)

SEMESTER III

Course Code	Course Description	Teaching Scheme (Contact Hours)			Credit Assigned			
		Theory	Practical	Tutorial	Theory	Tutorial	Practical	Total Credits
2303111	Mathematics for Signal Analysis	2	--	1-	2	1	–	3
2303112	Electronic Devices & Linear Circuits	3	–	--	3	–	–	3
2303113	Digital System Design	3	--	--	3	–	–	3
2303114	Network Theory and Control System	3	--	--	3	–	–	3
OEC301	Open Elective	2#	--	--	2	–	–	2
2303115	Electronic Devices & Linear Circuits Laboratory	--	2	–	–	–	1	1
2303116	Digital System Design Laboratory	--	2	–	–	–	1	1
2303611	C++ and Java Programming	--	2*+2	–	–	–	2	2
2993511	Entrepreneurship Development	--	2*+2	---	–	–	2	2
2993512	Environmental Science	--	2*+2	--	–	–	2	2
Total		**13**	**16**	**01**	**13**	**01**	**08**	**22**

* Two hours of practical class to be conducted for full class as demo/discussion.

Theory / Tutorial 1 credit for 1 hour and Practical 1 credit for 2 hours

Institute shall offer a course for Open Elective from Science/Commerce/Management stream bucket provided by the University of Mumbai.

Course Code	Course Description	Examination scheme							
		Internal Assessment Test (IAT)			End Sem. Exam Marks	End Sem. Exam Duration (Hrs)	Term Work (Tw)	Oral & Pract.	Total
		IAT-I	IAT-II	Total (IAT-I) + IAT-II)					
2303111	Mathematics for Signal Analysis	20	20	40	60	2	25	--	125
2303112	Electronic Devices & Linear Circuits	20	20	40	60	2	--	--	100
2303113	Digital System Design	20	20	40	60	2	--	--	100
2303114	Network Theory and Control System	20	20	40	60	2	--	--	100
OEC301	Open Elective	20	20	40	60	2	--	--	100
2303115	Electronic Devices & Linear Circuits Laboratory	--	--	--	--	--	25	25	50
2303116	Digital System Design Laboratory	--	--	--	--	--	25	25	50
2303611	C++ and Java Programming	--	--	--	--	--	50	25	75
2993511	Entrepreneurship Development	--	--	--	--	--	50	--	50
2993512	Environmental Science	--	--	--	--	--	50	--	50
Total		**100**	**100**	**200**	**300**	**10**	**225**	**75**	**800**

Program Structure for Second Year of Electronics and Telecommunication Engineering

UNIVERSITY OF MUMBAI (With Effect from 2025-2026)

SEMESTER IV

Course Code	Course Description	Teaching Scheme (Contact Hours)			Credit Assigned			
		Theory	Practical	Tutorial	Theory	Tutorial	Practical	Total Credits
2304111	Neural Network and Fuzzy Logic	2	--	1	2	1	–	3
2304112	Microcontroller	3	–	--	3	–	–	3
2304113	Analog and Digital Communication	3	--	--	3	–	–	3
MDC401	Multidisciplinary Minor	3@	–	--	3	–	–	3
OEC401	Open Elective	2#	–	--	2	–	–	2
2304114	Microcontroller Laboratory	–	2	–	–	–	1	1
2304115	Analog and Digital Communication Laboratory	–	2	–	–	–	1	1
MDL401	Multidisciplinary Minor	–	2	–	–	–	1	1
2304411	Smart Embedded Systems with ATmega168/328	–	2*+2	–	–	–	2	2
2994511	Business Model Development	–	2*+2	–	–	–	2	2
2994512	Design Thinking	–	2*+2	–	–	–	2	2
Total		**13**	**18**	**01**	**13**	**01**	**09**	**23**

* Two hours of practical class to be conducted for full class as demo/discussion.

Theory / Tutorial 1 credit for 1 hour and Practical 1 credit for 2 hours

\# Students must select course for Open Elective from Science/Commerce/Management stream bucket provided by the University of Mumbai.

@ Institute shall offer a course for MDM from other Engineering Boards.

Course Code	Course Description	Examination scheme							
		Internal Assessment Test (IAT)			End Sem. Exam Marks	End Sem. Exam Duration (Hrs)	Term Work (Tw)	Oral & Pract.	Total
		IAT-I	IAT-II	Total (IAT-I) + IAT-II)					
2304111	Neural Network and Fuzzy Logic	20	20	40	60	2	25	--	125
2304112	Microcontroller	20	20	40	60	2	--	--	100
2304113	Analog and Digital Communication	20	20	40	60	2	--	--	100
MDC401	Multidisciplinary Minor	20	20	40	60	2	--	--	100
OEC401	Open Elective	20	20	40	60	2	--	--	100
2304114	Microcontroller Laboratory	--	--	--	--	--	25	25	50
2304115	Analog and Digital Communication Laboratory	--	--	--	--	--	25	25	50
MDL401	Multidisciplinary Minor	--	--	--	--	--	25	--	25
2304411	Smart Embedded Systems with ATmega168/328	--	--	--	--	--	50	25	75
2994511	Business Model Development	--	--	--	--	--	50	--	50
2994512	Design Thinking	--	--	--	--	--	50	--	50
Total		**100**	**100**	**200**	**300**	**10**	**250**	**75**	**825**

Vertical – 1 Major

Detail Syllabus

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303111	Mathematics for Signal Analysis	2	-	1	2	-	1	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2303111	Mathematics for Signal Analysis	20	20	40	60	2	25	--	125

Rationale:

The goal of this course is to make the learner conversant with the basic tools of mathematics for application in Electrical, Electronics, and Telecommunication engineering. The syllabus designed will help the learner build a foundation to model Signal Analysis problems mathematically, analyze and solve the same.

Prerequisite:

Applied Mathematics-I
Applied Mathematics-II

Course Objectives:

1. To introduce the concept of Laplace Transform and its application in solving ODE.
2. To familiarize with the concept of expanding periodic functions/signals in the form of Fourier Series.
3. To introduce the concept Fourier Transform and its applications.
4. To familiarize with the concept of Z-Transform for discrete functions/signals and its applications.
5. To familiarize with the concept of random variable and probability distributions with its applications in engineering and science.
6. To introduce concepts and fundamentals of Matrix algebra for engineering problems.

Course Outcomes:

On successful completion of the course learner will be able to:

1. Understand Laplace Transform and its application in solving ordinary differential equations.
2. Apply the Fourier series to expand the given periodic function/signal.
3. Apply Fourier Transform and its properties to transform the function/signal from one domain (time) to another domain(frequency).
4. Understand and apply Z-transform to discrete functions/signals.

5. Understand and apply the concept of random variable and standard probability distributions.

6. Apply the concepts of eigenvalues and eigenvectors in engineering problems.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	**Laplace Transform**	Laplace Transform & Inverse Laplace Transforms of Standard Functions like $e^{at}, \sin(at), \cos(at), \sinh(\mathrm{at}), \cosh(\mathrm{at})$ and t^n, $where\ n \geq 0$ (without proof) First Shifting theorem, Laplace Transform of derivatives and integrals (Properties without proof) Inverse Laplace transform using First Shifting Theorem and Partial fractions method. Applications of Laplace Transforms for Solutions to ODE to electrical & electronics circuit problems. (Only first order differential equations). **Self -Learning Topics:** Heaviside &Dirac Delta function, Applications of Laplace Transforms for Solutions to ODE (Higher order differential equations)	**5**	**CO1**
II	**Fourier Series**	Fourier series of periodic function with period 2π and 2l. Fourier series of even and odd functions (-l, l), Complex form of Fourier Series$(-\infty, \infty)$(No deductions on the basis of Fourier Series) **Self-learning Topics:** Parseval's Identity, Half-range Cosine/sine Series, Fourier Integral	**4**	**CO2**
III	**Fourier Transform**	Fourier transform, Fourier Transform of Heaviside Unit step Function and Dirac Delta Function. Linearity Property, Time shifting Property, Frequency Shifting Property, convolution and Modulation property, Question related to (Electrical & Extc engineering) **Self -Learning Topics:** Standard Signals-Stop, input, Delta, Exponential Signals	**5**	**CO3**
IV	**Z-Transform**	Definition and Region of Convergence, Transform of Standard Functions: $\{k^n a^k\}, \{a^k\}, \{c^k \sin(\alpha k + \beta)\}, \{c^k sinh\alpha k\}, \{c^k \cos(\alpha k + \beta)\}, \{c^k cosh\alpha k\}$. Properties of Z Transform: Change of Scale, Shifting Property, Multiplication, and Division by k, Convolution theorem. Inverse Z transform: Partial Fraction Method, Question related to (Electrical & Extc engineering) **Self-learning Topics:** Initial value theorem, Final value theorem, Inverse Using Convolution Theorem	**4**	**CO4**
V	**Random Variable & Probability Distribution**	Discrete and Continuous random variables, Probability mass and density function, Probability distribution for random variables, Expectation, Variance, Poisson and Normal distribution (No question on finding the mean and variance)	**4**	**CO5**

		Self-Learning Topics: Binomial Distribution, Moment generating function		
VI	**Linear Algebra (Theory of Matrices**)	Characteristic Equation, Eigenvalues and Eigenvectors, and properties of eigenvalues (without proof) Similarity of matrices, diagonalizable and non-diagonalizable matrices **Self-learning Topics**: Cayley-Hamilton Theorem and its usage in reduction of higher degree polynomials Derogatory and non-derogatory matrices, Function of a square Matrix	**4**	**CO6**

Note:

- **Tutorial shall be conducted batch wise.**
- **No Questions to be asked from Self-Learning Topics.**

Text / Reference Books:

1. Higher Engineering Mathematics, Dr. B. S. Grewal, Khanna Publication.
2. Advanced Engineering Mathematics, Erwin Kreyszig, Wiley Eastern Limited.
3. Advanced Engineering Mathematics, R. K. Jain and S. R. K. Iyengar, Narosa publication
4. Signals and Systems, A Nagoor Nani, Tata McGraw Hill.
5. Probability, Statistics and Random Processes, T. Veerarajan, Mc. Graw Hill education.

Online References:

Sr. No.	Website Name
1.	https://nptel.ac.in/courses/111/106/111106139/
2.	https://www.youtube.com/watch?v=2CP3m3EgL1Q
3.	https://www.youtube.com/watch?v=Hw8KHNgRaOE

Term Work:

General Instructions:

1. Batch wise tutorials are to be conducted. The number of students per batch should be as per the university pattern for practical.

2. Students must be encouraged to write at least 6 class tutorials on entire syllabus.

3. A group of 4-6 students should be assigned a self-learning topic. Students should prepare a presentation/problem solving of 10-15 minutes. This should be considered as mini project in Engineering mathematics. This project should be graded for 10 marks depending on the performance of the students.

The distribution of Term Work marks will be as follows –

1.	Attendance (Theory and Tutorial)	05 marks
2.	Class Tutorials on entire syllabus	10 marks
3.	Mini project	10 marks

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303112	Electronic Devices and Linear Circuits	3	-	-	3	-	-	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2303112	Electronic Devices and Linear Circuits	20	20	40	60	2	--	--	100

Prerequisite:

1. Engineering Physics-I
2. Engineering Physics-II
3. Basic Electrical Engineering

Course Objectives:

1. To explain functionality of different electronic devices.
2. To perform DC and AC analysis of small signal amplifier circuits.
3. To explain working of differential amplifiers and its applications in operational amplifiers.
4. To understand the concept working principles of Linear Integrated Circuits.
5. To Perform analysis of Linear Integrated Circuits.
6. To design circuits and systems for particular applications using Linear Integrated Circuits.

Course Outcomes:

After successful completion of the course student will be able to

1. Explain working of various electronics devices.
2. Derive expressions for performance parameters of BJT and MOSFET circuits.
3. Understand the fundamentals and areas of applications for the Integrated circuits.
4. Develop the ability to design Linear and Non-Linear application of Integrated Circuits.
5. Cultivate the skill of designing Timer circuits.
6. Gain the skill to design Voltage regulator using Integrated Circuits.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
0	**Prerequisite**	Introduction of Diode, BJT, JFET, MOSFET	01	CO1
I	**Biasing of BJT and MOSFET**	1.1 Construction, working and characteristics of BJT (CE configuration) and E-MOSFET (CS configuration). 1.2 Concept of DC load line, Q point and regions of operations, Biasing circuits for BJT (Fixed bias & Voltage divider Bias). 1.3 DC load line and region of operation for E-MOSFET, Biasing circuits for E-MOSFET (Drain to Gate bias & voltage divider bias).	06	CO1
II	**Small Signal Analysis of BJT and MOSFET Amplifier.**	2.1 Concept of AC load line and Amplification, Small signal analysis (Zi, Zo, Av and Ai) of CE amplifier using hybrid pi model. 2.2 Small signal analysis (Zi, Zo, Av) of CS (for E-MOSFET) amplifiers. 2.3 Frequency response of amplifier, Effect of coupling bypass and parasitic capacitor on frequency response. Millers theorem.	06	CO2
III	**Introduction to Differential Amplifier and Operational Amplifier.**	3.1 E-MOSFET Differential Amplifier, Differential and common mode gain, CMRR, differential and common mode input impedance. 3.2 Block diagram of Op-Amp , Ideal and Practical characteristics of Op-Amp. Open loop and Closed loop configuration of Op-Amp.. 3.3 Inverting and Non-inverting Amplifier using Op-Amp, Summing Amplifier, Difference Amplifier.	06	CO3
IV	**Linear and Non-Linear Applications of Operational Amplifier**	4.1 Integrator & differentiator (ideal & practical), Active Filters: First and Second order active low pass, high pass. 4.2 Comparators: Inverting comparator, non-inverting comparator. Schmitt Triggers: Inverting Schmitt trigger, non-inverting Schmitt trigger. 4.3 Positive feedback, Barkhausen's criteria, Sine Wave Oscillators: RC phase shift oscillator, Wien bridge oscillator.	08	CO4

V	**Timer IC555 and its applications.**	5.1 IC 555 Timer: Block Schematic, Functional Diagram, Working of IC 555. 5.2 Design of Monostable and Astable multivibrator using IC 555. 5.3 Applications of astable and monostable multivibrator as Pulse Width Modulator and Pulse Position Modulator.	06	CO5
VI	**Voltage Regulators.**	6.1 Block diagram of regulated DC power supply. Functional block diagram, working and design of three terminal fixed voltage regulators (78XX, 79XX series). 6.2 Functional block diagram, working and design of general purpose IC 723 (HVLC and HVHC). 6.3 Design of regulator using three terminal IC LM 317.	06	CO6

Text Books:

1. Donald A. Neamen, "Electronic Circuit Analysis and Design", Tata McGraw Hill, 2nd Edition

2. D. Roy Choudhury and S. B. Jain, "Linear Integrated Circuits", New Age International Publishers, 4th Edition.

3. Ramakant A. Gaikwad, " Op-Amps and Linear Integrated Circuits", Pearson Prentice Hall, 4th Edition

References:

1. S. Salivahanan, N. Suresh Kumar, "Electronic Devices and Circuits", Tata Mc-Graw Hill, 3rd Edition
2. Boyiestad and Nashelesky, " Electronic Devices and Circuits Theory", Pearson Education,11th Edition
3. A.K. Maini, " Electronic Devices and Circuits", Wiley
4. K.R. Botkar, "Integrated Circuits", Khanna Publisher (2004)
5. David A. Bell, " Operation Amplifiers and Linear Integrated Circuits", Oxford University Press, Indian Edition.

Online References:

Sr. No.	**Website Name**
1.	NPTEL/ Swayam Course: Course: Integrated Circuits and Applications By Prof. Shaik Rafi Ahamed (IIT Guwahati) https://onlinecourses.nptel.ac.in/noc25_ee43/preview

2.	Course: ICs MOSFETs Op-Amps & Their Applications By Prof. Hardik Jeetendra Pandya (IISc Bangalore); https://swayam.gov.in/nd1_noc20_ee13/preview

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303113	Digital System Design	3	--	--	3	--	--	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2303113	Digital System Design	20	20	40	60	2	--	--	100

Prerequisite:
Basic Electrical Engineering

Course Objectives:

1. To understand number system representations and their inter-conversions used in digital electronic circuits.
2. To understand the functionalities, and Characteristics of Logic Families and Minimization techniques to realise logical operations.
3. To analyze digital logic processes and to implement logical operations using various combinational logic circuits.
4. To analyze, design and implement the logical operations using different sequential logic circuits.
5. To equip students with the knowledge and skills to design, and implement various registers, counters, and programmable logic devices.
6. To get acquainted with the basics of VHDL language.

Course Outcomes:

On successful completion of the course student will be able to:

1. Apply the concepts of number systems and perform code conversions.
2. Classify logic families, Understand Digital circuits and apply minimization techniques to implement logical functions.
3. Analyze, design and implement combinational logic circuits.
4. Analyze, design and implement sequential logic circuits.
5. Analyze, design and implement digital circuits using different registers, counters, and programmable logic devices.
6. Use HDL & appropriate EDA tool for logic design and simulation using VHDL/Verilog.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	**Number Systems and Codes**	Review of Binary, Octal and Hexadecimal Number Systems, their inter-conversion, Gray code and BCD code, Binary Addition, Subtraction using 1's and 2's Complement method.	**02**	CO1
II	**Logic families and Minimization Techniques**	Classification of logic families: Unipolar and Bipolar Logic Families, Characteristics of Digital ICs, TTL and CMOS comparison. Digital logic gates, Universal gates, Realization using NAND and NOR gates, Boolean Algebra, De Morgan's Theorem. Minimization of Boolean expressions :- SOP, POS, and Karnaugh map (up to 4 variables)	**08**	CO2
III	**Combinational Logic Circuits**	Adder, Subtractor, Multiplexer, De-multiplexer, Code Converter, BCD adder, Magnitude Comparator, Parallel Adder, Implementation of Logic expressions using Multiplexers, De-multiplexers, Encoders and Decoders.	**08**	CO3
IV	**Sequential Logic Circuits**	Flip flops (FF): SR, JK, T, D, Master Slave JK flip flops, Truth table, excitation table, triggering methods, and flip flop conversions. Counters: Asynchronous and Synchronous - MOD N, UP/DOWN, Decade counter, Frequency division, Finite State Machine: Introduction to Moore and Mealy machines - Block diagram, state diagram, state tables.	**10**	CO4
V	**Shift Registers and Programmable Logic Devices**	Registers: SISO, SIPO, PISO, PIPO, Universal Shift registers, Ring counter, Johnson counter, Sequence generator. Structure of Programmable Logic Devices (PLDs), Function implementation with Programmable Logic Array (PLA) and Programmable Array Logic (PAL). Introduction to CPLD and FPGA.	**06**	
VI	**Introduction to VHDL**	VLSI Design flow (Frontend): Design entry: Schematic different modeling styles in VHDL, Data types and objects, Synthesis and Simulation, implementation of combinational and sequential logic using VHDL.	**05**	CO6

Text Books:

1. R.P. Jain, "Modern Digital Electronics", Tata McGraw Hill Publication, 4th Edition.
2. Morris Mano, Michael D. Ciletti, "Digital Design", Pearson Education, Fifth Edition (2013).
3. A. Anand Kumar, "Fundamentals of Digital Circuits", PHI, Fourth Edition (2016).

4. J. Bhaskar A Verilog HDL Primer , Third Edition, Star Galaxy publishing
5. Sameer Palnitkar "Verilog HDL, A guide to digital
6. Douglas Perry, "VHDL programming", McGraw Hill, fourth edition.

References:

1. John F. Warkerly, "Digital Design Principles and Practices", Pearson Education,Fifth Edition (2018).
2. Digital fundamentals by FLOYD & JAIN, Pearsons Pub
3. Charles Roth, "Digital System Design using VHDL", Tata McGraw Hill

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303114	Network Theory and Control System	3	-	-	3	-	-	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2303114	Network Theory and Control System	20	20	40	60	2	--	--	100

Prerequisite:

1. Basic Electrical Engineering
2. Engineering Mathematics II

Course Objectives:

1. To evaluate the Circuits using network theorems, study network Topology, network Functions and two port networks.
2. To analyze the Circuits in time and frequency domain.
3. To synthesize passive network by various methods.
4. To analyze fundamental concepts of mathematical modeling, time response and Frequency response.
5. To develop concepts of stability and its assessment criteria.

Course Outcomes:

After successful completion of the course student will be able to

1. Evaluate circuit using network theorems.
2. Apply the time and frequency method of analysis.
3. Analyze the network function and finding the various parameters of two port network
4. Analyze the response and determine the transfer function of Control System
5. Understand the analysis of systems in time domain and predict stability of given system using appropriate criteria.
6. Understand the analysis of systems in frequency domain and predict stability of given system using appropriate criteria.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	**Electrical Circuit Analysis**	**I.1)** Analysis of Circuits with dependent sources using generalized loop and node analysis, super mesh and super node analysis technique Network Theorems with dependent sources: Superposition, Thevenin's, Norton's and Maximum Power Transfer Theorems **(Use only DC source)**	4	CO1
II	**Time and frequency domain analysis**	**II.1)** Time domain analysis of R-L and R-C Circuits: Forced and natural response, initial and final values. Solution using first order and second order differential equation with step signals	8	CO 2
		II.2) Frequency domain analysis of R-L-C Circuits: Forced and natural response, effect of damping factor. Solution using second order equation for step signal.		
III	**Network functions and Two Port Networks**	**III.1)** Network functions for the one port and two port networks, driving point and transfer functions, Poles and Zeros of Network functions.	6	CO3
		III.2) Two Port Parameters: Open Circuits, short Circuit, Transmission and Hybrid parameters, relationship among parameters, conditions for reciprocity and symmetry		
IV	**Analysis and response of control system**	**IV.1)** Open and closed loop systems, Transfer function modeling (Electrical only), Block diagram reduction techniques and Signal flow graph.	8	CO4
		IV.2) Dynamic Response: Standard test signals, transient and steady state behavior of first and second order systems, steady state errors in feedback control systems and their types.		
V	**Stability Analysis in Time Domain**	**V.1)** Concept of stability: Routh and Hurwitz stability criterion	6	CO5
		V.2) Root locus Analysis: Root locus concept, general rules for constructing root-locus, root locus analysis of control system.		
VI	**Stability Analysis in Frequency Domain**	**VI.1)** Frequency domain specification, Relationship between time and frequency domain specification of system, stability margins	7	CO6
		VI.2) Bode Plot: Magnitude and phase plot, Method of plotting Bode plot, Stability margins and analysis using Bode plot. Concept of Polar plot		

Textbooks:

1. Franklin F Kuo, “Network Analysis and Synthesis”, Wiley Toppan, 2 nd ed. ,1966.
2. M E Van Valkenburg, “Network Analysis”, Prentice-Hall of India Pvt Ltd, New Delhi, 26th Indian Reprint, 2000.

References:

1. A. Chakrabarti, “Circuit Theory”, Dhanpat Rai & Co., Delhi, 6th Edition.
2. A. Sudhakar, Shyammohan S. Palli “Circuits and Networks”, Tata McGraw-Hill education.
3. Smarajit Ghosh “Network Theory Analysis & Synthesis”, PHI learning.
4. K.S. Suresh Kumar, “Electric Circuit Analysis” Pearson, 2013.
5. D. Roy Choudhury, “Networks and Systems” , New Age International, 1998.
6. Nagrath, M.Gopal, “Control System Engineering”, Tata McGrawHill.
7. Rangan C. S., Sarma G. R. and Mani V. S. V., "Instrumentation Devices And Systems", Tata McGraw-Hill, 2nd Ed.,2004.
8. K.Ogata, “Modern Control Engineering, Pearson Education”, IIIrd edition.

NPTEL / Swayam Course:

1. Course: Basic Electrical Circuits By Prof. Nagendra Krishnapura (IIT Madras); https://swayam.gov.in/nd1_noc20_ee64/preview.
2. Course: Control Systems By Prof. C. S. Shankar Ram (IIT Madras); https://swayam.gov.in/nd1_noc20_ee90/preview

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303115	Electronic Devices & Linear Circuits Laboratory	--	2	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2303115	Electronic Devices & Linear Circuits Laboratory	--	--	--	--	25	25	50

Laboratory Objectives:

1. To make students familiar with equipment and measuring instruments used to perform this laboratory course.
2. To provide hands on experience to develop laboratory setup for performing given experimental using various equipment, electronic devices and measuring instruments.
3. To develop an ability among students to gather appropriate data and analyse the same to relate theory with practical.
4. To develop trouble shooting abilities among students

Laboratory Outcomes:

After successful completion of the course student will be able to

1. Know various equipment used in this laboratory course.
2. Understand how to make use of various devices and equipment to perform laboratory work.
3. Perform given experiment by making proper connections between various components, equipment and measuring devices for this course.
4. Acquire requisite data and analyze the same for this course.
5. Evaluate various parameters of the given circuit for this course.
6. Design the circuit for a given application for this course.

Suggested List of Experiments:

Sr No	List of Experiments	Hrs.
01	To study BJT biasing Circuits.	2
02	To Study BJT as CE amplifier.	2
03	To study EMOSFET biasing circuits	2
04	To study EMOSFET as CS amplifier.	2

05	**Simulations Experiment on study of Frequency Response of CS amplifier.**	**2**
06	**Simulations Experiment on study of Differential amplifier**	**2**
07	**Design and Implementation of Adder circuits using OPAMP.**	**2**
08	**Design and Implementation of Difference Amplifier using OPAMP.**	**2**
09	**Design and analyze Integrator circuit using OPAMP.**	**2**
10	**Design and analyze Differentiator circuit using OPAMP.**	**2**
11	**Design and analyze Schmitt trigger using OPAMP.**	**2**
12	**Design and analyze RC phase shift Oscillator.**	**2**
13	**Design and analyze first order High pass and Low pass filter.**	**2**
14	**Design of Monostable Multivibrator Circuit using 555 Timer**	**2**
15	**Design of Astable Multivibrator Circuit using 555 Timer**	**2**

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals' based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical & Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303116	Digital System Design Laboratory	--	2	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2303116	Digital System Design Laboratory	--	--	--	--	25	25	50

Laboratory Objectives:

1. To get familiarise with basic building blocks of Digital System Design and verify the operation of various digital ICs.
2. To understand and implement digital circuits for code conversion.
3. To train students to design and implement combinational circuits.
4. To instruct students on how to design and implement sequential circuits.
5. To understand digital logic simulation using the EDA tool.

Laboratory Outcomes:

After successful completion of the course student will be able to

1. Identify various Digital ICs and basic building blocks of digital system design
2. Design and implement combinational circuits like adder, subtractor, multiplexer, code converters etc.
3. Identify and understand the working of various types of flip flops and their interconversions.
4. Design and implement basic sequential circuits such as counters, registers etc
5. Develop and simulate VHDL architectural representations of digital systems and components using structural, behavioural, or data flow concepts

Suggested List of Experiments:

Sr No	List of Experiments	Hrs.
01	Study of characteristics of typical TTL and CMOS IC's like fan out, noise margin, propagation delay.	02
02	Implement AND, OR, NOT, EXOR, EX-NOR gates using Universal gates NAND and NOR.	02
03	Simplify the logical expressions using Boolean algebra/k-map technique and implement using logic gates.	02

04	Implement digital circuits to perform code conversions like Binary to Gray and Gray to Binary, BCD to 7 segment decoder operations.	02
05	Design and implement Encoder/ Decoder using IC.	02
06	Design and implement logic equations using Multiplexer IC.	02
07	Flip-flop conversions JK to D, JK to T and D to T FF.	02
08	Design and implementation of ripple and synchronous counters using JK and D FF and additional gates.	02
09	Design of counter using ICs like 7490/93 (ripple) and 74192/193(synchronous)	02
10	Study of Universal Shift Register using IC-74194.	02
11	Design a Ring/ Johnson's counter using IC-74194.	02
12	Implement a universal gates using VHDL/Verilog	02
13	Implement adder circuits using VHDL/Verilog	02
14	Design a Multiplexer using VHDL/Verilog	02
15	Design a 3-bit linear feedback shift register (LFSR) using VHDL/Verilog	02
16	Design a 3-bit Array Multiplier using VHDL/Verilog	02
17	Design a 2-bit Vedic Multiplier using VHDL/Verilog	02
18	Design and implementations of random sequence counter using D FF or JK FF ICs	02
19	Comparator using IC 7485 and Parity generator and checker using X-OR gate	02
20	Binary and BCD adders and Subtractor using IC 7483 and gates	02
21	Design asynchronous/synchronous MOD N counter using IC7490	02
22	Design and implement Magnitude Comparator.	02

Sr. No.	List of Assignments / Tutorials	Hrs.
01	Number Systems and Interconversions, Binary Codes.	01
02	Boolean Algebra and Minimization using K-Map.	01
03	Digital logic gates, Universal gates, Realization using NAND and NOR gates.	01
04	Design of Combinational and Sequential Logic Circuits.	01
05	PLDs and VHDL.	01

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals' based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical & Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304111	Neural Networks and Fuzzy Logic	2	--	1	2	--	1	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2304111	Neural Networks and Fuzzy Logic	20	20	40	60	2	25	--	125

Prerequisite:

1. Applied Mathematics-I
2. Applied Mathematics-II
3. Digital System Design

Course Objectives:

1. To study the basics of biological Neural Networks.
2. To understand the various terminologies in Artificial Neural networks.
3. To understand the different types of Artificial Neural Networks.
4. To study fuzzy logic and to provide knowledge of fuzzy logic to design the real-world fuzzy systems.

Course Outcomes:

After successful completion of the course student will be able to

1. Differentiate Biological system from Artificial Neuron.
2. Explain the different learning models and distinguish between the different types of supervised and unsupervised learning neural networks.
3. Evaluate the given neural network for specific input patterns and activation functions.
4. Apply the concept of competitive neural networks for clustering applications.
5. Understand the basic concept of fuzzy sets and fuzzy relations.
6. Develop innovative solutions to real-world problems using fuzzy logic

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	**Introduction to Neural Networks**	Biological neurons and its working Artificial neurons, Terminologies used in Artificial Neural Networks, Artificial Neuron Model, Applications of ANN, McCulloch-Pitts Model for AND, OR and NOT Gates	4	CO1
II	**Fundamentals of Artificial Neural Networks**	Activation Functions, Neural network architectures, Supervised, Unsupervised, Reinforcement learning rules, Linear Separability, Design of AND / OR gate using linear separability, XOR problem	4	CO2
III	**Supervised Learning**	Single Layer Perceptron, Adaline Network, Multilayer Perceptron, Error Back propagation algorithm	5	CO3
IV	**Unsupervised Learning**	Neural network based on competition: Kohonen Self Organizing Maps and Applications, learning vector quantization	4	CO4
V	**Introduction to Fuzzy Logic**	Introduction to Fuzzy Sets, Fuzzy set operations, Properties of Fuzzy sets. Fuzzy Relations: Cartesian product of Relation, Fuzzy Max-Min and Max-Product Composition.	4	CO5
VI	**Fuzzy Systems**	Features of Membership functions, Fuzzification (Intuition Method), Defuzzification (Centroid and mean of Maximum), Fuzzy Inference System and its types, Designing Fuzzy logic control systems like washing machines, and train brake control.	5	CO6

Text Books:

1. S.N. Sivanandam & S.N.Deepa , “Principles Of Soft Computing “, Wiley India Pvt. Limited, 2007
2. Timothy J Ross, “Fuzzy Logic With Engineering Applications”, John Willey And Sons, West Sussex, England, 2005.
3. Jack M. Zurada, “Introduction To Artificial Neural Systems”, PWS Publishing Co., Boston, 2002.

References:

1. Kosko, B, “Neural Networks And Fuzzy Systems: A Dynamical Approach To Machine Intelligence”, Prentice hall, New Delhi, 2004.

2. S. Rajasekaran, And G. A. Vijayalakshmi Pai , “Neural Networks, Fuzzy Logic And Genetic Algorithms : Synthesis, And Applications”, Prentice Hall Of India, 2007.
3. D. K. Pratihar , “Soft Computing” , Narosa, 2008.
4. Simon Haykin, “Neural Networks: A Comprehensive Foundation,” 2nd Edition, Prentice-Hall, 1999

Recommended Swayam NPTEL Courses:

1. Fuzzy Logic and Neural Networks by Prof. Dilip Kumar Pratihar , IIT Kharagpur
2. Fuzzy Sets, Logic and Systems & Applications, By Prof. Nishchal Kumar Verma, IIT Kanpur
3. Soft Computing Techniques, By Dr. T Subha , National Institute of Technical Teachers Training and Research (NITTTR), Taramani, Chennai.

Term Work: General Instructions

1. Batch wise tutorials are to be conducted. The number of students per batch should be as per University pattern for practical.
2. Students must be encouraged to write Python Programs in tutorial class only.
3. Each Student has to write at least 4 Python tutorials and at least 6 class tutorials on entire syllabus.
4. Suggested List for Python tutorials

i. Write a program to evaluate various activation functions
ii. Write a program for perceptron training algorithm and test it for two input AND & OR gate function
iii. Write a program for training and testing of Multilayer Perceptron for two input EX-OR gate
iv. Write a program for training and testing of Kohonen Self Organizing map for clustering application
v. Write a program to do the Fuzzy Set Operations: AND, OR, D-Morgan’s theorem.
vi. Write a program to find the fuzzy relations and compositions

The distribution of Term Work marks will be as follows

1	Attendance (Theory and Tutorial)	**05**
2	Class Tutorials on entire syllabus	**15**
3	Python Tutorials	**05**

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304112	Microcontrollers	3	-	-	3	-	-	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2304112	Microcontrollers	20	20	40	60	2	--	--	100

Prerequisite:
Digital System Design

Course Objectives:

1. To acquire a comprehensive understanding of the essential components and systems that constitute microcomputers
2. To understand the underlying principles, structures, and functionalities of memory systems
3. To understand the architecture and instructions of 8051 microcontrollers.
4. To apply the knowledge of 8051 I/O ports, Timer, Counter, Interrupts, serial port and write the programs related to same
5. To understand the concepts of advanced microcontroller ARM7
6. To apply the concepts of embedded systems and recent microcontrollers

Course Outcomes:

After successful completion of the course student will be able to

1. Understand of the fundamental components and systems integral to microcomputers
2. Understand the memory systems to optimize system performance.
3. Understand the architecture and instructions of 8051 microcontrollers
4. Apply the concepts of I/O ports, Timer, Counter, Interrupts, serial port for 8051 microcontroller programming.
5. Understand the ARM7 microcontroller architecture and its suitability for embedded systems and real-time applications.
6. Apply the concept of embedded systems and recent advancements in microcontroller technologies and explore their impact on modern applications.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
0	**Prerequisite**	Digital system design	-	-
I	**Overview of Microprocessor based System**	**Overview of Components and Systems of Microcomputers** Introduction to the basic components of microcomputers, Significance of signal lines of memory. The actions performed by the microprocessor to fetch and execute instructions from memory after RESET. Concept of RISC & CISC Architecture. Harvard & Von Neumann Architecture. **Self-learning Topics:** Comparative study of different microprocessors	05	CO1
II	**The Memory Systems**	Concepts of primary memory and secondary memory, Need of Secondary memory. Types of Semiconductor Memory, Features of SRAM and DRAM. Cache Memory and its need. Concept of virtual memory, Segmentation and Paging.	04	CO2
III	**8051 Microcontroller & Assembly Language Programming**	Comparison between Microprocessor and Microcontroller. Features, architecture and pin configuration. CPU timing and machine cycle. Memory organization. Addressing modes. Instruction set. Need of Assembler & Compiler, Assembler Directives, Programs related to: arithmetic, logical operations.	09	CO3
IV	**8051 I/O Ports, Timer/Counters, Interrupts, Serial Port communication & Interfacing**	8051 Input / Output ports and Port structure. 8051 Timer/Counter, Timer modes. 8051 Interrupt, 8051 serial communication & modes. Assembly language program related to: delay subroutine, input & output port, timer, counter, serial port and Interrupt. Interfacing LEDs, Relay and switches.	09	CO4
V	**ARM7**	Introduction and Features of ARM 7, Architectural inheritance, Pipelining, Programmer's Model, Brief introduction of exception and interrupt handling. Concept of cortex A, cortex R and Cortex M. Instruction Set, Data processing, Data Transfer, Control flow.	08	CO5
VI	**Study 8 bit microcontroller Applications**	Definition of Embedded System, Embedded Systems Vs General Computing Systems. Factors to be Considered in Selecting a Microcontroller for an	04	CO6

		Application. Understanding features of ATMEGA 328, LPC 2148, MSP 430 and STM 32 Microcontrollers. Case Study on microcontroller based applications		

Text Books:

1. M. A. Mazidi, J. G. Mazidi and R. D. Mckinlay, “The 8051 Microcontroller & Embedded systems”, Pearson Publications, Second Edition 2006.
2. C. Kenneth J. Ayala and D. V. Gadre, “The 8051 Microcontroller & Embedded system using assembly & ‘C’ ”, Cengage Learning, Edition 2010
3. Raj Kamal, “Microcontrollers” Architecture, Programming, Interfacing and System Design”, Pearson Education India, Second Edition 2011
4. N. Senthil Kumar, M. S. P. S. A. Kumar, “Microprocessor and Microcontroller”, Oxford University Press, 2nd edition
5. I. Scott MacKenzie and Raphael C.W. Phan, “The 8051 Microcontroller”, Pearson Education International, 4th edition
6. Steve Furber, “ARM System on chip Architecture”, Pearson, 2nd edition

References:

1. “MCS@51 Microcontroller, Family User’s Manual” Intel
2. ATmega328P 8-bit AVR Microcontroller with 32K Bytes In-System Programmable Flash datasheet, Atmel
3. James A. Langbridge, “Professional Embedded Arm Development”, Wrox, John Wiley Brand& Sons Inc., Edition 2014
4. Andrew N. Sloss, Dominic Symes, Chris Wright, “ARM System Developer's Guide: Designing and Optimizing System Software", Elsevier
5. The 8051 Microcontroller and Embedded Systems, “Manish K Patel”, McGraw Hill 2014

Online References:

Sr. No.	Website Name
1.	Microprocessors and Microcontrollers By Prof. Santanu Chattopadhyay (IIT Kharagpur) - https://swayam.gov.in/nd1_noc20_ee42/preview
2.	PLC and Microcontroller By Dr Ritula Thakur (NITTTR, Chandigarh) - https://onlinecourses.swayam2.ac.in/ntr25_ed25/preview

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304113	Analog and Digital Communication	3	-	-	3	-	-	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2304113	Analog and Digital Communication	20	20	40	60	2	--	--	100

Prerequisite:

- Elements of Telecommunication
- Electronic Devices and Linear Circuits
- Digital System Design

Course Objectives:

1. To demonstrate the fundamental concepts of Analog and Digital Communication Systems.
2. To understand various Analog and Digital Modulation and Demodulation techniques.
3. To analyze and compare the different Modulation and Demodulation techniques.
4. To explain the key concepts of Analog and Digital Pulse Modulation and Demodulation techniques.
5. To illustrate various source and channel coding techniques.
6. To examine the impact of noise and distortions in communication systems.

Course Outcomes:

After successful completion of the course student will be able to

1. Define the fundamental concepts of Analog and Digital Communication.
2. Explain the various Analog, Digital Modulation and Demodulation Techniques and Multiplexing Techniques.
3. Apply the concepts of information theory of source coding and channel coding techniques.
4. Analyse the various modulation and demodulation techniques.
5. Evaluate the performance of various error control codes.
6. Design an encoder and decoder for error control system with the given specifications.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
0	**Prerequisite**	Need for Modulation, Basics concepts of Amplitude and Frequency Modulation and Demodulation. Sampling Theorem and Pulse Code Modulation. Definition and waveforms of ASK, FSK and PSK.	**01**	**--**
I	**Introduction**	Block diagram of Analog and Digital Communication System, Signal-to-noise ratio, Noise factor, Noise Figure, Noise Temperature. Friis Formula. **Self-Learning Topics:** Types of Noise and Electromagnetic Spectrum.	**02**	**CO1**
II	**Analog Modulation and Demodulation**	**Amplitude Modulation and Demodulation-** Mathematical and Graphical Representation AM wave, Voltage distribution and Power Calculations. Basic Concepts of DSBSC and SSB. AM Diode Detectors - (Simple and Practical). **Self-Learning Topics:** Low level and High-level Transmitter.	**10**	**CO1** **CO2** **CO4**
		Frequency Modulation and Demodulation - Mathematical Representation of FM wave, deviation ratio, bandwidth requirement, Narrowband and Wideband FM, Pre-emphasis and De-emphasis, Noise-triangle, FM Generation (Varactor Diode and Armstrong method) , FM Detectors- Foster-Seeley Detector		
		Radio Receivers – Characteristics of radio receivers, Superheterodyne AM and FM receivers. **Self-learning Topics:** Use of AM and FM in Modern Communication Technology. Challenges faced by radio broadcasting industry.		
III	**Pulse Modulation and Multiplexing**	Sampling Theorem, Nyquist Criteria, Sampling Techniques, Aliasing error and Aperture effect.	**07**	**CO2** **CO4**
		Generation and Detection of PAM, PWM and PPM. PCM Transmitter and Receiver. Concepts of Delta modulation (DM) and Adaptive Delta Modulation (ADM).		
		Need of Multiplexing, Block Diagram explanation of TDM and FDM Systems. **Self-learning Topics:** Applications of Multiplexing		
IV	**Source Coding and Channel Coding**	Basics of Information Theory, Entropy, Shannon Hartley theorem Source Coding Techniques: Huffman coding and	**07**	**CO3** **CO5** **CO6**

	Techniques	Shannon-Fano coding. Channel Coding for Error Detection and Correction - Linear Block Codes and Convolutional Codes. **Self-learning Topics:** Applications of Source Coding and Channel Coding Techniques.		
V	**Baseband Transmission and Reception**	Block diagram of baseband Transmitter-Receiver system, Need of line codes, Properties, Types of line codes - RZ and NRZ Unipolar formats, RZ and NRZ Polar formats, RZ and NRZ Bipolar format (AMI format), Split phase Manchester format and Polar Quaternary formats. Inter Symbol Interference, Inter Channel Interference, Matched Filter. **Self-Learning Topics:** Equalizers	**04**	**CO1** **CO2**
VI	**Bandpass Transmission and Reception**	Generation, Detection, Error probability and Bandwidth of the following modulations: BASK, BFSK, BPSK, QPSK, offset QPSK, M-ary PSK, 16-ary QASK and MSK. **Self-learning Topics**: Applications of all the Modulation Techniques	**08**	**CO2** **CO4**

Text Books:

1. Kennedy and Davis "Electronics Communication System", Tata McGraw Hill
2. Wayne Tomasi, "Electronics Communication Systems" Pearson Education, 5th Edition.
3. Herbert Taub, Donald L Schilling, Goutam Saha, "Principles of Communication Systems", Tata McGraw Hill, 3rd Edition.
4. T. L. Singal, "Analog and Digital Communication," Tata Mc-Graw Hill, New Delhi, First Ed.
5. Sklar B, and Ray P. K., "Digital Communication: Fundamentals and Applications," Pearson, Dorling Kindersley (India), Delhi, Second Edition.

References:

1. Simon Haykin, "Communication System", John Wiley and Sons,4th Ed.
2. B P Lathi, Zhi Ding, Modern Digital and Analog Communication Systems, Oxford University. 4th Ed.
3. Beharouz A. Forouzan, "Data Communication and Networking", fourth edition.

Online References:

Sr. No.	Website Name
1.	Analog communication- https://swayam.gov.in/nd1_noc20_ee69/preview
2.	Principles of Digital Communication- https://nptel.ac.in/courses/108/101/108101113/
3.	Principles of Digital Communication- https://nptel.ac.in/courses/108/102/108102120/

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304114	Microcontroller Laboratory	--	2	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2304114	Microcontroller Laboratory	--	--	--	--	25	25	50

Prerequisite:
Digital System Design

Laboratory Objectives:

1. To evaluate and integrate development tools for effective system design and implementation for microcontroller based system
2 To study the addressing modes of 8051 and write the program to apply the same
3 To interface I/O devices to 8051 and write the program apply the same
4. To develop microcontroller based applications.

Laboratory Outcomes:

After successful completion of the course student will be able to:

1. Students will be able to utilize development tools to effectively design, simulate, and troubleshoot microcontroller-based systems.
2. Write assembly language programs for arithmetic and logical operations,
3. Write assembly language programs for code conversion & data transfer operations.
4. Write assembly language programs for general purpose I/O, Timers & Interrupts.
5. Interface & write programs for Input and Output devices.
6. Develop microcontroller based Applications.

List of Experiments:

Sr No	List of Experiments	Hrs
01	Explore the development tools to be used (Assembler, Linker, Compiler, Simulator, Emulator. IDE: like Keil, Edsim 51,tinkercad and wokwi etc	02
02	WAP in assembly language for 8051 to perform arithmetic operations such as: a) Addition b) Subtraction c) Multiplication d) Division	02
03	WAP in assembly language for 8051 to perform multiple byte decimal addition.	02
04	WAP in assembly language for 8051 to Exchange & moving block of elements. a) Exchange block from internal RAM to internal RAM. b) Moving block from internal RAM to external RAM.	02
05	WAP in assembly language for 8051 to Conversion of codes (Any One): a) Binary to BCD b) Binary to Gray etc	02
06	To write a program to arrange numbers in Ascending order or descending order	02
07	WAP in assembly language for 8051 To generate a) Square wave or b) Triangular wave	02
08	WAP in assembly language for 8051 for blinking LED. a) Using Timer and Interrupt b) Using Delay Subroutine.	02
09	Interfacing and Programming of 7 Segment display to 8051.	02
10	Interfacing and Programming of 16 x 8 LCD Display	02
11	Study of Stepper Motor. a) Continuous Mode. b) Step mode with 180 degree rotation. (Clockwise and Anticlockwise)	02
12	To write an ALP for 8051 Serial Communication to send string of characters on serial port.	02
13	Basic programming using ARM WAP to perform arithmetic operations such as: a) Addition b) Subtraction c) Multiplication d) Division	02
14	Small mini-Project Microcontroller based Applications on simulators like - Traffic Light Controller,Touchless door bell	02

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practical's based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical & Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304115	Analog and Digital Communication Laboratory	--	2	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2304115	Analog and Digital Communication Laboratory	--	--	--	--	25	25	50

Prerequisite:

- Elements of Telecommunication
- Electronic Devices and Linear Circuits
- Digital System Design

Laboratory Objectives:

1. To demonstrate generation and detection of Analog and Digital Modulation techniques.
2. To demonstrate generation and detection of Pulse Modulation Techniques.
3. To learn source coding and error control coding techniques.
4. To compare different line coding methods.
5. To illustrate multiplexing techniques.
6. To use simulation tools for analog and digital communication techniques.

Laboratory Outcomes:

After successful completion of the course student will be able to:

1. Demonstrate various Analog Modulation and Demodulation techniques.
2. Demonstrate Pulse Modulation, Demodulation and Multiplexing techniques.
3. Demonstrate various Digital Modulation and Demodulation Techniques.
4. Evaluate the performance parameters of a communication system.
5. Examine various Line Coding Techniques.
6. Design different source coding and channel coding Techniques.

DETAILED SYLLABUS:

Sr. No.	Module	Detailed Content	Hours	LO Mapping
0	**Prerequisite**	--	--	--
I	**Introduction**	Noise voltage and Noise Power Calculations	02	LO1
II	**Analog Modulation and Demodulation**	DSBFC Modulation and Demodulation, DSBSC, Diode Detector FM Modulation and Demodulation, Pre-emphasis and De-Emphasis	06	LO1 LO4
III	**Pulse Modulation and Multiplexing**	Sampling Theorem Analog Pulse Modulation Techniques -PAM, PWM, PPM. Digital Pulse Modulation Techniques- PCM, DM, ADM Multiplexing Techniques -TDM and FDM	06	LO2
IV	**Source Coding and Channel Coding**	Source Coding Techniques- Shannon Fano Coding and Huffmann Coding Channel Coding Techniques - Linear Block Codes and Convolution Codes	04	LO6
V	**Baseband Transmission and Reception**	Line Codes, Matched Filter	02	LO4 LO5
VI	**Bandpass Transmission and Reception**	Modulation and Demodulation of ASK, FSK, PSK, QPSK Techniques	04	LO3

Text Books:

1. Kennedy and Davis "Electronics Communication System", Tata McGraw Hill
2. Wayne Tomasi, "Electronics Communication Systems" Pearson Education, 5th Edition.
3. Herbert Taub, Donald L Schilling, Goutam Saha, "Principles of Communication Systems", Tata McGraw Hill, 3rd Edition.
4. T. L. Singal, "Analog and Digital Communication," Tata Mc-Graw Hill, New Delhi, First Ed.
5. Beharouz A. Forouzan, "Data Communication and Networking" fourth edition .

References:

1. Sklar B, and Ray P. K., "Digital Communication: Fundamentals and Applications," Pearson, Dorling Kindersley (India), Delhi, Second Edition.
2. Simon Haykin, "Communication System", John Wiley And Sons ,4th Ed.
3. B P Lathi, Zhi Ding, Modern Digital and Analog Communication Systems, Oxford University. 4th Ed.

Online Resources:

Sr. No.	Website Name
1.	Analog communication- https://swayam.gov.in/nd1_noc20_ee69/preview
2.	Principles of Digital Communication- https://nptel.ac.in/courses/108/101/108101113/
3.	Free/Libre and Open Source Software for Education project using cloud. scilab.in

List of Experiments:

Sr No	List of Experiments	Hrs
01	Generation and Detection of Amplitude Modulation	02
02	Generation and Detection of Frequency Modulation	02
03	Generation and Detection of DSBSC Signal.	02
04	Design and implementation of Pre-emphasis and De-emphasis circuit.	02
05	Verification of sampling theorem.	02
06	Generation of PAM Modulation /Demodulation.	02
07	Generation of PWM/PPM Modulation /Demodulation.	02
08	Demonstrate Digital Pulse Code Modulation Technique (PCM)	02
09	Demonstrate Delta Modulation and Adaptive Delta Modulation Techniques (DM, ADM)	02
10	Observation of Time Division multiplexing and De-multiplexing signals.	02
11	Observation of Frequency Division multiplexing and De-multiplexing signals.	02
12	Simulate Shannon-Fano Code and calculate code efficiency	02
13	Simulate Huffman code and calculate code efficiency.	02
14	Simulate Linear block code and find error detection capability.	02
15	Simulate Convolutional code as per given specification.	02
16	Observe and compare of various Line Codes.	02
17	Matched filter impulse response for a given input.	02
18	Modulation/Demodulation of Binary ASK.	02
19	Modulation/Demodulation of Binary FSK.	02
20	Modulation/Demodulation of Binary PSK.	02
21	Modulation/Demodulation of QPSK.	02
22	Generation (and detection) of MSK	02

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals' based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical& Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

Vertical – 4

Vocational and Skill Enhancement Course (VSEC)

Detail Syllabus

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303611	C++ and Java Programming	-	4	-	-	2	-	2

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2303611	C++ and Java Programming	--	--	--	--	50	25	75

Prerequisite:
C-Programming

Laboratory Objectives:

1. To introduce Object-Oriented Programming (OOP) principles and understand the necessity of OOP in software development using C++ and Java.
2. To develop problem-solving skills using control structures, functions, arrays, strings, and object-oriented programming concepts in C++.
3. To implement concepts like inheritance, polymorphism, operator overloading, file handling, and memory management in C++ and Java for better software design.
4. To explore Java programming paradigms and understand its differences from C++, focusing on Java classes, methods, inheritance, and polymorphism.
5. To familiarize students with advanced Java concepts like exception handling, multithreading, GUI programming, and applet development.

Laboratory Outcomes:

After successful completion of the course student will be able to

1. Demonstrate basic programming constructs such as data types, control statements, arrays, and strings in C++ and Java.
2. Apply object-oriented programming concepts such as classes, objects, encapsulation, inheritance, and polymorphism in software design.
3. Implement operator overloading, file handling, constructors, and destructors in C++ for efficient memory and resource management.
4. Develop Java applications using classes, objects, interfaces, exception handling, multithreading, and GUI programming.
5. Design and implement applet-based applications and GUI-based Java programs using AWT and event handling techniques.

DETAILED SYLLABUS

Sr. No.	Module	Detailed Content	Hours	LO Mapping
I		**Overview of CPP**	4	LO1
	1.1.	Need of Object-Oriented Programming (OOP), Object Oriented Programming Paradigm, Basic Concepts of Object-Oriented Programming, Benefits of OOP and C++ as object-oriented programming language.		
	1.2	C++ programming Basics, Data Types, Structures, Enumerations, control structures, Class, Object, class and data abstraction, class scope and accessing class members, separating interface from implementation, controlling access to members.		

		Prerequisites:
		Students should have knowledge of
		
		1. **Basic Computer Knowledge**
		   o Understanding how software works, how to run programs, compile code.
		2. **Introduction to Programming**
		   o Basic syntax and structure of C programming language
		   o Familiarity with writing simple programs (input/output, variables, loops).
		3. **Fundamentals of C Programming (Optional but helpful)**
		   o Data types, variables, control structures (if, for, while).
		   o Functions and arrays.
		
		Self-Learning Topics
		
		Branching - If statement, If-else Statement, Decision.
		Looping – while, do-while, for loop
		
		Nested control structure- Switch statement, Continue statement, Break statement.		

II		**C++ Function, Array and Strings**	6	LO1
	2.1	Returning values from functions. Reference arguments. Overloaded function. Inline		

		function. Default arguments. Return by reference		
	2.2	**Array and Strings** Concepts, Declaration, Definition, Accessing array element, One-dimensional and Multidimensional array. String, String Functions, standard C++ String class		
III		**Object-Oriented Programming using C++ and Files**	8	LO2, LO3
	3.1	**Operator Overloading-** concept of overloading, operator overloading, Overloading Unary Operators, Overloading Binary Operators, Data Conversion, Type casting (implicit and explicit), Pitfalls of Operator Overloading and Conversion, Keywords explicit and mutable. **Function-** Function prototype, accessing function and utility function, Constructors and destructors, Copy Constructor, Objects and Memory requirements, Static Class members, data abstraction and information hiding, inline function. **Constructor-** Definition, Types of Constructors, Constructor Overloading, Destructor.		
	3.2	**Inheritance-** Introduction, Types of Inheritance, Inheritance, Public and Private Inheritance, Multiple Inheritance, Ambiguity in Multiple Inheritance, Visibility Modes Public, Private, Protected and Friend, Aggregation, Classes Within Classes. Deriving a class from Base Class, Constructor and destructor in Derived Class, Overriding Member Functions, Class Hierarchies, **Polymorphism-** concept, relationship among objects in inheritance hierarchy, Runtime & Compile Time Polymorphism, abstract classes, Virtual Base Class.		
	3.3.	**File -**Stream in CPP, Class for File Stream Operation, Modes of Files, Opening and Closing File, Read, Write and append in File**.**		
IV		**Introduction to Java**	2	LO4
	4.1	Programming paradigms- Introduction to programming paradigms, Introduction to four main Programming paradigms like procedural, object oriented, functional, and logic & rule		

		based. Difference between C++ and Java		
	4.2	Java History, Java Features, Java Virtual Machine, Data Types and Size (Signed vs. Unsigned, User Defined vs. Primitive Data Types, Explicit Pointer type), Programming Language JDK Environment and Tools.		
V		**Inheritance, Polymorphism, Encapsulation using Java**	8	LO4
	5.1	**Classes and Methods**: class fundamentals, declaring objects, assigning object reference variables, adding methods to a class, returning a value, constructors, this keyword, garbage collection, finalize () method, overloading methods, argument passing, object as parameter, returning objects, access control, static, final, nested and inner classes, command line arguments, variable-length Arguments.		
	5.2.	Array, String, String buffer and Vectors		
	5.3	**Inheritances:** Member access and inheritance, super class references, Using super, multilevel hierarchy, constructor call sequence, method overriding, dynamic method dispatch, abstract classes, Object class. **Packages and Interfaces:** defining a package, finding packages and CLASSPATH, access protection, importing packages, interfaces (defining, implementation, nesting, applying), variables in interfaces, extending interfaces, instance of operator.		
VI		**Exception Handling and Applets in Java**	8	LO4, LO5
	6.1	**Exception Handling:** fundamental, exception types, uncaught exceptions, try, catch, throw, throws, finally, multiple catch clauses, nested try statements, built-in exceptions, custom exceptions (creating your own exception sub classes). **Multithreading Threading:** Introduction, thread life cycle, Thread States: new, runnable, Running, Blocked and terminated, Thread naming, thread join method, Daemon thread		
	6.2.	**Applet:** Applet Fundamental, Applet Architecture, Applet Life Cycle, Applet Skeleton, Requesting Repainting, status window, HTML Applet tag, passing parameters to Applets, Applet and Application Program		

	6.3	**GUI :** Introduction to AWT programming Layout and Component Managers Event handling.		
VII		**Mini Project**	3	LO2, LO3, LO4, LO5

Textbooks:

1. **E. Balagurusamy**, *"Object Oriented Programming with C++"*, Tata McGraw-Hill Education, **Sixth Edition**, 2013.
2. **D. Ravichandran**, *"Programming with C++"*, Tata McGraw-Hill Publishing Company Limited, **Second Edition**, 2006.
3. **Yashavant Kanetkar**, *"Let Us C++"*, BPB Publications, **Revised Edition**, 2020.
4. **E. Balagurusamy**, *"Programming with Java: A Primer"*, Tata McGraw-Hill Education, **Fifth Edition**, 2014.
5. **Cay S. Horstmann**, *"Core Java Volume I – Fundamentals"*, Pearson Education, **Eleventh Edition**, 2018.
6. **Kathy Sierra, Bert Bates**, *"Head First Java"*, O'Reilly Media, **Second Edition**, 2005.

References:

1. **Herbert Schildt**, *"Java: The Complete Reference"*, Tata McGraw-Hill Publishing Company Limited, **Ninth Edition**, 2014.
2. **Bjarne Stroustrup**, *"The C++ Programming Language"*, Addison-Wesley, **Fourth Edition**, 2013.
3. **Stanley B. Lippman, Josée Lajoie, Barbara E. Moo**, *"C++ Primer"*, Addison-Wesley, **Fifth Edition**, 2012.
4. **Scott Meyers**, *"Effective C++: 55 Specific Ways to Improve Your Programs and Designs"*, Addison-Wesley, **Third Edition**, 2005.
5. **Joshua Bloch**, *"Effective Java"*, Addison-Wesley, **Third Edition**, 2018.
6. **Sachin Malhotra, Saurabh Chaudhary** "Programming in Java", Oxford University Press, 2010.
7. **Grady Booch, James Rumbaugh, Ivar Jacobson,** "The Unified Modeling Languageser Guide", Pearson Education.

Software Tools for C++ and Java Programming

SrNo	Software Tools for C++ and Java Programming
1	**Visual Studio Code (VS Code)** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** C++, Java (via extensions) ○ **Features:** Lightweight, IntelliSense, debugging, Git integration, customizable with extensions.
2	**Eclipse IDE** ○ **Platform**: Windows, macOS, Linux ○ **Languages:** Primarily Java, with C/C++ support (via CDT plugin)

	○ **Features:** Project management, code analysis, UI builder for Java apps.
3	**NetBeans IDE** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** Java, C, C++ ○ **Features:** Excellent Java support, GUI builder, simple setup for projects
4	**IntelliJ IDEA** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** Java, Kotlin, Scala, C++ (limited) ○ **Features:** Powerful Java IDE with smart code completion and refactoring tools.
5	**Code::Blocks** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** C, C++ ○ **Features:** Lightweight C++ IDE, great for beginners, plugin support.
6	**Dev C++** ○ **Platform:** Windows ○ **Languages:** C, C++ ○ **Features:** Simple IDE with built-in compiler, perfect for learning C++.
7	**BlueJ** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** Java ○ Features: Educational IDE designed for teaching OOP concepts.
8	**JGrasp** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** Java, C, C++ ○ **Features:** Lightweight IDE with visualization tools for Java structures
9	**Xcode** ○ **Platform:** macOS ○ **Languages:** C, C++, Objective-C, Swift, Java (limited) ○ **Features:** Excellent for Apple ecosystem development; supports C++ well
10	**CLion (by JetBrains)** • **Platform:** Windows, macOS, Linux • **Languages:** C, C++ • **Features:** Smart C++ development with debugging, refactoring, CMake support.

Skill-Enhancement Activities for C++ and Java Programming
1. Use of Tools for programming and project development
Objective

- The objective is to enhance students' programming skills using industry-standard tools like Eclipse, NetBeans, IntelliJ IDEA, Code:Blocks, Dev C++, and BlueJ. It aims to familiarize them with project development, debugging, and external library integration. Training on tools like Maven prepares students for real-world software practices through hands-on, project-based learning.

Purpose

- To enhance programming skills and industry readiness, students should be trained to code using a variety of development environments and tools. **Eclipse**, **NetBeans**, and **IntelliJ IDEA** are widely accepted IDEs in the software industry, especially for Java development. These platforms support integration of **external libraries and frameworks**, project structuring, and version control. Students should also be introduced to **Maven** as a powerful build tool for managing project dependencies and builds efficiently.
- For C++ programming, students should gain hands-on experience using **Code::Blocks**, **Dev C++**, and **BlueJ**, which provide user-friendly interfaces and help build strong foundational knowledge in object-oriented programming and file handling. These tools also help in understanding how code compiles, links, and executes, making them ideal for beginners and intermediate learners. Emphasis should be placed on solving real-world problems, debugging, and project-based learning using these tools.

2. Real-World Mini Problem Statements via Industry Simulation

- **Objective:**
  Bridge the gap between academics and industry by simulating real-world development tasks aligned with **company-level expectations**.
- **Implementation Strategy:**
  - Identify **10–15 mini problem statements** from domains such as inventory management, attendance tracking, student feedback systems.
  - Organize students into **groups of 3–4** and assign one problem per group.
  - Allocate **8–12 hours (across the last two lab sessions)** for brainstorming, coding, testing, and demo.
  - Encourage the use of Eclipse, Maven, Git, and external APIs or libraries relevant to the solution.
- **Examples of Problem Statements:**
  - Build a **Leave Management System** using Java classes and file I/O.
  - Develop a **Library Management GUI** with Swing and JDBC.
  - Implement a **Patient Record Tracker** with JSON serialization and external libraries.
  - Create a **Book Recommendation Console App** using OOP and collections.
- **Expected Outcome:**
  - Students experience the **complete software development cycle**—from understanding a requirement to deploying a working solution.
  - Promotes **teamwork, time management, and tool proficiency**, building job-ready skills for campus placements.

Online Resources:

Sr. No.	Website Name (CPP Programming)
1.	**cplusplus.com -** Comprehensive reference for C++ syntax, standard libraries, and STL. Great for quick lookups http://www.cplusplus.com
2.	**GeeksforGeeks(C++) -** Rich in tutorials, quizzes, practice problems, and interview questions. Ideal for beginners to advanced learners https://www.geeksforgeeks.org/c-plus-plus
3.	**Codecademy – Learn C++ -** Interactive platform with real-time coding in browser. Gamified progress and projects included. https://www.codecademy.com/learn/learn-c-plus-plus
4.	**Coursera – C++ For C Programmers -** University-style course, great for structured learners. Includes peer-reviewed assignments and quizzes https://www.coursera.org/learn/c-plus-plus-a
.5.	**Udemy – Beginning C++ Programming - From Beginner to Beyond -** Covers both fundamentals and advanced concepts, Great for beginners and intermediate learners. https://www.udemy.com/course/beginning-c-plus-plus-programming/
6.	**NPTEL – Programming in C++-** Offered by IIT Kharagpur,Free to access, Comprehensive and academic-focused, Includes assignments, weekly quizzes, and final certification exam. https://nptel.ac.in/courses/106/105/106105151/
	Website Name (Java Programming)
1.	**JavaTpoint -** Easy-to-understand explanations, tons of examples, and hands-on exercises. Covers basics to frameworks. https://www.javatpoint.com/java-tutorial
2.	**Oracle Java Documentation -** The official and most authoritative resource on Java. Best for understanding the language in depth. https://docs.oracle.com/javase/tutorial/
3.	**W3Schools Java Tutorial-** Beginner-friendly and offers a try-it-yourself feature to code online. https://www.w3schools.com/java/
4.	**Coursera – Java Programming and Software Engineering Fundamentals -** Offered by Duke University,Beginner-friendly, includes real-world applications like web scraping and data analysis. https://www.coursera.org/specializations/java-programming
5.	**Udemy – Java Programming Masterclass updated to Java –** Over 80 hours of content, covering Java from basics to advanced, Taught by experienced software engineer https://www.udemy.com/course/java-the-complete-java-developer-course/
6.	**NPTEL – Object-Oriented Programming using Java** https://nptel.ac.in/courses/106/105/106105191/ - Offered by IIT Kharagpur, In-depth academic course with real-life applications and Java-specific concepts,Includes weekly assignments, lectures,

List of Experiments.

Problems Statement can be divided in three parts

1. Some Few can be solved in class during Lecture so involvement of Students will increase
2. Some Few can be given as Assignment so that repeated process will retain the syntax and logic
3. Some can be asked to solve while doing the practical session

Note : Out of the given list topic wise 25% can be solved in class, 50% can be taken in Lab and remaining 25% can be given as assignment

Unit	Topic	LO
1.0	1. Develop a C++ program to demonstrate the concept of class and object by creating a simple "Bank Account" management system. 2. Create a program that uses structures and enumerations to store and display student information. 3. Implement a C++ application that showcases the use of encapsulation by creating a class for employee data management. 4. Write a program to demonstrate polymorphism using function overloading and operator overloading. 5. Design a C++ program to calculate the area of different shapes (Circle, Rectangle, Triangle) using function overloading. 6. Create a C++ program to simulate a simple library management system using classes and objects. 7. Implement a program using default arguments and inline functions to calculate the volume of different geometric shapes. 8. Develop a C++ application to demonstrate the use of reference arguments in a function for swapping two numbers. 9. Write a C++ program that uses the concept of separating interface from implementation by creating a class for basic arithmetic operations. 10. Build a simple program to demonstrate returning values by reference in C++ using a class to manage complex numbers. **Objective :** These problem statements and objectives cover various concepts from OOP, including encapsulation, polymorphism, data abstraction, and other C++ programming concepts.	LO1
2.0	**1.** Write a C++ program to find the largest of three numbers	LO1

	using an if-else statement. 2. Develop a program to check whether a given number is prime using a while loop. 3. Create a C++ program that simulates a simple menu-driven calculator using a switch statement. 4. Implement a program to print the Fibonacci series using a do-while loop. 5. Write a C++ program to display numbers from 1 to 100, but skip multiples of 5 using the continue statement. **6.** Develop a C++ program to input and display elements of a one-dimensional array. **7.** Write a C++ program to perform matrix addition using a two-dimensional array. 8. Create a program to check if a given string is a palindrome using standard C++ string functions. 9. Implement a C++ program to count the number of vowels and consonants in a given string. 10. Design a simple student management system using structures to store student details and display information. **Objective :** To develop problem-solving skills and logical thinking by applying C++ control statements, arrays, and string manipulation techniques to create efficient and optimized programs.	
3.0	1. Create a C++ program to demonstrate operator overloading for adding two complex numbers using the + operator. 2. Implement a program to overload the ++ operator for incrementing the values of a custom class object. 3. Develop a C++ application that demonstrates explicit type casting using constructors. 4. Write a C++ program that demonstrates the use of mutable keyword for modifying a constant object. 5. Design a class with a copy constructor to create a duplicate of an existing object.. 6. Create a program using single inheritance to derive a class Student from a base class Person. 7. Write a C++ program that demonstrates multiple inheritance by creating a class that inherits from two base classes. 8. Develop a program to illustrate the concept of virtual base class to solve the diamond problem. Implement a C++ program that demonstrates runtime polymorphism using virtual functions.. 11. Create a base class Shape with a virtual function draw()	• Practical 1–4 → LO2 • Practical 5 → LO3 • Practical 6–10 → LO2 • Practical 11–15 → LO3

	and derive classes like Circle and Rectangle to override the function. Write a C++ program to read and write data to a file using file streams.. 12. Implement a simple file-handling application to append data to an existing file. 13. Create a student management system where student details are stored and retrieved from a file.. 14. Develop a C++ program to count the number of words in a given text file. 15. Implement a C++ application to copy content from one file to another. **Objective :** To understand and apply object-oriented programming concepts such as operator overloading, inheritance, polymorphism, and file handling in C++ to develop efficient and maintainable applications.	
4.0	1. Develop a Java program to demonstrate the procedural programming approach using simple arithmetic operations. 2. Implement a class in Java to demonstrate object-oriented programming by creating a Student class with attributes and methods. 3. Create a Java program to illustrate functional programming using lambda expressions and streams. 4. Write a simple rule-based expert system in Java using conditional statements to suggest clothing based on weather input. 5. Compare and contrast C++ and Java by implementing a simple calculator program in both languages. 6. Develop a Java program to demonstrate different data types and their sizes using simple variables. 7. Write a Java program to convert an integer from signed to unsigned using bitwise operations. 8. Create a simple Java application using JDK and explain the development process including compiling and running using javac and java commands. 9. Develop a Java program to illustrate the concept of explicit pointers using references. 10. Implement a Java program that simulates the working of a Java Virtual Machine (JVM) by creating and running multiple threads. **Objective :** To understand the fundamentals of Java programming, including programming paradigms, Java history,	LO4

	features, data types, JVM functionality, and the use of JDK tools for developing efficient applications.	
5.0	1. Write a Java program to demonstrate class fundamentals by creating a class Employee with attributes and methods. 2. Develop a Java program to demonstrate the use of this keyword to differentiate between instance variables and parameters. 3. Create a Java program that uses static methods and variables to calculate the area of a rectangle. 4. Implement a Java program to demonstrate method overloading using different parameter types. 5. Write a program to demonstrate the use of garbage collection and the finalize() method. 6. Develop a Java program to create and manipulate arrays by finding the largest element. 7. Write a Java program to perform basic string operations using the String class. Create a Java program to demonstrate the use of StringBuffer to reverse a given string. 8. Implement a program to simulate a simple task management application using Vectors. 9. Write a Java program to concatenate two strings using StringBuilder.. 11. Develop a Java program to demonstrate single inheritance using a Person class and a Student class. 12. Create a program to illustrate the concept of method overriding using a parent and child class. 13. Write a Java program using multiple inheritance through interfaces to implement a simple vehicle management system. 14. Develop a Java program to create a package named MyPackage and import it in another program. 15. Implement a Java program using the instanceof operator to check object types in an inheritance hierarchy. **Objective :** To apply the principles of object-oriented programming in Java by implementing concepts of inheritance, polymorphism, encapsulation, class management, interfaces, and file handling to build robust and scalable applications.	LO4
6.0	1. Write a Java program to demonstrate exception handling using try, catch, and finally blocks. 2. Develop a Java program to create a custom exception class and handle it using throw and throws keywords..	• Practical 1–5 → LO5 (Exception Handling) • Practical 6–10 → LO5 (Multithreading) • Practical 11–15 → LO5

	3. Create a Java application that handles multiple exceptions using multiple catch blocks. 4. Write a Java program to demonstrate nested try statements. 5. Implement a program to simulate uncaught exceptions and analyze its impact.. 6. Write a Java program to demonstrate thread creation using Runnable and Thread classes. 7. Develop a Java program to implement thread synchronization using synchronized methods. 8. Create a Java application to demonstrate inter-thread communication using wait(), notify(), and notifyAll(). 9. Write a Java program to demonstrate thread priorities and daemon threads.. 10. Implement a program to show the lifecycle of a thread using different states. 11. Create a simple Java applet that displays a welcome message using the paint() method 12. Develop a Java applet to handle mouse events and display coordinates. 13. Write a Java program to create a graphical user interface using AWT components. 14. Implement a Java applet that takes parameters from HTML using the Applet tag. 15. Create a simple drawing application using AWT that allows the user to draw shapes. **Objective :** To understand and implement advanced Java concepts such as exception handling, multithreading, applet programming, and graphical user interface (GUI) development using AWT, enhancing application reliability and user interactivity.	(Applets and GUI)

List of Mini project

Sr No	List of Mini Projects (CPP)	LO
01	**Student Report Card Management System** • **Concepts Used:** File Handling, Classes, Structures, Constructors • **Description:** Add, delete, modify, and view student academic records. Data is stored in files.	LO2, LO3
02	**Library Management System** • **Concepts Used:** File Handling, OOP, Arrays, Structures	LO2, LO3

	• **Description:** Manage books in a library (add/remove/search). Track issued and returned books.	
03	**Bank Management System** • **Concepts Used:** File Handling, Classes & Objects, Data Abstraction • **Description:** Simulate banking operations like opening an account, deposits, withdrawals, and account balance inquiry.	LO2, LO3
04	**Inventory Management System** • **Concepts Used:** File I/O, Object-Oriented Design, Polymorphism • **Description:** Track product stock, purchase and sales, and maintain transaction logs.	LO2, LO3
05	**Hotel Reservation System** • **Concepts Used:** File Handling, Class Inheritance, Constructors • **Description:** Manage room bookings, cancellations, and availability checks with cost estimation.	LO2, LO3
06	**Employee Payroll Management System** • **Concepts Used:** File Handling, Inheritance, Virtual Functions • **Description:** Calculate salary, taxes, bonuses, and store payroll details of employees.	LO2, LO3
07	**Clinic Patient Record System** • **Concepts Used:** File Streams, Class Hierarchy, Object Persistence • **Description:** Maintain patient records, appointment scheduling, and doctor allocation.	LO2, LO3
08	**Online Quiz Management System** • **Concepts Used:** File Handling, Menus, Functions, Classes • **Description:** Conduct multiple quizzes, store scores, and allow user login with progress tracking.	LO2, LO3
09	**Simple Railway Ticket Booking System** • **Concepts Used:** File I/O, Structures, Functions • **Description:** Simulate booking, cancellation, and displaying train details using file storage.	LO2, LO3
10	**Book Store Management System** • **Concepts Used:** File Handling, OOP Principles, Sorting and Searching • **Description:** Add/update/search/delete book details, generate billing and inventory reports.	LO2, LO3
	List of Mini Projects (JAVA)	
01	**Expense Tracker** **Problem Statement:** Build a desktop application where users can log daily expenses, categorize them (food, travel, etc.), and view monthly summaries. Data should be stored and retrieved from local files GUI Interface.	LO4, LO5
02	**Recipe Book Manager** **Problem Statement:** Create a GUI app where users can store, search, and edit their favorite recipes. Each recipe should be saved as an individual file or organized into categories using folders.	LO4, LO5

03	**Bug Tracker Tool** **Problem Statement:** Build a mini bug-tracking system where users can log, update, and mark bugs as resolved. Save bug reports to a file and display them in a sortable GUI table.	LO4, LO5
04	**Fitness and Workout Logger** **Problem Statement:** Allow users to create workout plans, log completed exercises, and track progress through charts or stats (optional). File handling should maintain history and progress logs and GUI.	LO4, LO5
05	**Event Scheduler and Reminder** **Problem Statement:** Design a scheduling system to plan events and get pop-up reminders. Save and load event lists using file handling, optionally integrating a basic calendar UI.	LO4, LO5
06	**Simple Customer Feedback Collector** **Problem Statement:** Build a feedback form where users submit their opinions on products or services. Responses should be saved in structured format (CSV/JSON) for analysis later and GUI.	LO4, LO5
07	**Contact Book with Export Feature** **Problem Statement:** Implement a GUI-based contact book that allows adding/editing/deleting contacts and exporting data to a .csv file for external use.	LO4, LO5
08	**Parking Lot Management System** **Problem Statement:** Simulate a parking lot with slots for vehicles. Allow entry/exit registration, generate parking slips, and save logs of all vehicles using file handling and GUI.	LO4, LO5
09	**Daily Mood Tracker** **Problem Statement:** Let users record their mood daily with a short note. Store entries in files and allow users to browse past entries and see frequency stats of mood types using GUI.	LO4, LO5
10	**Digital Notes Organizer** **Problem Statement:** Create an app to manage and organize personal notes. Provide options to add, delete, and edit notes, with autosave features and organized storage using file structures and GUI.	LO4, LO5

Online Repository

SrNo	Respository

1	**GitHub**
	• **Link:** https://github.com
	• **Reason to use It:**
	  ○ Largest open-source platform with thousands of C++ and Java projects.
	  ○ Great for exploring real-world applications, contributing to open source, and version control.
	  ○ Students can fork repositories, collaborate on code, and showcase projects for placements.

2	**GeeksforGeeks – Practice and Code Repository**
	• **Link:** https://practice.geeksforgeeks.org/
	• **Reason to use It:**
	  ○ Rich in C++ and Java examples, coding problems, and data structures.
	  ○ Covers programming concepts with working source code.
	  ○ Regularly updated with interview questions and competitive coding challenges.

3	**GitLab**
	• **Link:** https://gitlab.com
	• **Reason to use It:**
	  ○ Similar to GitHub but with more private repositories (ideal for academic use).
	  ○ Good for hosting collaborative coding projects and using CI/CD pipelines.
	  ○ Supports C++ and Java with various development tools.

4	**SourceForge**
	• **Link:** https://sourceforge.net
	• **Reason to use It:**
	  ○ Repository of open-source software including tools, utilities, and programming frameworks.
	  ○ Many Java GUI-based and C++ utility projects are available for download and modification.
	  ○ Ideal for exploring legacy and niche programming projects.

5	**CodeChef GitHub Repository (and Platform)**
	• **Link:**
	  ○ CodeChef: https://www.codechef.com/
	  ○ GitHub Repo: https://github.com/codechef
	• **Reason to use It:**
	  ○ Offers a massive problem-solving community for C++ and Java.
	  ○ Students can practice competitive programming and refer to community-driven solutions.
	  ○ Excellent for mastering logic and problem-solving patterns.

Term Work:

- At least **12** experiments (**06 experiments** each on **C++** and **JAVA**) covering entire syllabus should be set to have well predefined inference and conclusion. Teacher should refer the suggested experiments and can design additional experiment to maintain better understanding and quality.
- The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative.
- Term work assessment must be based on the overall performance of the student with every Experiments are graded from time to time.
- The grades will be converted to marks as per "**Attendance+performance+submission+Viva/MCQ Test**" and should be added and averaged. Based on above scheme grading and term work assessment should be done.

- The practical and oral examination will be based on entire syllabus. Students are encouraged to share their experiments codes on online repository. Practical exam should cover all **12** experiments for examination.
- Mini project either in CPP or java from the topic given or any other topic of same level which should include construct of CPP/Java, File handling and GUI is mandatory.

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals' based on the above list and Mini project. Also, Term work Journal must include at least 4 (2 CPP+ 2Java) assignments.

Term Work Marks: 50 Marks (Total marks) = 10 (Attendance) + 10 (Performance in Lab) + 10 (Timely Submission) + 20 (Viva or MCQ Test). MCQ test can be conducted using online system for which10-15 min can be allocated in every practical.

Practical& Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

AC –
Item No. –

As Per NEP 2020

University of Mumbai

Syllabus for HSSM Vertical 5		
Faculty of Engineering		
Board of Studies in Under Engineering		
Second Year Programme in HSSM– Common to All Branches		
Semester	**III & IV**	
Title of Paper (Lab)		**Credits**
I) Entrepreneurship Development	**III**	**2**
II) Environmental Science	**III**	**2**
III) Business Model Development	**IV**	**2**
IV) Design Thinking	**IV**	**2**
Total Credits		**8**
From the Academic Year		**2025-26**

Sem. - III

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2993511	Entrepreneurship Development	--	2*+2	-	-	2*+2	-	2

Course Code	Course Name	Examination Scheme							
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		IAT-I	IAT-II	IAT-I + IAT-II				
2993511	Entrepreneurship Development	--	--	--	--	50	--	50

Note: * Two hours of practical class to be conducted for full class as demo/discussion/theory.

Lab Objectives:

1. To introduce students to entrepreneurship concepts and startup development.
2. To develop business idea generation, validation, and business model preparation.
3. To provide hands-on experience in market research, financial planning, and business pitching.
4. To enhance problem-solving and decision-making skills in entrepreneurial ventures.
5. To familiarize students with government schemes and support systems for entrepreneurs.
6. To develop communication and presentation skills required for business pitching.

Lab Outcomes:

Upon successful completion of this course, students will be able to:

1. Understand the fundamental concepts of entrepreneurship and business models.
2. Conduct market research and develop business plans.
3. Utilize financial planning and cost analysis for startups.
4. Apply entrepreneurial skills to identify and solve business challenges.
5. Develop prototypes using open-source software for business operations.
6. Pitch business ideas effectively with structured presentations.

DETAILED SYLLABUS

Sr. No.	Module	Detailed Content	Hours	LO Mapping
0	**Prerequisite**	Fundamentals of communication and leadership skills.	01	--
I	**Introduction to Entrepreneurship**	Definition, Characteristics, and Types of Entrepreneurs. Entrepreneurial Motivation and Traits. Start-up Ecosystem in India. Challenges in Entrepreneurship	**02**	**LO1**
II	**Business Idea Generation &**	Ideation Techniques: Design Thinking, Brainstorming, Mind	**04**	**LO2**

	Validation	Mapping. Business Model Canvas (BMC). Market Research & Customer Validation. Minimum Viable Product (MVP) Concept.		
III	**Business Planning & Strategy**	Writing a Business Plan. SWOT Analysis and Competitive Analysis. Financial Planning and Budgeting. Risk Assessment and Management	**04**	**LO3**
IV	**Funding and Legal Framework**	Sources of Funding: Bootstrapping, Angel Investors, Venture Capital Government Schemes & Start-up India Initiatives. Business Registration & Legal Formalities. Intellectual Property Rights (IPR) & Patents	**05**	**LO4**
V	**Marketing & Digital Presence**	Branding and Digital Marketing. Social Media Marketing & SEO. Customer Relationship Management (CRM). E-commerce & Online Business Models	**05**	**LO5**
VI	**Business Pitching & Prototype Development**	Pitch Deck Preparation & Presentation Techniques. Prototyping with Open-source Tools. Elevator Pitch & Investor Pitch. Case Studies of Successful Start-ups	**05**	**LO6**

Text Books:

1. "Entrepreneurship Development and Small Business Enterprises" – Poornima M. Charantimath, Pearson, 3rd Edition, 2021.
2. "Innovation and Entrepreneurship" – Peter F. Drucker, Harper Business, Reprint Edition, 2019.
3. "Startup and Entrepreneurship: A Practical Guide" – Rajeev Roy, Oxford University Press, 2022.
4. "Essentials of Entrepreneurship and Small Business Management" – Norman Scarborough, Pearson, 9th Edition, 2021.
5. "The Lean Startup" – Eric Ries, Crown Publishing, 2018.

References:

1. "Disciplined Entrepreneurship: 24 Steps to a Successful Startup" – Bill Aulet, MIT Press, 2017.
2. "Zero to One: Notes on Startups, or How to Build the Future" – Peter Thiel, 2014.
3. "The $100 Startup" – Chris Guillebeau, Crown Business, 2019.
4. "Business Model Generation" – Alexander Osterwalder & Yves Pigneur, Wiley, 2020.
5. "Blue Ocean Strategy" – W. Chan Kim & Renée Mauborgne, Harvard Business Review Press, 2019.

Online Resources:

Website Name
1. Startup India Portal – https://www.startupindia.gov.in 2. MIT OpenCourseWare – Entrepreneurship – https://ocw.mit.edu/courses/sloan-school-of-management/ 3. Coursera – Entrepreneurship Specialization – https://www.coursera.org/specializations/entrepreneurship

4. Harvard Business Review – Entrepreneurship Articles – https://hbr.org/topic/entrepreneurship 5. Udemy – Startup & Business Courses – https://www.udemy.com/courses/business/entrepreneurship/

List of Experiments.

Sr No	List of Experiments	Hrs
01	Business Idea Generation using Mind Mapping.	02
02	Conducting Market Research & Customer Validation.	02
03	Preparing a Business Model Canvas for a Startup Idea.	02
04	Developing a Financial Plan & Break-even Analysis.	02
05	Creating a Website using WordPress/Wix.	02
06	Social Media Marketing Campaign using Open-source Tools.	02
07	Digital Prototyping using Figma/Inkscape.	02
08	Business Pitch Deck Preparation & Presentation.	02
09	Exploring Government Schemes for Startups.	02
10	Legal Compliance & IPR Basics (Case Study).	02

Sr No	List of Assignments / Tutorials	Hrs
01	a. Write a report on any successful entrepreneur and their startup journey. b. Conduct SWOT analysis for a real-life startup.	02
02	Develop a business idea and create a one-page business plan.	02
03	Conduct market research using surveys & present findings.	02
04	Design a simple logo and branding strategy for a startup.	02
05	Create a financial model and cost estimation for a startup.	02
06	Make a case study report on startup failure analysis.	02

List of Open-Source Software
1. Canva – Designing pitch decks, social media posts, and branding materials. 2. Trello / Asana – Project management for startups. 3. GIMP / Inkscape – Graphic design and logo creation. 4. WordPress / Wix – Website development for startups. 5. OpenCart / PrestaShop – E-commerce website setup. 6. Figma – UI/UX design and prototyping. 7. LibreOffice Calc – Financial planning and budgeting. 8. Google Suite (Docs, Sheets, Slides) – Documentation and presentations. 9. Python (Pandas, Flask, Django) – Data analytics and web application development. 10. MailChimp – Email marketing and customer engagement.

Assessment :

Term Work: Term Work shall consist of at least 08 to 10 practicals' based on the above list. Also, Term work Journal must include at least 6 assignments.

Term Work Marks: 50 Marks (Total marks) = 20 Marks (Experiment) + 15 Marks (Assignments) + 5 Marks (Attendance)+ 10 Marks (Report)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2993512	Environmental Science	--	2*+2	-	--	2*+2	-	2

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		IAT-I	IAT-II	IAT-I+IAT-II					
2993512	Environmental Science	--	--	--	--	--	50	--	50

Note: * Two hours of practical class to be conducted for full class as demo/discussion/theory.

Rationale:

Most of the engineering branches are offspring of applied sciences, and their practices have a significant impact on the environment. Understanding environmental studies is essential for engineers to develop sustainable solutions, minimize ecological footprints, and promote responsible resource management. This course equips students with the knowledge of ecosystems, biodiversity, pollution control, and environmental laws, enabling them to integrate sustainability into engineering practices.

Lab Objectives:

1. To understand the scope, importance, and role of environmental studies in public awareness and health.
2. To study different natural resources, their issues, and sustainable conservation.
3. To understand ecosystem types, structures, and functions.
4. To explore biodiversity, its importance, threats, and conservation.
5. To learn about pollution types, causes, effects, and control measures.
6. To understand environmental challenges, sustainability, and ethics.

Lab Outcomes:

1. Explain the significance of environmental studies and the role of IT in environment and health.
2. Describe resource types, associated problems, and conservation methods.
3. Classify ecosystems and explain their role in ecological balance
4. Analyze biodiversity levels and conservation strategies, especially in India.
5. Explain pollution impacts and suggest preventive measures.
6. Discuss environmental issues and propose sustainable solutions.

DETAILED SYLLABUS:

Unit Name	Topic Name	Topic Description	Hours	LO Mapping

I	**The Multidisciplinary Nature of Environmental Studies**	Definition, scope and importance. Need for public awareness, Role of information technology in environment and human health. Human population and the environment: Population growth, variation among nations. Population Explosion- family welfare program. Environment and human health Women and child welfare	**03**	LO1
II	**Natural Resources**	Renewable and non-renewable resources. Natural resources & associated problems: a) Forest resources: b) Water resources: Natural resources & associated problems c) Mineral resources: d) Food resources: e) Energy resources: Role of an individual in conservation of natural resources: f) Equitable use of resources for sustainable lifestyles.	**04**	LO2
III	**Ecosystems**	Concepts of an ecosystem. Introduction, types, characteristic features, structure and function of the following ecosystem: a. Forest ecosystem b. Grassland ecosystem c. Desert ecosystem d. Aquatic ecosystem (ponds, streams, lakes, rivers, oceans, estuaries). Case study on various ecosystems in India.	**05**	LO3
IV	**Biodiversity and its Conservation**	Introduction-Definition: genetic species and ecosystem diversity. Bio-geographical classification of India Value of biodiversity : Consumptive use, productive use, social, ethical, aesthetic and option values, Bio-diversity at global, national, local levels India as a mega diversity nation Case study on Bio diversity in India.	**05**	LO4
V	**Environmental Pollution Definition**	Causes, effects and control measures of: a) Air pollution b) Water pollution c) Soil pollution. Solid waste management: Causes, effect and control measures of urban and industrial wastes. Role of an individual in prevention of pollution, Case study on Pollution Disaster management: floods, earthquake, cyclone and landslides. Carbon Credits for pollution prevention	**05**	LO5

VI	**Social Issues and Environment**	From unsustainable to sustainable development Urban problems related to energy, Water conservation, rain water harvesting, watershed management. Environmental ethics: issues and possible solution. Climate change, global warming, acid rain, ozone layer depletion, nuclear accidents and holocaust. Case studies. Consumerism and waste products. Environment protection act. Public awareness Case study on Environmental Ethics	**04**	LO6

Textbooks

1. Environmental Science: Towards a Sustainable Future, G. Tyler Miller and Scott Spoolman, 13th Edition, Cengage Learning 2021
2. Environmental Management: Text and Cases, Bala Krishnamoorthy, 3rd Edition, PHI Learning, Publication Year: 2016
3. Green IT: Concepts, Technologies, and Best Practices, Markus Allemann, Springer 2008
4. Sustainable IT: Slimming Down and Greening Up Your IT Infrastructure, David F. Linthicum, IBM Press 2009
5. Environmental Modelling: Finding Solutions to Environmental Problems, David L. Murray, Cambridge University Press 2016
6. Remote Sensing and Image Interpretation, Thomas M. Lillesand, Ralph W. Kiefer, and Jonathan W. Chipman, 9th Edition, John Wiley & Sons 2020
7. Business Ethics: Concepts and Cases, Manuel Velasquez, Pearson 2012

Reference Books

1. Environmental Law and Policy in India, Shyam Divan and Armin Rosencranz, 2nd Edition, Oxford University Press 2018
2. The International Handbook of Environmental Laws, David Freestone and Jonathon L. Rubin, Edward Elgar Publishing 2000
3. E-Waste Management: Challenges and Opportunities in Developing Countries, Ruediger Kuehr and Ram K. Jain, Springer 2008
4. The E-Waste Handbook: Managing Electronic Waste, Klaus Hieronymi, Ruediger Kuehr, and Ram K. Jain, Earthscan 2009
5. Environmental Ethics: An Introduction, J. Baird Callicott, University of Georgia Press1999

Online References:

Sr. No.	Website Name
1.	Centre for Science and Environment (CSE), Website: cseindia.org
2.	Ministry of Environment, Forest and Climate Change (MoEFCC), Government of India
3.	CSIR-National Environmental Engineering Research Institute (NEERI)

List of Experiments.

Sr No	List of Experiments	Hrs

01	Study of Environmental Components and Ecosystems.	2
02	Visit and Report on Solid Waste Management Plant.	2
03	Study of Renewable Energy Sources (Solar, Wind, Biogas).	2
04	Analysis of Air and Water Quality Parameters.	2
05	Study of Local Biodiversity and Conservation Methods.	2
06	Awareness Activity on Environmental Issues.	2
07	Rainwater Harvesting System Design	2
08	Case Study on Environmental Pollution & Control Measures.	2
09	Report on Climate Change Impact and Adaptation.	2
10	Study of Environmental Laws and Acts.	2
11	Study of Disaster Management Techniques.	2
12	Report on Role of IT in Environmental Protection.	2

Sr No	List of Assignments / Tutorials	Hrs
01	Prepare a report on Renewable and Non-Renewable Resources.	2
02	Write a case study on Ecosystem Types in India	2
03	Write a report on Biodiversity in India.	2
04	Prepare a report on Pollution Types and Control Measures.	2
05	Prepare a report on Environmental Ethics and Sustainability.	2
06	Prepare a case study report on Global Warming and Climate Change.	2
07	Report on Role of an Individual in Environmental Protection.	2
08	Write a report on Disaster Management Techniques.	2
09	Prepare a report on Environmental Laws and Acts in India.	2
10	Case Study on E-waste Management and Recycling Techniques.	2

Assessment :

Term Work: Term Work shall consist of at least 10 to 12 practical's based on the above list. Also, Term work Journal must include at least 8 to 10 assignments.

Term Work Marks: 50 Marks (Total marks) = 20 Marks (Experiment) + 15 Marks (Assignments) + 5 Marks (Attendance)+ 10 Marks (Report)

Sem. – IV

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2994511	Business Model Development	--	2*+2	-	--	2*+2	-	2

Course Code	Course Name	Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		IAT-I	IAT-II	IAT-I+IAT-II					
2994511	Business Model Development	--	--	--	--	--	50	--	50

Note: * Two hours of practical class to be conducted for full class as demo/discussion/theory.

Lab Objectives:

1. To introduce a learner to entrepreneurship and its role in economic development.
2. To familiarize a learner with the start-up ecosystem and government initiatives in India.
3. To explain the process of starting a business.
4. To familiarize a learner with the building blocks of a business.
5. To teach a learner to plan their own business with the help of Business Model Canvas.
6. To teach a learner to have financial plan for a business model.

Lab Outcomes:

The learner will be able to:

1. Discuss the role of entrepreneurship in the economic development of a nation and describe the process of starting a business.
2. Describe start-up ecosystems in Indian and global context.
3. Identify different types of business models.
4. Identify customer segments, channels and customer relationship components for a particular business.
5. Identify key activities, key partners and key resources for a particular business.
6. Develop a financial plan for a business with the help of cost structure and revenue model.

DETAILED SYLLABUS:

Sr. No.	Module	Detailed Content	Hours	LO Mapping
0	**Prerequisite**	Basic Design Thinking principles	**01**	**--**
I	**Introduction to Entrepreneurship**	**Introduction to Entrepreneurship**: Definition, the role of entrepreneurship in the economic development, the entrepreneurial process, Women entrepreneurs, Corporate entrepreneurship, Entrepreneurial mindset **Self-learning Topics:** Case studies: Henry Ford https://www.thehenryford.org/docs/default-source/default-document-library/default-document-library/henryfordandinnovation.pdf?sfvrsn=0 The Tatas: How a Family Built a Business and a Nation by Girish Kuber, April 2019, Harper Business	**04**	**L1, L2**
II	**Entrepreneurship Development**	**Entrepreneurship Development**: Types of business ownerships: Proprietorship, Public and Private Companies, Co-operative businesses, Micro, Small and Medium Enterprises (MSME): Definition and role of MSMEs in economic	**05**	**L2, L3, L4**

		development		
III	**Start-up financing**	**Start-up financing:** Cost and revenue models, Sources of start-up fundings: Angel investors, Venture capitalists, Crowd funding, Government schemes for start-up funding **Self-learning Topics:** Successful business pitching	**04**	**L2, L3, L4, L5**
IV	**Intellectual Property Rights (IPR)**	**Intellectual Property Rights (IPR):** Types of IPR: Patents, trademarks and copyrights, Patent search and analysis, Strategies for IPR protection, Ethics in technology and innovation	**04**	**L2, L3, L4**
V	**Business Model Development**	**Business Model Development:** Types of business models, Value proposition, Customer segments, Customer relationships, Channels, Key partners, Key activities, Key resources, Prototyping and MVP **Self-learning Topics:** The Art of the Start 2.0: The Time-Tested, Battle-Hardened Guide for Anyone Starting Anything by Guy Kawasaki	**04**	**L3, L4, L5, L6**
VI	**Digital Business Management**	**Digital Business Management:** Digital Business models (Subscription, Freemium *etc*), Digital marketing: Search Engine Optimization (SEO), Search Engine Marketing (SEM), Social media and influencer marketing, Disruption and innovation in digital business **Self-learning Topics:** Case study: Airbnb https://www.prismetric.com/airbnb-business-m	**04**	**L2, L3**

Textbooks:

1. Entrepreneurship: David A. Kirby, McGraw Hill, 2002
2. Harvard Business Review: Entrepreneurs Handbook, HBR Press, 2018
3. Business Model Generation; Alexander Ostlewalder and Yves Pigneur, Strategyzer, 2010
4. E- Business & E– Commerce Management: Strategy, Implementation, Practice – Dave Chaffey, Pearson Education

Reference books:

1. Entrepreneurship: New venture creation by David Holt, Prentice Hall of India Pvt. Ltd.
2. E- Business & E– Commerce Management: Strategy, Implementation, Practice – Dave Chaffey, Pearson Education

Online Resources:

Sr. No.	Website Name
3.	Entrepreneurship by Prof. C Bhaktavatsala Rao https://onlinecourses.nptel.ac.in/noc20_mg35/preview
4.	Innovation, Business Models and Entrepreneurship by Prof. Rajat Agrawal, Prof. Vinay Sharma https://onlinecourses.nptel.ac.in/noc21_mg63/preview
3.	Sarasvathy's principles for effectuation https://innovationenglish.sites.ku.dk/model/sarasvathy-effectuation/

List of Experiments.

The lab activities are to be conducted in a group. One group can be formed with 4-5 students. A group has to develop a Business Model Canvas and a digital prototype (Web App/ mobile app). Weekly activities are to be conducted as follows:

Sr No	Lab activities	Hrs
01	Problem identification (Pain points, Market survey)	2
02	Design a digital solution for the problem (Ideation techniques)	2
03	Preparing a business model canvas: Value proposition, Key partners, Key resources, Key activities	2
04	Preparing a business model canvas: Customer segment, Customer relationships and channels	2
05	Preparing a business model canvas: Cost and Revenue structure	2
06	Prototype development: Low fidelity	2
07	Prototype development: Customer feedback	2
08	Prototype development: High fidelity	2
09	Presentation of high-fidelity prototype	2

Sr No	List of Assignments / Tutorials	Hrs
01	Presentation on case study of a failed business model	2
02	Presentation on case study of a woman entrepreneur	2

Assessment:

Term Work: Term Work shall consist of 09 lab activities based on the above list. Also, Term work journal must include any 2 assignments from the above list.

Term Work Marks: 50 Marks (Total marks) = 25 Marks (Experiment) + 10 Marks (Assignments) + 5 Marks (Attendance)+10 Marks (Report).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2994512	Design Thinking	--	2*+2	-	--	2*+2	-	2

Course Code	Course Name	Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		IAT-I	IAT-II	IAT-I+IAT-II					
2994512	Design Thinking	--	--	--	--	--	50	--	50

Note: * Two hours of practical class to be conducted for full class as demo/discussion/theory.

Lab Objectives:

1. To introduce a learner to the principles of Design Thinking.
2. To familiarize a learner with the process (stages) of Design Thinking.
3. To introduce various design thinking tools.
4. Study of the techniques for generation of solutions for a problem.
5. To expose a learner to various case studies of Design Thinking.
6. Create and test a prototype.

Lab Outcomes:

Students will be able to …

1. Compare traditional approach to problem solving with the Design Thinking approach and discuss the principles of Design Thinking
2. Define a user persona using empathy techniques
3. Frame a problem statement using various Design Thinking tools
4. Use ideation techniques to generate a pool of solutions for a problem
5. Create prototypes using different techniques
6. Test the prototypes and gather feedback for refining the prototype

DETAILED SYLLABUS:

Sr. No.	Module	Detailed Content	Hours	LO Mapping
0	Prerequisite	No perquisites	-	-
I	**Introduction to Design Thinking**	**Introduction to Design Thinking:** Definition, Comparison of Design Thinking and traditional problem-solving approach, Need for Design Thinking approach, Key tenets of Design Thinking, 5 stages of Design Thinking (Empathize, Define, Ideate, Prototype, Test) **Self-learning Topics:** Design thinking case studies from various domains https://www.design-thinking-association.org/explore-design-thinking-topics/external-links/design-thinking-case-study-index	**05**	**L1, L2**
II	**Empathy**	**Empathy:** Foundation of empathy, Purpose of empathy, Observation for empathy, User observation technique, Creation of empathy map	**05**	**L2, L3**

		Self-learning Topics: Creation of empathy maps https://www.interaction-design.org/literature/topics/empathy-mapping		
III	**Define**	**Define:** Significance of defining a problem, Rules of prioritizing problem solving, Conditions for robust problem framing, Problem statement and POV **Self-learning Topics:** Creating a Persona – A step-by-step guide with tips and examples https://uxpressia.com/blog/how-to-create-persona-guide-examples	**05**	**L2, L3**
IV	**Ideate**	**Ideate:** What is ideation? Need for ideation, Ideation techniques, Guidelines for ideation: Multi-disciplinary approach, Imitating with grace, Breaking patterns, Challenging assumptions, Looking across value chain, Looking beyond recommendation, Techniques for ideation: Brainstorming, Mind mapping **Self-learning Topics:** How To Run an Effective Ideation Workshop: A Step-By-Step Guide https://uxplanet.org/how-to-run-an-effective-ideation-workshop-a-step-by-step-guide-d520e41b1b96	**05**	**L3**
V	**Prototype**	**Prototype:** Low and high-fidelity prototypes, Paper prototype, Story board prototype, Scenario prototype	**03**	**L6**
VI	**Test**	**Test:** 5 guidelines of conducting test, The end goals of test: Desirability, Feasibility and Viability, Usability testing	**03**	**L4, L5**

Textbooks:

1. Design Your Thinking: The Mindsets, Toolsets, and Skill Sets for Creative Problem-solving, Pavan Soni, Penguin Random House India Private Limited
2. Design Thinking: Methodology Book, Emrah Yayichi, 2016
3. Handbook of Design Thinking: Christian Mueller-Roterberg, 2018

Reference books:

1. Design Thinking for Strategic Innovation: What They Can't Teach You at Business or Design School, Idris Mootee, Wiley, 2013
2. Change by Design, Tim Brown, Harper Business, 2009

Online Resources:

Sr. No.	Website Name
5.	Design Thinking and Innovation by Ravi Poovaiah https://onlinecourses.swayam2.ac.in/aic23_ge17/preview
6.	Introduction to Design Thinking by Dr. Rajeshwari Patil, Dr. Manisha Shukla, Dr.

	Deepali Raheja, Dr. Mansi Kapoor https://onlinecourses.swayam2.ac.in/imb24_mg37/preview
3.	Usability Testing https://www.interaction-design.org/literature/topics/usability-testing

List of Experiments.

The experiments are to be performed in groups. A practical batch may be divided into groups of 4-5 students.

Sr No	List of Experiments	Hrs
01	Customer Journey Mapping: Visualize the steps users take to interact with a product or service. Map out the customer journey from discovering a product to making a purchase and using the product. Identify pain points and opportunities for improvement.	2
02	Stakeholder mapping: Identify all relevant stakeholders in a project. Create a stakeholder map, categorizing stakeholders based on their influence and interest. Include management of relationships with key stakeholders.	2
03	"How Might We" Problem Framing: Transform user insights into actionable problem statements. After empathizing with users, turn challenges into "How Might We" statements that define the problem without prescribing a solution.	2
04	Brainstorming Session: Generate a pool of ideas in a creative, non-judgmental environment. Using ideation techniques like mind mapping and brainwriting, students brainstorm as many solutions as possible to their "How Might We" problem statements.	2
05	Affinity Diagramming: Organize group ideas to find patterns and insights. After brainstorming, students will categorize their ideas into themes by placing sticky notes on a wall and moving them into groups based on similarities.	2
06	Rapid Prototyping: Create quick, low-fidelity versions of solutions. Use materials like paper, cardboard, and markers to build a prototype of their solution within 30 minutes. The focus is on speed and functionality, not aesthetics.	2
07	Wireframing: Create a visual guide for digital interfaces for mobile app / web app for the problems identified in earlier lab sessions. Students will sketch wireframes of the user interface for their product or service. Use tools like Balsamiq or paper and pen for low-fidelity wireframes.	2
08	Role-Playing: Walk through a prototype from the user's perspective. Students act as both users and designers, role-playing scenarios where they interact with their prototype (Developed in earlier lab sessions). Gather feedback from participants on how to improve the experience.	2
09	Usability Testing: Evaluation of the effectiveness and user-friendliness of a prototype (developed in earlier lab sessions). Students will have peers or target users test their prototypes, observe how they interact with it, and collect feedback on any issues or improvements needed.	2
10	Feedback Loop and Iteration: Refine solutions based on user feedback. After usability testing, students will refine their prototypes. Document changes made based on feedback and discuss how continuous iteration improves the design.	2

Sr No	List of Assignments (Any two)	Hrs
01	Create an empathy map for a target user group. Break them into four sections: *Says, Thinks, Feels, and Does*. Interview users or research their experiences to	3

	fill in the map.	
02	Based on research, students will create user personas including demographic details, motivations, pain points, and goals. Each group will present their persona to the class.	3
03	Consider 3 examples of real-life products which have good design and bad design. Write down reasons why do you think they are good or bad designs. May take user survey to support your work.	3
04	Study any open-source design thinking tool and write a brief report about it.	3

Assessment:

Term Work: Term Work shall consist of 08 to 10 lab activities based on the above list. Also, Term work journal must include any 2 to 4 assignments from the above list.

Term Work Marks: 50 Marks (Total marks) = 25 Marks (Experiment) + 10 Marks (Assignments) + 5 Marks (Attendance)+ 10 Marks (Report).

Vertical – 6

Experiential Learning Courses (CEP)

Detailed Syllabus

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303611	Smart Embedded Systems with Atmega 168/328	-	4	-	-	2	-	2

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test 1	Test 2	Avg. of 2 Tests				
2303611	Smart Embedded Systems with Atmega 168/328	--	--	--	--	50	25	75

Prerequisite:

- C++ and Java Programming
- Electronic Devices and Circuit
- Digital System Design

Course Objectives:

1. To understand the architecture, memory organization, and peripheral features of the ATmega168/328 microcontroller.
2. To explore internal functional blocks like timers, ADC, PWM, and serial communication interfaces.
3. To gain proficiency in embedded C programming using development tools for microcontroller programming and interfacing.
4. To interface wireless modules, sensors, actuators, and integrate IoT platforms for real-time control and monitoring.
5. To design, develop, and convert a prototype into a marketable embedded product considering DFM and cost analysis.
6. To design schematics and PCB layouts using EDA tools and apply fabrication standards

Course Outcomes:

After successful completion of the course student will be able to:

1. Identify pin configurations, explain memory types, and write simple code to configure I/O ports.
2. Configure and program internal peripherals like timers, interrupts, and serial communication modules (USART, SPI, I2C).
3. Develop, compile, and upload programs using IDEs, and implement sensor interfacing and interrupt handling.

4. Design and build embedded applications integrating sensors, actuators, displays, and wireless modules with cloud-based data logging and control.
5. Create a prototype, optimize it for production, and present a final project report including BOM and feasibility study.
6. Create a PCB layout, follow design rules, and generate Gerber files for the fabrication of microcontroller-based projects

DETAILED SYLLABUS:

Sr No	Detailed Content	Hours	LO Mapping
I	**Introduction to ATmega168/328 Microcontroller**	**05**	LO1
	Focus:		
	This unit introduces the fundamental concepts of **microcontrollers**, their difference from microprocessors, and specifically focuses on the **AVR family** with an emphasis on **ATmega168/328**. It covers **architecture, pin configuration, memory organization, and peripheral features**, helping students understand how to work with these controllers in embedded systems.		
	Key Skills: • Understanding microcontroller architecture • Pin Mapping and Peripheral Understanding • Memory Organization & Power Management • Programming I/O Ports • Clock and Power Management		
	Syllabus: • Overview of Microcontrollers and Microprocessors • Introduction to AVR Family • Features and Specifications of ATmega168/328 • Pin Diagram and Pin Description • Clocking and Power Management • Memory Organization: Flash, SRAM, EEPROM • I/O Ports and Peripheral Features		
	Activities: • Study and analyze the **ATmega168/328** datasheet • Identify and explain **pin configurations** and their functions • Discuss **Flash, SRAM, EEPROM**, and clocking system • Write simple **code to configure and control I/O ports** • Explore different **clock sources and power-saving modes**		
	Self-Learning:		
	• RAM and its Types of RAM • ROM and Its Types • Difference between RAM and ROM • Different ways of Clock Generation		

II	ATmega168/328 Architecture	07	LO2
	Focus:		
	This unit explores the **internal architecture and functionality** of the **ATmega168/328 microcontroller**, covering essential components such as registers, timers, interrupts, ADC, PWM, and communication protocols (USART, SPI, and I2C). The primary objective is to help students understand how the microcontroller processes data, interacts with external components, and handles real-time operations.		
	Key Skills:		
	• Understanding the ATmega168/328 Architecture • Register and Memory Utilization • Timers and Counters • Interrupt Handling • ADC (Analog to Digital Conversion) • PWM (Pulse Width Modulation) • USART, SPI, and I2C Communication		
	Syllabus: • Detailed Architecture and Block Diagram • Registers and Memory Mapping • Timers and Counters • Interrupt Handling • ADC (Analog to Digital Conversion) • PWM (Pulse Width Modulation) USART, SPI, and I2C Communication		
	Activities: • Study the **block diagram** and analyze internal components. • **Hands-on coding** for configuring **registers and memory-mapped I/O**. • Write a **program to implement delay generation** using timers. • Configure and test **external and internal interrupts** using ISR functions. • Interface a **potentiometer or sensor** and read analog values. • Generate **PWM signals** to control LED brightness or motor speed. • Establish serial communication between **ATmega168/328 and peripherals** (e.g., sensors, LCD, Bluetooth module).		
	Self-Learning:		
	• Types of IO Mapping • Interrupt and its types • PWM (Pulse Width Modulation) • Types of Communication (serial and Parallel) • Difference between Serial and Parallel Communication		

	• Serial Communication Frame format(UART)		
III	**Programming ATmega168/328**	**10**	LO3
	Focus: This unit introduces **Embedded C programming** and the necessary **software tools (IDEs)** for programming microcontrollers. It focuses on **basic coding, hardware interfacing**, and **handling interrupts**, providing students with the fundamental skills required for embedded system development.		
	Key Skills: • Understanding Embedded C Programming • Installing and Setting Up IDEs • Writing and Compiling Code in Arduino IDE • GPIO (General Purpose Input/Output) Interfacing • Sensor Interfacing • Serial Communication • Interrupt Handling		
	Syllabus: • Introduction to Embedded C Programming • Basic Data types, Functions & Pointers, Data Structures, • Memory & Optimization, Bit Manipulation & Efficiency. • Installation and Setup of IDEs like Keil, Atmel Studio / Microchip Studio, PlatformIO (with VS Code), Eclipse with AVR Plugin (AVR-Eclipse),AVR-GCC + Makefiles (Bare Metal CLI) • GPIO Interfacing and LED Blinking • Analog and Digital Sensor Interfacing • Serial Communication Programming • ISR for Interrupt handling		
	Activities: • **Write and execute basic C programs** for microcontrollers. • Install **Atmel Studio, or PlatformIO** and configure settings. • Develop **basic programs**, compile and upload them to a microcontroller. • Implement **LED blinking** and control output pins using code. • Interface **analog (e.g., temperature sensor) and digital sensors (e.g., IR sensor)**. • Implement **UART communication** to send and receive data. • Use **Interrupt Service Routines (ISRs)** to handle external events (e.g., button press detection).		
	Self-Learning:		
	• Basics of embedded c programming • Various IDE for microcontroller programming • I/O pins and sensor interfacing • Serial communication and interrupt handling		

Unit 4	**Advanced Technologies and Project Integration**	**18**	LO4
	Focus: This unit focus **wireless communication, sensor and actuator interfacing, display modules, motor control, and IoT integration** with **ATmega microcontrollers**. The goal is to develop skills in **real-time data acquisition, processing, and remote monitoring/control** using cloud platforms.		
	Key skills: • **Wireless Communication** (Bluetooth, Wi-Fi, Zigbee, RF 434MHz, LoRa) • **Sensor Interfacing** (Gas, Fire, DHT11, Obstacle, Soil Moisture, etc.) • **Actuator Interfacing** (Relay, Motor, Buzzer) • **Display Interfacing** (LCD, LED, OLED, 7-Segment) • **Motor Control with L298N Motor Driver** • **IoT Integration with ATmega** (ThingSpeak, MQTT, Blynk,Google Firebase) • **Real-Time Data Monitoring and Control**		
	Syllabus: • Introduction to Wireless Communication (Bluetooth, Wi-Fi, Zigbee, RF434Mhz, LoRa) • Introduction to modules of Wireless identification (RFID-RC-522, EM-18, nRF24L01) • Introduction to Sensors like IR (Obstacle, Gas, Fire, DHT11, Obstacle, Soil Moisture, Ultrasonic(HC-SR-04), Touch, Biometric Pressure(BMP180/280),Soil Sensor moisture ,water Sensor, Heart Rate (Max30100),PIR, Gyroscope(MPU6050), magneto meter(HMC5883L), Accelerometer(ADXL345), Finger Print Sensor(R305), Camera Modules (e.g., OV7670), Strain Gauge, Voltage(ZMPT101B), Current(ACS712) and Power Sensor(PZEM-004T) or similar etc. • Introduction to Actuator like Relay, Motor (DC Motor, Stepper, Servo), Buzzer • Motor Control using L293D/L298N Motor Driver • Relay Control using ULN2803 • Stepper Control using ULN 2003/2803 • Servo Motor Control • Interfacing of LCD, LED, OLED,7Segment • IoT Integration with ATmega (Cloud Like Thingspeak, MQTT, Blynk, Google Firebase or Similar) • Real-Time Data Monitoring and Control • Using above Hardware/Controller define the Project define problem statement to make a project and into product		

	Activities: • Establish wireless communication between **microcontroller and other devices** for data exchange. Example: **Control LEDs using a Bluetooth app**. • Connect sensors to **ATmega** and write code to **collect and process sensor data**. Example: **Display temperature readings from DHT11**. • Use **relays and buzzers** for control actions. Example: **Activate a buzzer when gas is detected**. • Display sensor readings and system status on **LCD/OLED**. Example: **Show soil moisture levels on OLED display**. • Control **DC motors and stepper motors** using **L298N motor driver**. Example: **Move a robotic car forward and backward**. • Connect microcontroller to **cloud platforms** for **real-time monitoring and control**. Example: **Upload temperature data to ThingSpeak** and view it on a dashboard. • Develop a system to **monitor and control devices remotely**. Example: **Send sensor data to a web dashboard and control relays from a mobile app**		
	Self-Learning:		
	• Wireless communication and its type • Sensor and its pin diagram for interfacing • Actuator and its pin diagram for interfacing • Display and its pin diagram for interfacing • IOT integration with embedded system		
V	**Project Development, Testing and Product Conversion**	**4+4**	LO5
	Focus:		
	This unit focuses on **end-to-end product development**, from **conceptualization to market-ready product conversion**. It covers **system design, prototyping, testing, debugging, hardware-software integration, and final manufacturing considerations**.		
	Key Skills: • **Design for Manufacturing (DFM)** • **Enclosure Design** • **Cost Estimation & Bill of Materials (BOM)** • **Market Analysis & Feasibility Study** • **Final Project Report & Presentation**		
	Syllabus (Part-1): • System Design and Block Diagram Creation • Component Selection and Circuit Design • Prototype Development and Testing • Debugging Techniques • Software and Hardware Integration		

	Syllabus (Part-2): • Design for Manufacturing (DFM) • Enclosure Design • Cost Estimation and Bill of Materials (BOM) • Market Analysis and Feasibility Study • Final Project Report, Presentation and Research Paper		
	Activities: • Optimize the design for **mass production**. Example: **Ensure PCB layout follows industry-standard manufacturing constraints**. • Develop **3D-printed or injection-molded enclosures** for the product. Example: **Design a waterproof casing for an outdoor sensor**. • Analyze the **cost-effectiveness** of components. Example: **Prepare a BOM with pricing details for a consumer IoT device**. • Conduct a study on **potential users and competitors**. Example: **Analyze demand for a smart wearable device**. • Document findings and present the **prototype-to-product journey**. Example: **Showcase a working prototype with performance insights**.		
	Self-Learning:		
	• 3D Printing, Types of 3D printing, • Material used in 3D printing • Basics of 3D printing model • 3D modelling Software, how to use that software • Basic knowledge of cost estimation and bills of material • Idea to product development procedure		
VI	**PCB Design for Microcontroller Projects**	**04**	LO6
	Focus: This unit introduces **Printed Circuit Board (PCB) design**, covering **schematic creation, layout design, tools, design rules, and fabrication processes**. The goal is to develop skills for designing efficient and manufactural PCBs using industry-standard software		
	Key Skills: • **Understanding PCB Design** • **Schematic Creation & PCB Layout Design** • **Using PCB Design Software** (Eagle, Altium, KiCAD, EasyEDA) • **Design Rules & Guidelines** • **PCB Fabrication Process**		
	Activities: • Learn **PCB design fundamentals**, importance, and real-world applications. Example: **Exploring single-layer and multi-layer PCB designs**.		

	• Design circuit schematics and convert them into PCB layouts. Example: **Create a simple LED driver circuit in Proteus/Eagle**. • Work with **PCB design tools** to simulate and design circuits. Example: **Simulate an Arduino-based circuit in Proteus**. • Learn **trace width, spacing, ground planes, and via placement** for optimized PCB design. Example: **Follow proper clearance rules to prevent short circuits**. • Understand **etching, drilling, solder masking, and component placement** for PCB manufacturing. Example: **Design a PCB and generate Gerber files for fabrication for the project taken by students**		
	Syllabus: • Introduction to PCB Design • PCB Layout and Schematic Creation • Tools for PCB Design: Proteus, Eagle, Altium Designer, KiCAD, Easy EDA or any similar tool • Design Rules and Guidelines • PCB Fabrication Process		
	Self-Learning:		
	• PCB, Types of PCB • Material used for PCB and their advantages, Disadvantages and Application • Standard PCB design processes • Basic knowledge about PCB design tools • PCB design rules for compact design structure • PCB fabrication process		

Textbooks:

1. "AVR Microcontroller and Embedded Systems" by Muhammad Ali Mazidi, Sarmad Naimi, and Sepehr Naimi.
2. "Programming and Customizing the AVR Microcontroller" by Dhananjay V. Gadre.
3. "Embedded Systems: Real-Time Interfacing to ARM Cortex-M Microcontrollers" by Jonathan Valvano.
4. "Introduction to Embedded Systems" by Shibu K V.
5. "Embedded C Programming and the Atmel AVR" by Barnett, Cox, and O'Cull.
6. **Additive Manufacturing Technologies: 3D Printing, Rapid Prototyping, and Direct Digital Manufacturing" by** Ian Gibson, David Rosen, Brent Stucker
7. **PCB Design Tutorial" by** David L. Jones (EEVblog)

Reference Books:

1. "AVR Programming: Learning to Write Software for Hardware" by Elliot Williams
2. "Microcontroller Projects in C for the 8051" by Dogan Ibrahim

3. "The AVR Microcontroller and Embedded Systems Using Assembly and C" by Muhammad Ali Mazidi.
4. "Designing Embedded Systems with PIC Microcontrollers" by Tim Wilmshurst.
5. "Fundamentals of Microcontrollers and Applications in Embedded Systems" by Ramesh S. Gaonkar
6. **"3D Printing for Dummies"** *(Introductory & DIY-friendly)* **By** Kalani Kirk Hausman, Richard Horne
7. **"Designing Printed Circuits Boards with KiCad" by** Peter Dalmaris

Software Tools:

1. **Arduino IDE:** **https://www.arduino.cc/en/software**
2. **Atmel Studio:** **https://www.microchip.com/en-us/tools-resources/develop/microchip-studio**
3. **Microchip Studio:** **https://www.microchip.com/en-us/tools-resources/develop/microchip-studio**
4. **PlatformIO: https://platformio.org/install/integration**

   https://platformio.org/platformio-ide

5. **Visual Studio Code:** **https://code.visualstudio.com/**
6. **AVR-GCC (GNU AVR C Compiler):**

   https://gcc.gnu.org/wiki/avr-gcc

7. **SimulAVR or AVRSim (Simulation Tools):** **https://sourceforge.net/projects/simulavr/**
8. **3D Model Design Website**

   https://www.sketchup.com/

   Spline - 3D Design tool in the browser with real-time collaboration

   3D Models for Free - Free3D.com

Online Resources:

Sr No	Website
1	Microchip Technology – ATmega328P Datasheet: Detailed specifications and features of the ATmega328P microcontroller.
	https://ww1.microchip.com/downloads/en/DeviceDoc/Atmel-7810-Automotive-Microcontrollers-ATmega328P_Datasheet.pdf
2	AVR Libc – NonGNU.org: Comprehensive user manual for AVR Libc, providing standard C library functions for AVR microcontrollers.nongnu.org+1Courses at Washington University+1
	https://www.nongnu.org/avr-libc/user-manual/

3.	Arduino Official Website: Resources, tutorials, and documentation for Arduino boards and software.
	https://www.arduino.cc/
4.	SparkFun Inventor's Kit Experiment Guide: Step-by-step projects and experiments using the SparkFun Inventor's Kit.Microchip Docs+2SparkFun Learn+2SparkFun Learn+2
	https://learn.sparkfun.com/tutorials/sparkfun-inventors-kit-experiment-guide---v41
5	Microchip Developer Help: Comprehensive online resource for developers using Microchip's products and technologies.Developer Help
	https://developerhelp.microchip.com/xwiki/bin/view/Main/
6	KiCad EDA – Schematic Capture & PCB Design Software: Open-source software suite for electronic design automation.Altium+6Wikipedia+6KiCad EDA+6
	https://www.kicad.org/
7	Autodesk EAGLE: Electronic design automation software for PCB designers.Autodesk
	https://www.autodesk.com/products/eagle/overview
8	Altium Designer: Industry-leading PCB design software combining schematic, layout, and more.Altium
	https://www.altium.com/
9	ThingSpeak – IoT Analytics Platform: Platform for aggregating, visualizing, and analyzing live data streams in the cloud.Halvorsen Blog+3ThingSpeak+3MathWorks - Maker of MATLAB and Simulink+3
	https://thingspeak.com/
10	Blynk IoT Platform: Low-code platform for building and managing IoT applications.
	https://blynk.io/
11	3D printing : Tinkercad (by Autodesk)
	https://www.tinkercad.com
12	3D printing : FreeCAD (Open Source CAD Software)
	https://www.freecadweb.org

List of Experiments:

Sr No	List of Experiments	Hours
01	Installation of IDE used and How write program, compile, and Upload	2
02	Interfacing of Buzzer, LED, Relay, Motors	4
03	Interfacing of different Sensors	4
04	Interfacing of Different cloud Platform & Database	4
05	Designing of circuit diagram of project	2
06	Designing of PCB on Software	2
07	Preparing PCB and Testing	2

08	Assembling project and Testing	4
09	Designing of enclosure on 3d Printing Software	2
10	Printing the enclosure	
11	Preparing Bill of Material, Market analysis and Feasibility study	2
12	Writing Research Paper	4

List of Mini Projects:

Note: This is tentative list student select any other good topic which should have 2-3 sensor, 2-3 actuator, 2 Cloud Platform and database.

Mini Project Topic	
1	**Title: Smart Environment Monitor: Temperature & Gas Alert System** **Synopsis:** • This project integrates a ATMEGA168/328 with a DHT11 sensor to measure temperature and humidity, along with a gas sensor to detect harmful gas leaks. • An OLED display shows real-time data locally. The system sends sensor data to both Blynk and Thingspeak for remote monitoring. • Alerts are triggered if the values exceed safe limits, ensuring prompt action. This IoT solution is ideal for home or industrial environmental safety **Sensors used**: DHT11, Gas Sensor **Actuator used:** OLED, Buzzer,LED **Cloud Platform:** Bynk, Thingspeak
2	**Title: Smart Irrigation System: Automated Plant Watering** **Synopsis:** • Using a soil moisture sensor interfaced with **ATMEGA168/328**, this project continuously monitors the moisture level of the soil. • When moisture falls below a preset threshold, a **water pump** is automatically activated to irrigate the plants. • Data is logged and displayed remotely via **Thingspeak** and **Blynk**, allowing users to track plant health. It's a practical application of IoT in agriculture and home gardening. **Sensors used:** Soil Moisture Sensor **Actuator used:** Water Pump, Buzzer, LED **Cloud Platform:** Blynk, Thingspeak
3	**Title: Dual Sensor Alert: Obstacle & Metal Detection System** **Synopsis:** • This project combines a **proximity sensor** with an **IR sensor** on an **ATMEGA168/328** platform to detect obstacles and metallic objects. • When an object is detected, the system immediately sends a signal via the **Blynk app** for

	real-time monitoring. • It can be used for security or automated machinery, ensuring safety and operational efficiency. **Sensors used:** Proximity Sensor, IR Sensor **Actuator used:** Buzzer, LED **Cloud Platform:** Blynk
4.	**Synopsis:** • Integrating an **IR sensor** with a **servo motor**, this **ATMEGA168/328** project automates parking space management. • The IR sensor detects the presence of a vehicle, while the servo motor operates the parking gate. • Communication through **MQTT** ensures real-time updates and remote monitoring of parking slot availability. **Sensors used:** IR Sensor **Actuator used:** Servo Motor, Buzzer, LED **Cloud Platform:** MQTT
5	**Title: Clean Air Sentinel: Air Quality Monitoring & Alert System** **Synopsis:** • This project uses **ATMEGA168/328** paired with an **MQ135 sensor** and an additional gas sensor to monitor ambient air quality. • Readings are displayed on an **LCD** and transmitted via **MQTT** for remote monitoring. • When pollutant levels exceed safe thresholds, the system generates an alert. It is suitable for both indoor and outdoor environments, promoting health and safety. **Sensors used:** MQ135, Gas Sensor **Actuator used:** LCD, Buzzer,LED **Cloud Platform:** MQTT, Blynk
6	**Title: Smart Water Tank: Automated Water Level Control System** **Synopsis:** • In this project, an **ultrasonic sensor** connected to **ATMEGA168/328** monitors the water level in a tank continuously. • When water levels drop below a predetermined level, a **water pump** is automatically activated to refill the tank. • Real-time data is sent through **MQTT** to a cloud-based dashboard for remote supervision. This system ensures efficient water management and minimizes wastage. **Sensors used:** Ultrasonic Sensor **Actuator used:** Water Pump, LED, Buzzer **Cloud Platform:** MQTT, Blynk
7	**Title: Smart Waste Manager: Automated Dustbin Monitoring System** **Synopsis:** • This project employs an **ultrasonic sensor** to measure the fill level of a dustbin, with a **servo**

	motor mechanism for automated lid control. • A **buzzer** sounds an alert when the bin nears capacity. Sensor data is sent to both **Thingspeak** and **Blynk**, enabling remote monitoring and timely waste collection. • The system aims to maintain cleanliness and optimize waste management processes. **Sensors used:** Ultrasonic Sensor **Actuator used:** Servo Motor, Buzzer, LED **Cloud Platform:** Thingspeak, Blynk
8	**Title: Safety First: Temperature & Fire Early Warning System** **Synopsis:** • A **DS18B20 temperature sensor** and a **fire sensor** work with **ATMEGA168/328** to monitor critical environmental parameters. • An **LCD** provides local readouts while data is sent via **MQTT** and **ThingSpeak** to cloud platforms for remote access. • Alerts are triggered when temperature or fire hazards are detected, ensuring early intervention. **Sensors used:** DS18B20, Fire Sensor **Actuator used:** LCD, Buzzer,LED **Cloud Platform:** Thingspeak, MQT
9	**Title: CloudGuard: Comprehensive Fire & Environmental Monitoring System** **Synopsis:** • This project combines **DHT11**, **gas**, and **fire sensors** with **ATMEGA168/328** to continuously monitor temperature, humidity, and fire risks. • An **LCD** displays current readings while data is uploaded to **Thingspeak** and **Blynk** for remote access. • Automated alerts notify users of any hazardous conditions, facilitating quick responses. **Sensors used:** DHT11, Gas Sensor, Fire Sensor **Actuator used:** LCD, Buzzer,LED **Cloud Platform:** Thingspeak, Blynk
10	**Title: Guardian Fall Alert: IoT-based Fall Detection and Response** **Synopsis:** • Using a **gyroscope sensor** interfaced with **ATMEGA168/328**, this system detects abnormal movements indicative of a fall. • A **buzzer** provides immediate local alerts, while sensor data is shared with **Thingspeak** and **Blynk** for remote monitoring. • The project is designed to assist elderly individuals or those at risk by ensuring rapid response in emergencies. **Sensors used:** Gyroscope Sensor,SOS button **Actuator used:** Buzzer,LED **Cloud Platform:** Thingspeak, Blynk

11	**Title: Local IoT Controller: Web-based Device Management System** **Synopsis:** • This project connects **ATMEGA168/328** to a **relay**, **buzzer**, and **light sensor** to manage multiple devices via a **local web server**. • Users can control appliances, receive feedback from the light sensor, and get alert notifications through a web interface. • It eliminates the need for external servers, ensuring quick response times within a local network. Ideal for smart home applications requiring robust local control. **Sensors used:** Light Sensor **Actuator used:** Relay, Buzzer,LED **Cloud Platform:** Local Web Server
12	**Title: Atmospheric Insights: Multi-parameter Environmental Monitor** **Synopsis:** • Utilizing the **BMP280 sensor** with **ATMEGA168/328**, this project measures temperature, pressure, and humidity simultaneously. • An **OLED display** presents real-time environmental data, while **MQTT** transmits the readings to remote platforms. • The system provides comprehensive weather insights and alerts if values deviate from normal, applicable in both indoor and outdoor environments. **Sensors used:** BMP280 **Actuator used:** OLED,Buzzer,LED **Cloud Platform:** MQTT,Blynk
13	**Title: Wireless Switchboard: Remote Device Control using RF Communication** **Synopsis:** • This project uses an **RF434 MHz wireless module** with **ATMEGA168/328** to remotely control devices via a **relay**. • It demonstrates basic wireless communication by sending on/off commands over a distance. • Useful for home automation where remote appliance control is needed, offering a simple and effective RF-based IoT solution. **Sensors used:** — **Actuator used:** Relay **Cloud Platform:** —
14	**Title: Smart Crossing Controller: Integrated Road and Railway Signal System** **Synopsis:** • In this project, **ATMEGA168/328** is connected to four **obstacle sensors** to control traffic signals at a road-railway crossing. • Sensors detect approaching vehicles or trains, triggering appropriate signal changes and automated barrier controls. • This system enhances safety and reduces the risk of accidents at high-risk intersections. **Sensors used:** Obstacle Sensor (×4)

	Actuator used: Traffic Signal LEDs, Servo Motor (if used for barrier) **Cloud Platform:** Bynk, MQTT
15	**Title: QR-Controlled Smart Home: Automated Appliance Management** **Synopsis:** • This project employs **ATMEGA168/328** to control appliances (lights, fans, bulbs) via **QR code scanning**. • By scanning a QR code, users send commands to the controller which toggles the connected devices. • Combines traditional QR technology with IoT to provide secure and convenient home automation. **Sensors used:** QR Scanner **Actuator used:** Relay (for light, fan, bulb control), Buzzer, LED **Cloud Platform:** Blynk
16	**Title: Smart Attendance Tracker: RFID-based Monitoring System** **Synopsis:** • Using an **RFID RC522 module** with **ATMEGA168/328**, this project automates attendance for classrooms or offices. • When a tag is scanned, entry is recorded and status is displayed on an **LCD**. Data is sent to **Thingspeak** and **Blynk** for monitoring. • It reduces manual errors and improves attendance management with IoT integration. **Sensors used:** RFID RC522 **Actuator used:** LCD, Buzzr, LED **Cloud Platform:** Thingspeak, Blynk
17	**Title: Secure Access Control: RFID-based Smart Door Lock** **Synopsis:** • This project uses an **RFID RC522** sensor with **ATMEGA168/328** for door access. • Authorized users unlock the door via RFID, which triggers a **solenoid lock** and updates an **LCD** display. • Remote monitoring is available via **Blynk**, enhancing access security for homes and offices. **Sensors used:** RFID RC522 **Actuator used:** Solenoid Lock, LCD, Buzzer, LED **Cloud Platform:** Blynk, MQTT
18	**Title: Touch-Activated Controller: MQTT-based Device Interface** **Synopsis:** • **ATMEGA168/328** is integrated with a **touch sensor**, **buzzer**, and **relays** to control devices. • Touch input triggers the devices and sends real-time updates via **MQTT**, enabling low-latency IoT-based control. • Highlights efficient use of MQTT in responsive home automation systems. **Sensors used:** Touch Sensor (4 switches)

	Actuator used: Relay, Buzzer, LED **Cloud Platform:** MQTT, BlyNK **Database** :Firebase
19	**Title: Retro Metro Switch: Automated Switch Control** **Synopsis:** • This creative project repurposes a retro switch using two **servo motors** controlled by **ATMEGA168/328**. • The system mimics metro-style switching and allows remote control via **Blynk**, enhancing traditional infrastructure. • A smart approach to blending legacy tech with IoT automation. **Sensors used:** — **Actuator used:** Servo Motor (×2),LED, Buzzer **Cloud Platform:** Blynk, MQTT
20	**Title: Intelligent Entry: Smart Door Security and Access Control** **Synopsis:** • A **PIR sensor**, **servo motor**, and **buzzer** are connected to **ATMEGA168/328** for door control. • The PIR sensor detects motion and triggers servo-based door lock/unlock. • Alerts and monitoring are enabled via **Blynk**, enhancing home and office entry security. **Sensors used:** PIR Sensor **Actuator used:** Servo Motor, Buzzer, LED **Cloud Platform:** Blynk Database: Cloud Fire base
21	**Title: PillTime: Smart Medication Reminder and Tracker** **Synopsis:** • This project combines a **Real Time Clock (RTC)**, **LCD**, and **RF module** with **ATMEGA168/328** to remind users about their medication schedules. • It displays scheduled pill times on an LCD and sends reminders via **Blynk** for timely medication intake. • Especially beneficial in healthcare to improve adherence to prescribed routines. **Sensors used:** RTC **Actuator used:** LCD, RF Module, LED **Cloud Platform:** Blynk, MQTT
22	**Title: IoT Robotic Arm: Remote Manipulation and Control** **Synopsis:** • Using multiple **servo motors** interfaced with **ATMEGA168/328**, this project creates a remotely controllable robotic arm. • Movements are controlled via **Blynk**, enabling precise automation for education, industry, or remote handling.

	• Demonstrates effective integration of mechanics and IoT. **Sensors used:** — **Actuator used:** Servo Motors, Buzzer, LED **Cloud Platform:** Blynk, MQTT
23	**Title: Smart Scale: IoT-enabled Digital Weighing System** **Synopsis:** • This project uses a **load cell** with **ATMEGA168/328** to build a digital weighing scale. • Weight is displayed on an **LCD** and transmitted to **Blynk** for monitoring and analysis. • Ideal for commercial or personal use, combining accuracy and IoT analytics. **Sensors used:** Load Cell **Actuator used:** LCD, LED, Buzzer **Cloud Platform:** Blynk, MQTT
24	**Title: ColorSort Pro: Automated Color-based Sorting System** **Synopsis:** • A **color sensor** attached to **ATMEGA168/328** identifies object colors and sorts them accordingly. • An **OLED display** shows the detected color, while **MQTT** handles cloud-based monitoring. • Used in recycling or quality control for industrial automation. **Sensors used:** Color Sensor **Actuator used:** OLED,LED, Buzzer **Cloud Platform:** MQTT **Database:** Google Firebase
25	**Title: Smart Traffic Manager: IoT-Enabled 4-Way Signal Control** **Synopsis:** • This project uses an **IR sensor** and **LEDs** with **ATMEGA168/328** to manage traffic at a 4-way signal. • Sensors detect vehicle presence and optimize signal timing. • Integration with **Blynk** allows remote monitoring, improving traffic flow and safety. **Sensors used:** IR Sensor **Actuator used:** LEDs, Buzzer **Cloud Platform:** Blynk, MQTT **Database :** Google Firebase
26	**Title: Smart Assistance Stick: Multi-sensor Navigation Aid** **Synopsis:** • Designed for visually impaired individuals, this **ATMEGA168/328** project uses **ultrasonic**, **fire**, **water**, and **IR sensors**. • Real-time alerts are sent via **Blynk**, assisting safe navigation. • A powerful example of assistive technology using IoT.

	Sensors used: Ultrasonic, Fire, Water, IR **Actuator used:** Buzzer, LED **Cloud Platform:** Blynk, MQTT
27	**Title: Smart Waste Sorter: Automated Waste Segregation System** **Synopsis:** • **IR sensor** and **servo motor** with **ATMEGA168/328** automatically sort waste into separate bins. • Sensor data is transmitted over **MQTT** for remote tracking. • Promotes smart recycling by reducing manual effort. **Sensors used:** IR Sensor **Actuator used:** Servo Motor, LED, Buzzer **Cloud Platform:** MQTT, Thingspeak
28	**Title: Washroom Sentinel: Feedback and Monitoring System** **Synopsis:** • Built using **ATMEGA168/328**, this project collects user feedback via a **switch** and displays it on an **LCD**. • Data is logged and can be analyzed via **Blynk** or exported to Excel. • Helps maintain public hygiene by analyzing satisfaction levels. **Sensors used:** Switch **Actuator used:** LCD, Buzzer, LED **Cloud Platform:** Blynk **Database** Google Firebase
29	**Title: FireGuard Pro: Automated Fire and Temperature Alert System** **Synopsis:** • **Fire** and **temperature sensors** are connected to **ATMEGA168/328** to continuously monitor safety conditions. • When thresholds are exceeded, alerts are sent via **SMTP email** (HTML/text/attachments). • Excellent for proactive safety in industrial and residential spaces. **Sensors used:** Fire Sensor, Temperature Sensor **Actuator used:** Buzzer, LED **Cloud Platform:** SMTP (Email Notifications)
30	**Title: DoorWatch: Real-Time Door Status Monitoring with Telegram Alerts** **Synopsis:** • This project uses **ATMEGA168/328** with door sensors to monitor and send alerts via **Telegram**. • Notifies users instantly of door access or breaches, and logs the data for review. • Enhances home and office security using messaging platforms. **Sensors used:** Magnetic Door Sensor **Actuator used:** Buzzer, LED

	Cloud Platform: Telegram
31	**Title: MotionSense Hub: Comprehensive Inertial and Temperature Monitoring** **Synopsis:** • Combines **MPU-6050** (accelerometer, gyroscope) with **ATMEGA168/328** to monitor motion and temperature. • Data is displayed in **Blynk** and **Thingspeak** dashboards. • Ideal for asset tracking, movement analysis, and safety monitoring. **Sensors used:** MPU-6050 **Actuator used:** LED, Buzzer **Cloud Platform:** Blynk, Thingspeak
32	**Title: CloudBME: Real-time Environmental Monitoring with BME280** **Synopsis:** • **BME280** sensor with **ATMEGA168/328** monitors temperature, pressure, and humidity. • Data is sent to a real-time cloud database for access via mobile/web dashboard. • Triggers alerts on deviations, useful for both home and industry. **Sensors used:** BME280 **Actuator used:** LED, Buzzer **Cloud Platform:** Real-time Database
33	**Title: FarmSense Secure: Firebase-Integrated Sensor Dashboard** **Synopsis:** • **ATMEGA168/328** with **DS18B20 temperature sensor** and **soil moisture sensor** sends data to **Firebase**. • Secure web dashboard with Firebase authentication allows only authorized access. • Ideal for smart farming and remote environment monitoring. **Sensors used:** DS18B20, Soil Moisture Sensor **Actuator used:** Buzzer, LED **Cloud Platform:** Bynl, Thingspeak Database: Firebase
34	**Title:** Smart Energy Meter / Smart Grid System **Synopsis:** • This project utilizes **ATMEGA168/328** integrated with a **ZMPT101B voltage sensor** and an **ACS712 current sensor** to measure real-time voltage and current from the electrical supply. • The measured data is processed to calculate **power consumption**, which is displayed locally on an **OLED display**. • For remote monitoring and analytics, the data is sent to **Blynk**, **Thingspeak**, and stored in **Firebase** as a real-time database. • The system helps in monitoring energy usage, detecting overloads or abnormal consumption patterns, and promotes efficient energy management in homes or industries. • It also supports grid-level applications where power quality and usage data from multiple

	locations can be aggregated and analyzed. **Sensors used:** ZMPT101B (Voltage Sensor), ACS712 (Current Sensor) **Actuator used:** OLED Display, LED, Buzzer **Cloud Platform:** Blynk, Thingspeak Database: Firebase
35	**Title: Smart Health Monitor: Pulse Rate, Heartbeat & Oxygen Level Tracking System** **Synopsis:** • This project is based on **ATMEGA328** and integrates biomedical sensors to measure **pulse rate**, **heart rate**, and **SpO_2 (oxygen saturation)** levels. • The real-time health data is displayed on an **OLED screen**, and abnormal values trigger alerts via a **buzzer** and **LED indicator**. • For remote health tracking and data logging, the readings are uploaded to **Blynk**, **Thingspeak**, and **Firebase Real-time Database**. • This system is particularly useful for **home healthcare**, **elderly monitoring**, and **telemedicine**, allowing patients and doctors to monitor vitals remotely with real-time alerts. • It ensures timely awareness of critical health changes and supports emergency response. **Sensors used:** Pulse Sensor (or MAX30100/MAX30102 for heart rate & SpO_2) **Actuator used:** OLED Display, Buzzer, LED **Cloud Platform:** Blynk, Thingspeak Database: Firebase (Real-time Database)

Guidelines for execution of syllabus and maintain quality of mini project are as follows:

Teaching Approach:

1. Modules 1, 2, and 3 shall be taught in parallel to provide an integrated learning experience.
2. After covering the theoretical concepts of ATmega328 microcontroller architecture and its features, programming fundamentals should be introduced.
3. Embedded C programming should be taught in the context of microcontroller programming.
4. Initial programming exercises should focus on developing a strong understanding of Embedded C syntax and structure specific to microcontroller applications.
5. Subsequently, fundamental programming related to GPIO, Timers, and Interrupts must be covered along with their practical implementation on the ATmega328 microcontroller.
6. Module 4 shall focus on interfacing various sensors, actuators, and communication protocols with the ATmega328 microcontroller, covering their working principles and practical implementation on the development board.
7. Hands-on experiments must be conducted for each interfacing to ensure practical understanding and testing of sensors, actuators, and communication protocols.
8. After thorough understanding and practical exposure to Modules 1, 2, 3, and 4, students should proceed with project selection and execution based on the ATmega328 microcontroller.

Project Topic Selection and Approval:

1. A mini-project group shall consist of a minimum of THREE (03)
2. Project topic selection and approval should be done by a panel of two expert faculty members from the department.
3. Each group must maintain a logbook to record weekly work progress in terms of milestones. The guiding faculty should provide remarks/comments weekly. Both students and faculty must sign the logbook every week.

Project Report Format:

1. The mini-project report should include the following sections:
    - Abstract (maximum 1 page summarizing the complete report)
    - Introduction (justification for project selection, applications, and existing commercial products)
    - Implementation (proposed specifications, block diagram/circuit diagram, working principle/operation)

- Results & Discussion (photographs, video links, recorded results such as waveforms/observations/demonstrations)
- Conclusion and Learning Outcomes
- References
- Participation in Mini-project competition/Technical Paper Presentation (TPP), etc.

2. The report should not exceed 10 pages. Reports should be stapled to avoid the use of plastic.

Term Work & Practical/Oral Examination Evaluation:

1. Minimum six (06) experiments based on Modules 1, 2, 3, and 4 should be performed.
2. One mini project must be implemented.
3. It is not compulsory to select a project from the given sample list; students are encouraged to propose innovative ideas.
4. Students must deliver a presentation and demonstrate their mini project during the Practical and Oral Examination (25 Marks Evaluation).
5. Project reports must be checked for plagiarism using software such as Turnitin or any equivalent tool.
6. Evaluation should consider each student's individual contribution, understanding, and knowledge gained during the project execution. Marks should be awarded accordingly.

Term Work & Evaluation Scheme (Total 50 Marks):

Component	Marks
Performance in Experiments & Evaluation by Guide	15
Performance in mini project & Evaluation by Guide	10
Performance in mini project & Evaluation by Review Committee Evaluation	10
Logbook and Quality of Project Report	05
Research Paper	10

AC 29/6/2021
Item No. 6.5

UNIVERSITY OF MUMBAI

Bachelor of Engineering

in

Electronics and Telecommunication Engineering

Second Year with Effect from AY 2020-21

Third Year with Effect from AY 2021-22

Final Year with Effect from AY 2022-23

(REV- 2019 ‘C’ Scheme) from Academic Year 2019 – 20

Under

FACULTY OF SCIENCE & TECHNOLOGY

(As per AICTE guidelines with effect from the academic year 2019–2020)

AC 29/6/2021
Item No. 6.5

UNIVERSITY OF MUMBAI

Syllabus for Approval

Sr. No.	Heading	Particulars
1	Title of the Course	**Third Year in Bachelor of Electronics and Telecommunication Engineering**
2	Eligibility for Admission	**After Passing Second Year Engineering as per the Ordinance 0.6243**
3	Passing Marks	**40%**
4	Ordinances / Regulations (if any)	**Ordinance 0.6243**
5	No. of Years / Semesters	**8 semesters**
6	Level	~~P.G.~~ / **U.G.**/ ~~Diploma / Certificate~~ (Strike out which is not applicable)
7	Pattern	~~Yearly~~ / **Semester** (Strike out which is not applicable)
8	Status	~~New~~ / **Revised** (Strike out which is not applicable)
9	To be implemented from Academic Year	**With effect from Academic Year: 2021-2022**

Date **29-06-2021**

Dr. S. K. Ukarande
Associate Dean
Faculty of Science and Technology
University of Mumbai

Dr Anuradha Muzumdar
Dean
Faculty of Science and Technology
University of Mumbai

Preamble

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Science and Technology (in particular Engineering)of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth and approach of course to be taught, which will enhance learner's learning process. Choice based Credit and grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. Credit assignment for courses is based on 15 weeks teaching learning process, however content of courses is to be taught in 13 weeks and remaining 2 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

There was a concern that the earlier revised curriculum more focused on providing information and knowledge across various domains of the said program, which led to heavily loading of students in terms of direct contact hours. In this regard, faculty of science and technology resolved that to minimize the burden of contact hours, total credits of entire program will be of 171, wherein focus is not only on providing knowledge but also on building skills, attitude and self learning. Therefore in the present curriculum skill based laboratories and mini projects are made mandatory across all disciplines of engineering in second and third year of programs, which will definitely facilitate self learning of students. The overall credits and approach of curriculum proposed in the present revision is in line with AICTE model curriculum.

The present curriculum will be implemented for Second Year of Engineering from the academic year 2020-21. Subsequently this will be carried forward for Third Year and Final Year Engineering in the academic years 2021-22, 2022-23, respectively.

Dr. S. K. Ukarande
Associate Dean
Faculty of Science and Technology
University of Mumbai

Dr Anuradha Muzumdar
Dean
Faculty of Science and Technology
University of Mumbai

<u>Incorporation and Implementation of Online Contents from NPTEL/ Swayam Platform</u>

The curriculum revision is mainly focused on knowledge component, skill based activities and project based activities. Self learning opportunities are provided to learners. In the revision process this time in particular Revised syllabus of 'C ' scheme wherever possible additional resource links of platforms such as NPTEL, Swayam are appropriately provided. In an earlier revision of curriculum in the year 2012 and 2016 in Revised scheme 'A' and 'B' respectively, efforts were made to use online contents more appropriately as additional learning materials to enhance learning of students.

In the current revision based on the recommendation of AICTE model curriculum overall credits are reduced to 171, to provide opportunity of self learning to learner. Learners are now getting sufficient time for self learning either through online courses or additional projects for enhancing their knowledge and skill sets.

The Principals/ HoD's/ Faculties of all the institute are required to motivate and encourage learners to use additional online resources available on platforms such as NPTEL/ Swayam. Learners can be advised to take up online courses, on successful completion they are required to submit certification for the same. This will definitely help learners to facilitate their enhanced learning based on their interest.

Dr. S. K. Ukarande
Associate Dean
Faculty of Science and Technology
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Dr Anuradha Muzumdar
Dean
Faculty of Science and Technology
University of Mumbai

Preface By BoS

Technological developments in the field of electronics and telecommunication engineering have revolutionized the way people see the world today. Hence, there is a need for continuously enriching the quality of education by a regular revision in the curriculum, which will help our students achieve better employability, start-ups, and other avenues of higher studies. The current revision in the Bachelor of Engineering program (REV- 2019 'C' Scheme) aims at providing a strong foundation with required analytical concepts in the field of electronics and telecommunication engineering.

Some of the salient features of this revised curriculum are as below and they fall in line with the features in AICTE Model Curriculum.

1. The curriculum is designed in such a way that it encourages innovation and research as the total number of credits has been reduced from around 200 credits in an earlier curriculum to 171 credits in the current revision.
2. In the second and third-year curriculum, skill-based laboratories and mini-projects are introduced.
3. It will result in the students developing a problem-solving approach and will be able to meet the challenges of the future.
4. The University of Mumbai and BoS – Electronics and Telecommunication Engineering will ensure the revision of the curriculum on regular basis in the future as well and this update will certainly help students to achieve better employability; start-ups and other avenues for higher studies.

The BoS would like to thank all the subject experts, industry representatives, alumni, and various other stakeholders for their sincere efforts and valuable time in the preparation of course contents, reviewing the contents, giving valuable suggestions, and critically analyzing the contents.

Board of Studies in Electronics and Telecommunication Engineering

Dr. Faruk Kazi: Chairman

Dr. V. N. Pawar: Member

Dr. Ravindra Duche: Member

Dr. Milind Shah: Member

Dr. R. K. Kulkarni: Member

Dr. Baban U. Rindhe: Member

Dr. Mrs. Nair: Member

Dr. Nalbarwar: Member

Dr. Sudhakar Mande: Member

Dr. S. D. Deshmukh: Member

Program Structure for Third Year Engineering
Semester V & VI
UNIVERSITY OF MUMBAI
(With Effect from 2021-2022)

Semester V

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ECC501	Digital Communication	3	--	--	3	--	--	3
ECC502	Discrete Time Signal Processing	3	--	--	3	--	--	3
ECC503	Digital VLSI	3	--	--	3	--	--	3
ECC504	Random Signal Analysis	3	--	1	3	--	1	4
ECCDLO 501X	Department Optional Course-1	3	--	--	3	--	--	3
ECL501	Digital Communication Lab	--	2	--	--	1	--	1
ECL502	Discrete Time Signal Processing Lab	--	2	--	--	1	--	1
ECL503	Digital VLSI Lab	--	2	--	--	1	--	1
ECL504	Professional Communication & Ethics - II	--	2*+2~	--	--	2	--	2
ECM501	Mini Project 2A- Embedded System Project	--	4$	--	--	2	--	2
Total		**15**	**14**	**1**	**15**	**07**	**1**	**23**

*** Theory should be conducted for the full class.**
~ Batch-wise practical's to be conducted
$ Indicates work load of a learner (Not Faculty) for Mini Project 2A. Faculty Load: 1 hour per week per four groups.

Course Code	Course Name	Examination Scheme							
		Theory					Term Work	Pract. & oral	Total
		Internal Assessment			End Sem. Exam	Exam. Duration (in Hrs)			
		Test 1	Test 2	Avg.					
ECC501	Digital Communication	20	20	20	80	3	--	--	100
ECC502	Discrete Time Signal Processing	20	20	20	80	3	--	--	100
ECC503	Digital VLSI	20	20	20	80	3	--	--	100
ECC504	Random Signal Analysis	20	20	20	80	3	25	--	125
ECCDLO 501X	Department Level Optional Course-1	20	20	20	80	3	--	--	100
ECL501	Digital Communication Lab	--	--	--	--	--	25	25	50
ECL502	Discrete Time Signal Processing Lab	--	--	--	--	--	25	25	50
ECL503	Digital VLSI Lab	--	--	--	--	--	25	25	50
ECL504	Business Communication and Ethics Lab	--	--	--	--	--	25	25	50
ECM501	Mini Project 2A- Embedded System Project	--	--	--	--	--	25	25	50
Total		**--**	**--**	**100**	**400**	**--**	**150**	**125**	**775**

Department Level Optional Course-1

Course Code	Department Level Optional Course-1
ECCDLO5011	Digital and IPTV Engineering
ECCDLO5012	Data Compression and Cryptography
ECCDLO5013	IT Infra and Security
ECCDLO5014	Data Structures and Algorithm
ECCDLO5015	Sensor Technology

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC501	Digital communication	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC501	Digital communi-cation	20	20	20	80	03	--	--	100

Course Pre-requisite:

ECC401 - Engineering Mathematics-IV
ECC404 - Signals and Systems
ECC405 - Principles of Communication Engineering

Course Objectives:

1. To describe the basics of information theory and source coding.
2. To illustrate various error control codes.
3. To describe baseband system.
4. To learn different digital modulation and demodulation techniques

Course Outcomes:

After successful completion of the course student will be able to:

1. Apply the concepts of information theory in source coding.
2. Compare different error control systems and apply various error detection codes.
3. Analyze different error correction codes.
4. Compare various baseband transmission methods for digital signals.
5. Evaluate the performance of optimum baseband detection in the presence of white noise.
6. Compare the performances of different digital modulation techniques

Module No.	Unit No.	Topics	Hrs.
1.0		**Information Theory and Source Codes**	**05**
	1.1	Block diagram of digital communication system, Information content of a source symbol, Source entropy, Average information rate, AWGN channel, and Shannon-Hartley channel capacity theorem.	03
	1.2	Introduction of source code, Huffman code, Shannon-Fano code.	02
2.0		**Error Control System and Error Detection Codes**	**03**
	2.1	Introduction of error control system, Automatic Retransmission Query (ARQ) system, Types of ARQ systems and comparison, Forward error correction (FEC) system. Comparison between FEC and ARQ.	01
	2.2	Error detection codes: Vertical Redundancy Check (VRC) code, Longitudinal Redundancy Check (VRC) code, Cyclic Redundancy Check (CRC) code and Checksum code.	02
3.0		**Error Correction Codes**	**10**
	3.1	Linear block code: Code generation, calculation of minimum Hamming distance, error detection capability, error correction capability, implementation of encoder, error detection, syndrome table, error correction and implementation of decoder.	03
	3.2	Cyclic code: Code generation, calculation of minimum Hamming distance, error detection capability, error correction capability, implementation of encoder, error detection, syndrome table, error correction and implementation of decoder.	03
	3.3	Convolutional code: Generation, path responses, encoder, state transition table, state diagram, tree diagram, trellis diagram, decoding using Viterbi's algorithm.	04
4.0		**Baseband Transmission**	**05**
	4.1	Block diagram of baseband transmitter-receiver system, Line codes (RZ and NRZ UniPolar formats, RZ and NRZ Polar formats, NRZ Bipolar format (AMI format), NRZ Manchester format, and Quaternary Polar format). Comparison of line codes with respect to bandwidth, power requirement, synchronization capability, DC level, polarity inversion error and complexity. Power spectral density and spectrum of NRZ Unipolar and Polar formats.	03
	4.2	Inter Symbol Interference (ISI), Inter Channel Interference (ICI). Nyquist criterion for distortionless baseband binary transmission, Nyquist bandwidth and practical bandwidth.	02
5.0		**Optimum Detection of Baseband Signal**	**04**
	5.1	Matched filter, Output SNR, Transfer function, Impulse response and Error probability. Integrate and dump receiver, Correlator receiver.	04
6.0		**Digital Modulations**	**12**
	6.1	Generation, Detection, Error probability (using signal space representation and Euclidean distance), Bandwidth (using PSD and spectrum except for MSK) and applications of the following modulations: Binary ASK, Binary PSK, Quadrature PSK, Off-Set QPSK, M-ary PSK, Binary FSK, M-ary FSK, 16-ary QASK and MSK.	12
		Total	**39**

Text Books:

1.H. Taub, D. Schlling, and G. Saha-Principles of Communication Systems, Tata Mc- Graw Hill, New Delhi, Third Edition, 2012.
2. Lathi B P, and Ding Z-Modern Digital and Analog Communication Systems, Oxford University Press, Fourth Edition, 2017.
3. Haykin Simon-Digital Communications, John Wiley and Sons, New Delhi, Fourth Edition, 2014.
4. John G. Proakis-Digital Communications, McGraw-Hill, Fourth Edition

Reference Books:

1. Sklar B, and Ray P. K.-Digital Communication: Fundamentals and applications, Pearson,Dorling Kindersley (India), Delhi, Second Edition, 2009.
2. T L Singal-Analog and Digital Communication, Tata Mc-Graw Hill, New Delhi, First Edition,2012.
3. P Ramakrishna Rao-Digital Communication, Tata Mc-Graw Hill, New Delhi, First Edition,2011.
4. K. Sam Shanmugam-Digital and analog communication Systems, John Wiley and sons.
5. Upamanyu Madhow- Fundamentals of Digital Communication- Cambridge University Press
6. W.C. Huffman, Vera Pless- Fundamentals of Error Correcting Codes, Cambridge University Press
7. Graham Wade-Coding Techniques, Palgrave, New York

NPTEL / Swayam Course:

1. https://nptel.ac.in/courses/108/101/108101113/
2. https://nptel.ac.in/courses/108/102/108102096/
3. https://nptel.ac.in/courses/108/102/108102120/

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus completed and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC502	Discrete-Time Signal Processing	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC502	Discrete-Time Signal Processing	20	20	20	80	03	--	--	100

Course Pre-requisite:

ECC404 Signals & Systems

Course Objectives:

1. To develop a thorough understanding of discrete Fourier transform and its use in spectral analysis and frequency domain filter designing.
2. To design and realize IIR filters and FIR filters, gain an appreciation for the tradeoffs necessary in the filter design and to evaluate the effects of finite word lengths on the filters.
3. To introduce applications of digital signal processing in the field of biomedical and audio signal processing.

Course Outcomes:

After successful completion of the course student will be able to:

1. Recall the system representations and understand the relation between different transforms.
2. Understand the concepts of discrete-time Fourier transform, fast Fourier transform and apply in system analysis.
3. Design digital IIR and FIR filters to satisfy the given specifications and evaluate the frequency response and pole-zero representations to choose a particular filter for the given application.
4. Interpret the different realization structures of Digital IIR and FIR filters.
5. Analyze the impact of hardware limitations on the performance of digital filters.
6. Apply signal processing concepts, algorithms in applications related to the field of biomedical and audio signal processing.

Module No.	Unit No.	Topics	Hrs.
1.0		**Discrete Fourier Transform & Fast Fourier Transform**	**08**
	1.1	Discrete Fourier transform (DFT), DFT as a linear transformation, Properties of the DFT, Relationship of the DFT to other transforms, Filtering of long data sequences: Overlap-Save and Overlap-Add Method	05
	1.2	Fast Fourier Transform: Radix-2 Fast Fourier Transforms (FFT), Radix-2 decimation in time and decimation in frequency FFT algorithms, Inverse FFT	03
2.0		**IIR Digital filters**	**08**
	2.1	LTI systems as frequency-selective filters like low pass, high pass, band pass, notch, comb, all-pass filters, and digital resonators, Analog filter approximations: Butterworth, Chebyshev I, Elliptic	03
	2.2	Mapping from s-plane to the z-plane - impulse invariant and bilinear transformation, Design of IIR digital filters (Butterworth and Chebyshev-I) from analog filters using impulse invariant and bilinear transformation techniques, Analog and digital frequency transformations	05
3.0		**FIR Digital Filters**	**09**
	3.1	Characteristics of linear phase FIR digital filters, Symmetric and antisymmetric FIR filter, Location of the zeros of linear phase FIR filters, Minimum, maximum and mixed phase systems	04
	3.2	Design of FIR filters using Window techniques (Rectangular, Hamming, Hanning, Blackman, Bartlett), Design of FIR filters using Frequency Sampling Technique – Type I low pass filter design, Comparison of IIR and FIR filters	05
4.0		**Digital Filter Structures**	**05**
	4.1	Realization structures for FIR systems: Cascade form, Frequency sampling structure, Lattice structure, Computational complexities for N length filter	02
	4.2	Realization structures for IIR systems: Cascade form and parallel form structures, Lattice Ladder structure, Computational complexities for N order filter	03
5.0		**Finite Word Length Effects in Digital Filters**	**05**
	4.1	Rounding and truncation errors, Quantization error, Output noise power from a digital system	02
	4.2	Product quantization, Noise model for direct form and cascaded IIR structure (first order), Coefficient quantization error and zero input limit cycle	03
6.0		**Applications of Digital Signal Processing**	**04**
	6.1	Application of DSP for ECG and EEG signals analysis.	02
	6.2	Application of DSP for echo cancellation and sub-band coding of speech signal	02
		Total	**39**

Text Books:

1. Proakis J., Manolakis D., "*Digital Signal Processing*", 4th Edition, Pearson Education.
2. Emmanuel C. Ifeachor, Barrie W. Jervis," Digital Signal Processing", A Practical Approach", Pearson Education
3. A Nagoor Kani "Digital Signal Processing", 2nd Edition. Tata Mc Graw Hill Education Private Limited

Reference books

1. Sanjit K. Mitra, "Digital Signal Processing – A Computer Based Approach ", 4th Edition McGraw Hill Education (India) Private Limited, 2013
2. Oppenheim A., Schafer R., Buck J., "Discrete Time Signal Processing", 2nd Edition, Pearson Education, 3rd Edition, 2010
3. L. R. Rabiner and B. Gold, "Theory and Applications of Digital Signal Processing", Prentice-Hall of India, 2006.
4. S Salivahan, C Gnanapriya, "Digital Signal Processing", Mc Graw Hill Education (India) limited, 4th Edition, 2015
5. Monson H Hayes, "Digital Signal Processing", Schaum's Outline Series, 2nd Edition, 2011
6. Rangaraj M. Rangayyan, "Biomedical Signal Analysis- A Case Study Approach",Wiley 2002.

NPTEL / Swayam Course:

1. Course: Digital Signal Processing By Prof. S.C Dutta Roy, IIT Delhi
   http://www.nptelvideos.in/2012/12/digital-signal-processing.html
2. Course: Digital Signal Processing By Prof. V. M. Gadre , IIT Bombay
   https://nptel.ac.in/courses/108/101/108101174/
3. Course: Digital Signal Processing By Prof. T. K. Basu , IIT Kharagpur
   https://nptel.ac.in/courses/108/105/108105055/

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC503	Digital VLSI	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC503	Digital VLSI	20	20	20	80	03	--	--	100

Course Pre-requisite:

ECC302 – Electronic Devices and Circuits
ECC303 – Digital System Design
ECC403 – Linear Integrated Circuits

Course Objectives:

1. To introduce process flow of VLSI Design.
2. To understand MOSFET operation from VLSI design perspective.
3. To learn VLSI design performance metric and various tradeoffs.
4. To design, implement and verify combinational and sequential logic circuits using various MOS design styles.
5. To provides an exposure to RTL design and programming

Course Outcomes:

After successful completion of the course student will be able to:

1. Know various tools and processes used in VLSI Design.
2. Explain working of various CMOS combinational and sequential circuits used in VLSI Design.
3. Derive expressions for performance parameters of basic building blocks like CMOS inverter.
4. Relate performance parameters with design parameters of VLSI circuits.
5. Select suitable circuit and design style for given application.
6. Design and realize various combinational and sequential circuits for given specifications.

Module No.	Unit No.	Topics	Hrs.
1.0		**Review of MOSFET operation and Fabrication**	**08**
	1.1	Overview of VLSI Design Flow, Review of MOSFET operation, MOSFET Capacitances, MOSFET scaling, Short channel effects	03
	1.2	Fabrication process flow of NMOS and CMOS, Lambda based design rules	03
	1.3	Novel MOSFET Architectures FinFET, GAA-FET, CNTFET	02
2.0		**Combinational CMOS Logic Circuits**	**06**
	2.1	CMOS inverter operation, Voltage Transfer characteristics (VTC), Noise Margins, Propagation Delay, Power Dissipation, Design of CMOS Inverter, Layout of CMOS Inverter	03
	2.2	Realization of CMOS NAND gate, NOR gate, Complex CMOS Logic Circuits, Layout of CMOS NAND, NOR and complex CMOS circuits	03
3.0		**MOS Design Logic Styles**	**09**
	3.1	Static CMOS, Pass Transistor Logic, Transmission Gate, Pseudo NMOS, Dynamic Logic, Domino Logic, NORA, Zipper, C^2MOS	04
	3.2	Setup time, Hold time, clocked CMOS SR Latch, CMOS JK Latch, MS –JK Flip Flop, Edge triggered D-Flip Flop and realization using design styles	03
	3.3	Realization of Shift Register, MUX, Decoder using above design styles ,1-bit full adder	02
4.0		**Semiconductor Memories**	**06**
	4.1	ROM array, 6T-SRAM (operation, design strategy, leakage currents, sense amplifier), layout of SRAM	03
	4.2	Operation of 1T and 3T DRAM Cell, NAND and NOR flash memory	03
5.0		**Data path and system design issues**	**06**
	5.1	Ripple carry adder, CLA adder, carry save adder, carry select adder, carry skip adder, Array Multiplier	04
	5.2	On chip clock generation and distribution, Interconnect delay model, interconnect scaling and crosstalk	02
6.0		**RTL Design**	**04**
	6.1	High Level state machines, RTL design process	02
	6.2	RTL design of Soda dispenser machine, FIR Filter	02
		Total	**39**

Text Books:

1. Sung-Mo Kang and Yusuf Leblebici, "*CMOS Digital Integrated Circuits Analysis and Design*", Tata McGraw Hill, 3rd Edition, 2012.
2. Jan M. Rabaey, Anantha Chandrakasan and Borivoje Nikolic, "*Digital Integrated Circuits: A Design Perspective*", Pearson Education, 2nd Edition.
3. Frank Vahid, "Digital Design with RTL design, VHDL and VERILOG", John Wiley and Sons Publisher 2011.

Reference Books:

1. Neil H. E. Weste, David Harris and Ayan Banerjee, ―*CMOS VLSI Design: A Circuits and Systems Perspective*‖, Pearson Education, 3rd Edition.
2. John P. Uyemura, "*Introduction to VLSI Circuits and Systems*", Wiley, Student Edition, 2013.
3. R. Jacob Baker, "*CMOS Circuit Design, Layout and Simulation*", Wiley, 2nd Edition, 2013

NPTEL / Swayam Course:

1. https://nptel.ac.in/courses/117/101/117101058/
2. https://nptel.ac.in/courses/108/107/108107129/

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on completion of approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC504	Random Signal Analysis	03	--	01	03	--	01	04

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC504	Random Signal Analysis	20	20	20	80	03	25	--	125

Course Pre-requisite:

ECC401- Engineering Mathematics IV
ECC404 - Signals and Systems

Course Objectives:

1. To strengthen the foundations of probability
2. To teach continuous and discrete random variables.
3. To explain statistical behavior of one dimensional and two dimensional random variables.
4. To describe the concept of random process which is essential for random signals and systems encountered in Communications and statistical learning.
5. To develop problem solving skills and explain how to make the transition from a real world problem to a probabilistic model.

Course Outcomes:

After successful completion of the course student will be able to:

1. Apply theory of probability in identifying and solving relevant problems.
2. Differentiate continuous and discrete random variables and their distributions.
3. Analyze mean, variance, and distribution function of random variables and functions of random variables.
4. Define a random process, determine the type of the process and find the response of LTI system for WSS process.
5. Explain linear regression algorithms and apply for predictive applications.

Module No.	Unit No.	Topics	Hrs.
1.0		**Basic Concepts in Probability**	**04**
	1.1	Definitions of probability, joint, conditional, and total probability, Bayes' theorem, independence of events, binary symmetric communication channel analysis using Bayes' theorem.	
2.0		**Introduction to Random Variables**	**08**
	2.1	Continuous, discrete, and mixed random variables, probability density function, probability distribution function, and probability mass function, properties of PDF and CDF	
	2.2	Special distributions- Binomial, Poisson, Uniform, Gaussian and Rayleigh Distributions Mean, variance and moments of random variables	
3.0		**Operations on One Random Variable**	**08**
	3.1	Function of a random variable and their distribution and density functions.	
	3.2	Expectation, variance, moments, and characteristic function of random variable.	
	3.3	Transformation of a random variable, Markov and Chebyshev inequality, characteristic functions, moment theorem.	
4.0		**Multiple Random Variables and Convergence**	**08**
	4.1	Pairs of random variables, joint CDF and joint PDF.	
	4.2	One function of two random variables; joint moments, covariance and correlation-independent, uncorrelated and orthogonal random variables.	
	4.3	Central limit theorem and its significance	
5.0		**Random Processes**	**06**
	5.1	Definitions, statistics of stochastic processes, n^{th} order distribution, second-order properties: mean and autocorrelation, Poisson process, normal processes, SSS, WSS.	
	5.2	Mean and correlation ergodic processes, transmission of WSS through LTI system, introduction to Markov process.	
6.0		**Introduction to Statistical Learning and Applications**	**05**
	6.1	Regression and model building, simple linear regression, multiple linear regression, least square estimation of the coefficients, residual calculations.	
	6.2	Applications of simple linear regression in prediction of new observations.	
		Total	**39**

Text Books:

1. T. Veerarajan, "Probability, Statistics and Random Process", Tata McGraw Hill Education, Third Edition (2018).
2. Athanasios Papoulis and S. Unnikrishna Pillai, "Probability, Random Variables, and Stochastic Processes", Tata McGraw Hill Education
3. Henry Stark & John Woods, "Probability, Statistics, and Random Processes for Engineers, 4th Edition, Pearson Education, 2012

4. Douglas C. Montgomery, Elizabeth A. Peck and G. Geoffrey Vining, “Introduction to linear regression Analysis”, student edition, Wiley publications.

Reference Books

1. Scott Miller and Donald Childers, “Probability and Random Processes with Applications to Signal Processing and Communications”, Elsevier Publication.
2. Hwei Hsu, “Theory and Problems of Probability, Random Variables, and Random Processes”, Schaum’s Outline Series, McGraw Hill, 1997.
3. P. Ramesh Babu, “Probability Theory and Random Process”, Tata McGraw Hill Education.
4. Alberto Leon Garcia, “Probability and Random Processes for Electrical Engineering”, second edition , Pearson education.
5. Daniela Witten, Trevor Hastie , Robert Tibshirani, “An Introduction to Statistical Learning by Gareth James”, 7th Edition, Springer 2017.
6. Ronald Walpole, et. al., “Probability and Statistics for Engineers and Scientists”, 8th edition, Pearson Education.
7. P. Kousalya, “Probability, Statistics, and Random Processes”, Pearson Education.

NPTEL / Swayam Course:

1. Introduction to probability and Statistics, Prof. G. Srinivasan (IIT Madras); https://onlinecourses.nptel.ac.in/noc21_ma01/preview
2. Probability and Probability Distributions By Dr. P.Nagesh: https://onlinecourses.swayam2.ac.in/cec21_ma02/preview

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus completed and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Term Work (25-Marks):

At least 08 Tutorials covering entire syllabus must be given during the “Class Wise Tutorial”. Term work assessment must be based on the overall performance of the student with every tutorial graded from time to time. The grades will be converted to marks as per “Credit and Grading System” manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 5011	Digital and IP TV Engineering	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term work	Practical and Oral	Total
		Internal assessment			End Sem. Exam				
		Test1	Test 2	Avg.					
ECCDLO 5011	**Digital and IP TV Engineering**	20	20	20	80	03	--	--	100

Prerequisite:

1. Basics of various Television standards and operation
2. TCP/IP Protocol
3. Basics of conventional video camera and standards

Course Objectives:

1. To provide in depth knowledge about Digital Television system
2. To familiarize students' various types of advanced types of Video cameras and Displays
3. To introduce the students to different television standards and applications
4. Acquaintance with HDTV and 3D TV system
5. To familiarize the students to IPTV, Its architecture, Protocols and hardware
6. To Introduce students to IP delivery networks, threats and mitigation

Course Outcomes:

After successful completion of the course student will be able to:

1) Understand the working principles of advanced digital television systems.
2) Enable to choose or develop an appropriate camcorder and displays based on applications.
3) Familiar with current digital TV standards.
4) Evaluate the Stereoscopic images and binocular depth perception.
5) Acquire knowledge of IPTV and develop hardware and protocols.
6) Ability to provide customized IPTV services to end user.

Module No	Unit No	Topics	Hrs
1		**Fundamentals of Digital Television**	**7**
	1.1	Fundamentals of colour television, Compatibility, and reverse compatibility, colour perception, Three colour theory, luminance, hue and saturation. Interlaced scanning, Composite video signal	
	1.1	Introduction to Digital TV, Digital TV signals and parameters	
	1.2	Digital TV transmitter and Receiver its merits and demerits	
	1.3	MAC Signals and advanced MAC Signal Transmission	
	1.4	Digitization, Chroma sub sampling, Digital audio compression techniques and video compression techniques MPEG1,MPEG2,H.264,MPEG- 4,AVC,H.265, SMPTE 421M,	
	1.5	Set Top Box with recording	
2		**Digital Video Cameras, Displays and Streaming media device**	**5**
	2.1	Colour TV Digital cameras, Camcorders, Handycams, and Digicams	
	2.2	LED, LCD, OLED, PLASMA, Quantum Dot LED Displays	
	2.3	Chromecast	
	2.4	Consumer applications: DVD, Blue ray DVD	
3		**Digital TV standards and advanced TV**	**8**
	3.1	DVB-T, and its successors	
	3.2	ISDB -T	
	3.3	ATSC	
	3.4	ISD TV	
	3.5	DTMB	
	3.6	Ultra HDTV	
	3.7	CCTV	
	3.8	Direct to Home TV(DTH)	
	3.9	Smart TV and its functions	
	3.10	3D TV	
4		**IPTV**	**6**
	4.1	Introduction to IPTV	
	4.2	IP TV hardware	
	4.3	Features of IPTV	
	4.4	Architecture of IPTV	
	4.5	Bandwidth requirement	
	4.6	IPTV Set top Box, Smart TV comparison	
5		**IP TV Protocols and Applications**	**9**
	5.1	Internet Group Management Protocol (IGMP)	
	5.2	Real-Time Streaming Protocol (**RTSP**)	
	5.3	Real-Time Messaging Protocol (**RTMP**)	
	5.4	Hypertext Transfer Protocol (HTTP).	
	5.5	Applications of IPTV	

	5.6	IPTV Delivery: Broad cast. Unicast, Multicast	
	5.7	IPTV Streaming: Time Shifted Stream-On -the- fly streaming	
	5.8	experimental framework used for evaluating the classification algorithm	
	5.9	Experimental framework for evaluating the classification algorithm (Self learning) Configuring IPTV to android phone, Tablet, Television and Computer(Self Learning)	
6		**IPTV Network Security: Threats and Countermeasures**	**4**
	6.1	Threats on IPTV Delivery Networks, Theft or Abuse of Network Assets, Theft of Service, Theft of IPTV-Related Data, Disruption of Service, Privacy Breach, Compromise of Platform Integrity	
	6.2	Security Issues of IPTV Delivery Networks: Protocols Vulnerabilities, Countering the threats	
	6.3	Advantages and disadvantages of IPTV	
	6.4	Future of IPTV	
		Total	**39**

Textbooks:

1.Television and video Engineering, A. M. Dhake, Tata McGraw Hill Publication.

2. Video Demystified, Kelth jack, Hand book for digital engineers, Newness, Elsevier

3.Digital Television Systems. Marcelo S. Alencar, Cambridge University Press

4.Understanding IPTV, Gilbert Held, CRC Press

Reference Books:

1.The digital evolution of Television, D. Gerbarg, Springer

2. Applications and Usability of interactive TV, Maris Jos Abisolo, Springer

3. IPTV Delivery network, Suliman Mohamed Fati, Saiful Azad, Al-Sakib Khan Pathan, Wiley Publications

4. Television Engineering & Video Systems, R. G. Gupta, McGraw Hill Publication

5. Quantum dot based light emitting diodes, Morteza Sasani Ghamsari, Google book

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus completed and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDL O5012	Data Compression and Cryptography	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam. Duration (in Hrs)	Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. Of Test 1 and Test 2					
ECCDL O5012	Data Compression and Cryptography	20	20	20	80	03	--	--	100

Course Objectives:

1. Gain a fundamental understanding of data compression methods for text, images, video and audio.
2. Understand the concepts of cryptography and different algorithms to provide system security.

Course Outcomes:

After successful completion of the course student will be able to

1. Apply various compression techniques for text and understand image compression and its standards.
2. Select suitable compression techniques for specified lossless and lossy audio and video applications.
3. Compare between symmetric and asymmetric cryptography and also describe different symmetric cryptographic techniques and standards.
4. Apply number theory concepts to solve the cryptographic problems.
5. Analyze different public key cryptography algorithms and also describe methods that provide the goals for integrity, confidentiality and authentication.
6. Describe system security facilities designed to protect a computer system from security threats and also appreciate ethical issues related to system security.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Data Compression**	**06**
	1.1	Data compression, modelling and coding, Lossless and Lossy Compression, Arithmetic Coding – Decoding, Dictionary Based Compression, Sliding Window Compression: LZ-77, LZ-78, LZW.	
	1.2	Image Compression DCT, JPEG, JPEG – LS, Differential Lossless Compression, DPCM, JPEG – 2000 Standards.	
2.0		**Video and Audio Compression**	**06**
	2.1	Video compression: Motion compensation, temporal and spatial prediction, MPEG-4, H.264 encoder and decoder.	
	2.2	Sound, Digital Audio, μ-Law and A-Law Companding, MPEG –4 Audio Layer, Advanced Audio Coding (AAC) standard.	
3.0		**Data Security**	**10**
	3.1	Security Goals, Cryptographic Attacks and Techniques	
	3.2	Symmetric Key: Substitution Cipher, Transposition Cipher , Stream and Block Cipher	
	3.3	DES, double DES and triple DES, AES	
4.0		**Number Theory**	**04**
	4.1	Prime Numbers, Fermat's and Euler's Theorem.	
	4.2	Chinese Remainder Theorem	
5.0		**Asymmetric Key Cryptography**	**09**
	5.1	Principles of Public Key Crypto System, RSA, Key Management, Deffie-Hellman Key Exchange.	
	5.2	Message Integrity, Message Authentication and Hash Functions, SHA, HMAC, Digital Signature Standards.	
6.0		**System Security**	**04**
	6.1	Intrusion Detection System, Secure Electronic Transactions.	
	6.2	Firewall Design, Digital Immune systems, Biometric Authentication, Ethical Hacking.	
		Total	**39**

Textbooks:

1. Khalid Sayood , 3rd Edition, |Introduction to Data Compression|, Morgan Kauffman
2. Mark Nelson, Jean-Loup Gailly,‖The Data Compression Book‖, 2nd edition, BPB Publications
3. William Stallings ,|Cryptography and Network Security Principles and Practices 5th Edition‖, Pearson Education.
4. Behrouz A. Forouzan, |Cryptography and Network Security‖, Tata McGraw-Hill.

Reference Books:

1. 1. David Salomon, |Data Compression: The Complete Reference‖, Springer.
2. Matt Bishop, |Computer Security Art and Science‖, Addison-Wesley.
3. Bernard Menesez,| Network Security and Cryptography| Delmar Cengage Learning, 7th Edition.

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus completed and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 5013	IT Infra & Security	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 5013	IT Infra & Security	20	20	20	80	03	--	--	100

Course prerequisite:

- **Principles of Communication**

Course Objectives:

1. **To introduce basic fundamentals of IT Infrastructure and its Management.**
2. **To develop underlying principles of infrastructure security.**
3. **To explore software vulnerabilities and attacks.**
4. **To introduce the protection mechanisms for operating systems and database security.**
5. **To explore the security aspects of wireless network infrastructure and protocols.**
6. **To investigate the different attacks on Web Applications and Web services.**

Course Outcomes: Students will be able to:

1. **Understand IT Infrastructure and its Management.**
2. **Understand the concept of Information securities.**
3. **Summarize the concepts of vulnerabilities, attacks and protection mechanisms.**
4. **Analyze software vulnerabilities and attacks on databases and operating systems.**
5. **Explain the need for security protocols in the context of wireless communication.**
6. **Analyze the different attacks on Open Web Applications and Web services.**

Module No.	Unit No.	Topics	Hrs
1.0		**Overview of Networks and IT Infrastructure**	**09**
	1.1	**Overview of OSI and TCP/ IP Networks, introduction to IP Addressing scheme, introduction to Networking Components**	
	1.2	**Information Technology, Design Issues of IT Organizations and IT Infrastructure, Information System Design Process, IT Infrastructure Management, Challenges in IT Infrastructure Management, Determining Customers, Requirements, Security controls and safeguards, IT security Plans.**	
2.0		**Introduction to Information Security**	**06**
		Cyber-attacks, Vulnerabilities, Defense Strategies and Techniques, Authentication Methods- Password, Token and Biometric, Access Control Policies and Models (DAC, MAC, RBAC, BIBA, Bell La Padula), Authentication and Access Control Services- RADIUS, TACACS, and TACACS+	
3.0		**Software Vulnerabilities**	**04**
		Buffer overflow, Format String, Cross-Site Scripting, SQL Injection, Malware: Viruses, Worms, Trojans, Logic Bomb, Bots, Rootkits	
4.0		**Operating System and Database Security**	**08**
	4.1	**Introduction operating system security, system security planning, Application security, Linux/ Unix security, Windows, security, Security Maintenance,**	
	4.2	**Database Security Requirements, Reliability and Integrity, Sensitive Data, Inference Attacks, Multilevel Database Security**	
5.0		**Wireless Security**	**05**
		The need for Wireless Network Security, Attacks on Wireless Networks, Security services, WEP & WPA protocols, Mobile IP, Virtual Private Network (VPN): PPTP, L2TP, IPSec	
6.0		**Web Security**	**07**
		Introduction: Transport Protocol and Data Formats, Web Browser, Threat Model **Authenticated Sessions: Cookie Poisoning, Cookies and Privacy, Making Ends Meet** **Code Origin Policies, Cross-Site Scripting: Cookie Stealing, Defending against XSS, Cross-Site Request Forgery, JavaScript Hijacking**	
		Total	**39**

Text Books:

1. **Gupta, "IT Infrastructure & Its Management", First Edition, Tata McGraw-Hill Education.**
2. **Computer Security Principles and Practice, William Stallings, Sixth Edition, Pearson Education**
3. **Computer Security, Dieter Gollmann, Third Edition, Wiley Publications.**
4. **Data Communications and Networking, Forouzan, Fourth Edition, Mc Graw Hill Publication**
5. **Wireless Networks, P. Nicopolitidis, M.S. Obaidat, G.I Papadimitriou, A.S Pomportsis, Wiley Publications**

Reference Books:

1. **Security in Computing, Charles P. Pfleeger, Fifth Edition, Pearson Education**
2. **CCNA Security Study Guide, Tim Boyle, Wiley Publications**
3. **Introduction to Computer Security, Matt Bishop, Pearson.**

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 5014	Data Structure & Algorithm	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 5014	Data Structure & Algorithm	20	20	20	80	03	--	--	100

Course pre-requisite:

ECL404 Skill Lab: Python Programming

Course Objectives:

The course aims:

1. To Introduce the fundamental knowledge & need of Data Structures.
2. To Abstract the concept of Algorithm and these concepts are useful in problem solving.
3. To Implement fundamental knowledge and applications of Stack, Queue, Linked List, Trees, Graphs etc.
4. To Understand the working of different Sorting, Searching & Hashing techniques.
5. To understanding about writing algorithms and step by step approach in solving problems with the help of fundamental data structures.

Course Outcome:

After successful completion of the course the student will: -

1. Compare functions using asymptotic analysis and describe the relative merits of worst-, average-, and best-case analysis.
2. Apply various operations on Stack and Queue.
3. Ability to demonstrate the operation of Linked list.
4. Ability to demonstrate and apply Trees & Graph data structures.
5. Become familiar with various Sorting and Searching Algorithms and their performance characteristics.
6. Describe the hash function and concepts of collision and its resolution methods

Module No.	Unit No.	Topics	Hrs.
		Prerequisite: Control Structures, Arrays, Recursion, Pointers, Structures, Memory Allocation Techniques, Self-referential structures.	
1.0		**Introduction to Data Structure & Algorithm**	**5**
	1.1	Introduction to Data Structures, Concept of ADT, Types of Data Structures-Linear and Nonlinear, Operations on Data Structures.	
	1.2	Algorithm: Performance characteristics of algorithm, Importance of Algorithm Analysis, Complexity of an Algorithm, Introduction to Asymptotic Analysis and Notations.	
2.0		**Stack & Queue**	**8**
	2.1	Introduction to Stack, ADT of Stack, Operations on Stack, Array Implementation of Stack	
	2.2	Applications of Stack- Infix to Postfix Expression Conversion, Infix Expression to Prefix Expression Conversion, Postfix Expression Evaluation	
	2.3	Introduction to Queue, ADT of Queue, Operations on Queue, Array Implementation of Queue, Types of Queue-Circular Queue, Priority Queue, Introduction to Double Ended Queue	
	2.4	Applications of various types of Queue	
		Self-Learning Topic: Well form-ness of Parenthesis using Stack	
3.0		**Linked List**	**7**
	3.1	Introduction, Linked List v/s Array, Representation of Linked List, Types of Linked List - Singly Linked List, Doubly Linked List	
	3.2	Operations on Singly Linked List and Doubly Linked List	
	3.3	Singly Linked List Application-Polynomial Representation and Addition, Doubly Linked List Application	
		Self-Learning Topic: Stack and Queue using Singly Linked List	
4.0		**Trees & Graph**	**9**
	4.1	Introduction, Tree Terminologies, Binary Tree, Binary Tree Representation, Types of Binary Tree, Binary Tree Traversals, Binary Search Tree, Operations on Binary Search Tree,	
	4.2	Applications of Binary Tree- Expression Tree, Huffman Encoding.	
	4.3	**Graph:** Introduction, Graph Terminology, Memory Representation of Graph, Operations Performed on Graph.	
	4.4	Graph Traversal, Breadth First Search, Depth First Search, Applications of the Graph, Shortest Path, Minimum Spanning Tree.	
5.0		**Searching & Sorting**	**6**
	5.1	**Searching:** Sequential Search, Index Sequential Search, Binary Search	
	5.2	**Sorting:** Bubble Sort, Quick Sort, Merge Sort	
		Self-Learning Topic: Selection Sort, Insertion Sort	
6.0		**Hashing**	**4**
	6.1	Hashing-Concept, Hash Functions, Common hashing functions	
	6.2	Collision resolution Techniques	
		Total	**39**

Text Books:

1. Jean Paul Tremblay, P. G. Sorenson, "Introduction to Data Structure and its Applications", McGraw-Hill Higher Education

2. "Fundamentals of Computer Algorithms" Ellis Horowitz, Sartaj Sahani and Sanguthevar Rajasekaran, Second Edition, Universities Press (India) Pvt. Ltd.

3. "Learning with Python" Allen Downey, Jeffrey Elkner, Chris Meyers, Dreamtech Press

Reference Books:

1. Jean Paul Tremblay, Paul G. Sorenson; An introduction to data structures with applications; Tata McGrawHill; 1984

2. Sanjoy Dasgupta, Christos Papadimitriou, Umesh Vazirani, "Algorithms", Tata McGrawHill Edition.

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus completed and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 5015	Sensor Technology	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Exam Duration (in Hrs.)	Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ECCDLO 5015	Sensor Technology	20	20	20	80	03	--	--	100

Course Pre-requisite:
1. FEC202 – Engineering Physics-II
2. ECC302 -- Electronic Devices & Circuits
3. ECC403 - Linear Integrated Circuits

Course Objectives:
1. To understand various physical parameters and its sensing techniques
2. To familiarize about MEMS sensors and Actuators
3. To introduce wireless sensing technologies
4. To develop understanding about signal conditioning using ADC and DAC
5. To provide insight into various sensor applications

Course Outcome:
After successful completion of the course student will be able to

1. Understand the transduction principal of various sensors.
2. Select sensors suitable for required application
3. Analyze wireless sensing techniques
4. Design the data acquisition system
5. Identify signal conditioning method for particular application
6. Create an application using various sensor technologies

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction**	**03**
	1.1	**Classification of Sensors :** The sensors are classified with criteria like primary physical quantity to be sensed , transduction principle, material and technology used and application	
	1.2	**Criteria to choose a Sensor:** Accuracy, Environmental condition, Range, Calibration, Resolution , Cost and Repeatability	
	1.3	**Digital sensors** : Principle and its advantage over analog sensors	
	1.4	**Smart Sensors:** Low-power, Self –diagnostic and Self- calibration	
2.0		**Types of Sensors**	**09**
	2.1	**Temperature Sensors :** RTD, Thermocouple and Thermistors sensor	
	2.2	**Proximity Sensors :** Inductive (LVDT), Capacitive, Photoelectric and Ultrasonic sensors	
	2.3	**Chemical Sensors :** Gas , Smoke, Conductivity and pH sensor	
	2.4	**Other Sensors :** Optical, Infrared (IR), Sound, Motion , Pressure , Level , Moisture, Humidity, Laser , Image and GPS sensor	
3.0		**MEMS Sensors and Actuators**	**06**
	3.1	**MEMS SENSORS:** General design methodology, techniques for sensing, Pressure sensor , Mass Flow sensor , Acceleration sensor , Angular Rate sensor and Gyroscopes, Micro machined microphones, Chemical sensors, Taguchi Gas sensor, Combustible Gas sensors	
	3.2	**MEMS ACTUATORS:** Techniques for actuation, Digital Micro mirror Device, Micro Machined Valves	
4.0		**Wireless Sensing Technologies**	**05**
	4.1	**Bluetooth:** Concepts of Pico net, Scatter net, Link types, Network connection establishments	
	4.2	**ZigBee:** components, architecture, network topologies	
	4.3	**Ultra Wide Band (UWB), Near Field Communication (NFC) and RFID:** technical requirements, components and characteristics	
	4.4	**WLAN (WiFi) :** WLAN Equipment, WLAN topologies , IEEE 802.11 Architecture	
5.0		**Data Acquisition and Signal Conditioning**	**08**
	5.1	**Fundamentals of Data Acquisition:** Analog and Digital data acquisition system with different configurations, Data loggers, Noise and interference	
	5.2	**Signal Conditioning :** Wheatstone Bridge, Flash ADC, R2R DAC	
	5.3	Utilization of Signal conditioning circuits for Temperature, Pressure, Optical, Strain gauges, Displacement and piezoelectric Transducers	
6.0		**Sensor Applications**	**08**
	6.1	Onboard Automobile sensing system, Home appliances sensors, Aerospace Sensors, Sensors for Environmental Monitoring, Biomedical Sensing Applications	
	6.2	Radio sensors for industrial applications, Radio Astronomy, Remote Sensing, Ground Penetrating Radars, Underwater sensing, LIDAR	
		Total	**39**

Textbooks:

1. D.V.S. Murthy, "Transducers and Instrumentation", PHI Learning, 2nd Edition, 2013.
2. D. Patranabis – Sensor and Transducers (2e) Prentice Hall, New Delhi, 2003
3. Antti V. Raisanen, Arto Lehto, "Radio Engineering for Wireless Communication and Sensor Applications", Artech House mobile communications series, USA, 2003.

4. Sensors and Signal Conditioning, Ramon Pallas Areny, John G. Webster, 2nd edition, John Wiley and Sons, 2000.
5. Vijay K. Garg, “Wireless Communication and Networking”, Morgan -Kaufmann Series in Networking, Elsevier, 2010.

Reference Books:

1. An Introduction to Microelectromechanical Systems Engineering, Nadim Maluf, Kirt Williams, Artech House, 2004.
2. Micro Electro Mechanical System Design, James J. Allen, Taylor and Francis, 2005
3. A.K. Sawhney, “A Course in Electrical and ElectronicMeasurements and Instrumentation”, Dhanpatrai & Co., 19th Edition, 2011.
4. Nathan Ida, “Sensors, Actuators and their Interfaces: A Multidisciplinary Introduction”, Second Edition, IET Control, Robotics and Sensors Series 127, 2020
5. Instrumentation Devices and System, C.S. Rangan, G.R. Sarma, V.S. Mani, TMH,1997.
6. Jacob Fraden Handbook of Modern Sensors Physics, Designs, and Applications Fourth Edition, Springer, 2010.

NPTEL / Swayam Course :
https://nptel.ac.in/courses/108/108/108108147/ https://www.youtube.com/watch?v=vjhp0zTXEsc

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus completed and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL501	Digital Communication Lab	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam.			
		Test 1	Test 2	Avg.				
ECL501	Digital communication Lab	--	--	--	--	25	25	50

Course objectives:

1. To learn source coding and error control coding techniques
2. To compare different line coding methods
3. To distinguish various digital modulations
4. To use different simulation tools for digital communication applications

Course outcomes:

After the successful completion of the course student will be able to

1. Compare various source coding schemes
2. Design and implement different error detection codes
3. Design and implement different error correction codes
4. Compare various line coding techniques
5. Illustrate the impulse response of a matched filter for optimum detection
6. Demonstrate various digital modulation techniques

Suggested list of experiments: (Course teacher can design their own experiments based on the prescribed syllabus)

1. Huffman code generation
2. Shannon-Fano code generation
3. Vertical redundancy Check (VRC) code generation and error detection
4. Horizontal Redundancy Check (HRC) code generation and error detection
5. Cyclic redundancy Check (CRC) code generation and error detection
6. Checksum code generation and error detection
7. Compare the performances of HRC and Checksum
8. Linear block code generation and error detection
9. Error detection and correction using Hamming code virtual lab http://vlabs.iitb.ac.in/vlabs-dev/labs/mit_bootcamp/comp_networks_sm/labs/exp1/index.php
10. Cyclic code generation and error detection
11. Convolutional code generation

12. Line Codes generation and performance comparison
13. Spectrum of line codes (NRZ unipolar and polar)
14. Impulse responses of ideal (Nyquist filter) and practical (Raised cosine filter) solution for zero ISI
15. Matched filter impulse response for a given input
16. Generation (and detection) of Binary ASK
17. Generation (and detection) of Binary PSK
18. Generation (and detection) of Binary FSK
19. Generation (and detection) of QPSK
20. Generation (and detection) of M-ary PSK
21. Generation (and detection) of M-ary FSK
22. Generation (and detection) of 16-ary QASK
23. Generation (and detection) of MSK

Term Work, Practical and Oral:

At least 8 experiments covering the entire syllabus must be given "**Batch Wise**". The experiments can be conducted with the help of simulation tool (preferably open source) and breadboard and components. Teacher should refer the suggested list of experiments and can design additional experiments to acquire practical design skills. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting and innovative.

Term work assessment must be based on the overall performance of the student with every experiment and assignments graded from time to time. The grades will be converted to marks as per **"Credit and Grading System"** manual and should be added and averaged. Based on the above scheme grading and term work assessment should be done.
The practical and oral examination will be based on entire syllabus. Students are encouraged to share their experiments codes on online repository. Practical exam slip should cover all the 8 experiments for examination.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL502	Discrete-Time Signal Processing Laboratory	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam.			
		Test 1	Test 2	Avg.				
ECL502	Discrete-Time Signal Processing Laboratory	--	--	--	--	25	25	50

Course objectives:

1. To carryout basic discrete time signal processing operations.
2. To implement and design FIR filters and IIR filters.
3. To implement applications related to the field of biomedical signal processing and audio signal processing.

Course outcomes:

Learners will be able to …

1. Perform basic discrete time signal processing operations such as Linear Convolution, Circular Convolution, Auto Correlation, Cross Correlation, etc. and interpret the results.
2. Demonstrate their ability towards interpreting and performing frequency analysis of different discrete time sequences and systems.
3. Design and implement the FIR and IIR Filters for given specifications.
4. Implement and analyse applications related to the field of biomedical signal processing and audio signal processing.

Suggested list of experiments:

1) To perform linear convolution of two signals, auto correlation of non-periodic signals, periodic signals and random noise and interpret the results obtained.
2) To linearly convolve swept frequency sinusoidal wave with LPF and HPF impulse response filters in time domain and interpret the results obtained.

3) To obtain cross correlation of a signal with its delayed and attenuated version (Concept of radar signal processing).
4) To perform block convolution using overlap - add method and overlap-save method.
5) To determine impulse, magnitude, phase response and pole-zero plot of given transfer functions.
6) To perform circular convolution and linear convolution of two sequences using DFT.
7) To perform the DFT of DT sequence and sketch its magnitude and phase spectrum or To Generate a discrete time signal having minimum three frequencies and analyse its frequency spectrum.
8) To study the effect of frequency resolution and zero padding.
9) DFT based spectral analysis to detect the signal buried in noise.
10) To perform denoising of a speech signal using circular convolution.
11) Design of IIR digital filters and use the designed filter to filter an input signal which has both low and high frequency components or real-world signal like ECG/EEG, speech signal etc).
12) Design a notch filter to supress the power supply hum in audio signals.
13) Design a comb filter to suppress 50Hz hum in biomedical signals.
14) Design of FIR filter using windowing method and use the designed filter to filter an input signal which has both low and high frequency components or real-world signal like ECG/EEG, speech signal etc.
15) Design of FIR filter using frequency sampling technique.
16) Design of minimum phase, maximum phase and mixed phase systems.
17) To verify the location of zeros in symmetric and antisymmetric FIR filters.
18) To reconstruct DT signals contaminated with sinusoidal interference using FIR filters.
19) To realise an IIR filter in cascade and parallel form.
20) To obtain lattice parameters of a given transfer function (FIR and IIR systems).
21) To perform coefficient quantisation using truncation and rounding.
22) To study the effect of coefficient quantisation on the frequency response of an IIR filter.
23) To study the effect of coefficient quantisation on the frequency response of an FIR filter.
24) To investigate the behaviour of limit cycle in an IIR system.
25) To generate the ECG signal and detect the characteristic points.
26) Classification of ECG signals.
27) To read an ECG signal and separate the QRS Complex.
28) To filter out the noise in an ECG signal using Spectral subtraction.
29) To extract delta, theta, alpha, sigma, and beta waveforms from EEG signal.
30) Perform sub-band coding on speech signal.
31) To generate Echo, Reverberation, Flanging effects in a sound signal.
32) Musical tone generation.
33) DTMF tone generation and detection.
34) Echo cancellation.

Also check

Virtual Laboratory http://vlabs.iitkgp.ernet.in/dsp/# for demonstration of concepts like DFT and its inverse, FIR filter using windowing method etc

Term Work:

At least 08 experiments covering the entire syllabus must be given "Batch Wise" and implemented using any software namely C, Python, Scilab, Matlab, Octave, etc. The experiments should be set to have well predefined inference and conclusion. Application oriented one course-project can be conducted for maximum batch of four students. Teacher should refer the suggested experiments and can design additional experiment to maintain better understanding and quality.

The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting and innovative.

Term work assessment must be based on overall performance of the student with every experiment graded. The grade must be converted to marks as per credit and grading system manual, and should be added and averaged. Based on above scheme, grading and term work assessment should be done. Practical and oral examination will be based on entire syllabus. Students are encouraged to share their experiments codes on online repository. Practical exam slip should cover all 08 experiments for examination.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL503	Digital VLSI Lab	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam.			
		Test 1	Test 2	Avg.				
ECL503	Digital VLSI Lab	--	--	--	--	25	25	50

Course objectives:

1. To become familiar with open source circuit simulation tools like Ngspice, Magic etc.
2. To perform various type of analysis of combinational and sequential CMOS circuits
3. To evaluate performance of given combinational and sequential CMOS circuits
4. To design, implement and verify combinational and sequential CMOS circuits using open source VLSI design tools.

Course outcomes:

After the successful completion of the course student will be able to

1. Write spice code for given combinational and sequential CMOS circuits.
2. Perform various analysis like operating point, dc, transient etc of given CMSO circuits.
3. Evaluate performance of given CMOS circuits.
4. Draw layout of given CMOS circuit and also able extract various parasitic using open source layout tool like Magic.
5. Design, simulate, and verify CMOS circuit for given specifications.

Suggested list of experiments: (Course teacher can design their own experiments based on the prescribed syllabus)

1. Constant Voltage and Constant field MOSFET scaling
2. Layout of MOSFET and extraction of parasitic capacitances
3. Voltage transfer characteristics of CMOS inverter and calculation of Noise Margin and static power
4. Transient Analysis of CMOS inverter and calculation of tpHL, tpLH, tr, tf, average power
5. Design of CMOS inverter for given specifications
6. Layout of CMOS inverter and comparison of pre layout and post layout performance.
7. Voltage transfer characteristics of 2 input NAND/NOR gate and calculation of noise margins and validation using equivalent inverter approach.
8. Transient Analysis of 2 input NAND/NOR CMOS gate and calculation of tpHL, tpLH, tr, tf, average power and validation using equivalent inverter approach.

9. Layout of 2 input CMOS NAND/NOR gate and comparison of pre layout and post layout performance.
10. Static and transient analysis of Complex CMOS gate.
11. Layout of complex CMOS gate using euler path.
12. Implementation of various combinational and sequential circuits using different design styles.
13. Design and implementation of NAND based and NOR based ROM array.
14. Performance analysis of 6T-SRAM Cell
15. Design of 6T SRAM cell robust read and write operation.
16. Performance analysis of 1T and 3T DRAM Cell
17. RTL design of Soda dispenser machine
18. RTL design of FIR Filter

Link for virtual lab
http://www.vlsi-iitg.vlabs.ac.in

Term Work, Practical and Oral:

At least 8 experiments (at least three experiments on layout) covering the entire syllabus must be given "**Batch Wise"**. The experiments can be conducted with the help of simulation tool (preferably. Teacher should refer the suggested list of experiments and can design additional experiments to acquire practical design skills. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting and innovative.

Term work assessment must be based on the overall performance of the student with every experiment and assignments graded from time to time. The grades will be converted to marks as per **"Credit and Grading System"** manual and should be added and averaged. Based on the above scheme grading and term work assessment should be done.
The practical and oral examination will be based on entire syllabus. Students are encouraged to share their experiments codes on online repository. Practical exam slip should cover all the 8 experiments for examination.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ECL504	Professional Communication & Ethics-II	--	2*+ 2 Hours (Batch-wise)	--	--	2	--	02

*Theory class to be conducted for full class.

Course Code	Course Name	Examination Scheme									
		Theory					Term work	Pract	Oral	Internal Oral	Total
		Internal Assessment			End sem	Duration (hrs)					
		Test 1	Test 2	Avg.							
ECL504	Professional Communication & Ethics-II (abbreviated PCE-II)	--	--	--	--	--	25	--	--	25	50

Course Code	Course Name	Credits
ECL504	Business Communication & Ethics	02
Course Rationale	This curriculum is designed to build up a professional and ethical approach, effective oral and written communication with enhanced soft skills. Through practical sessions, it augments student's interactive competence and confidence to respond appropriately and creatively to the implied challenges of the global Industrial and Corporate requirements. It further inculcates the social responsibility of engineers as technical citizens.	
Course Objectives	• To discern and develop an effective style of writing important technical/business documents. • To investigate possible resources and plan a successful job campaign. • To understand the dynamics of professional communication in the form of group discussions, meetings, etc. required for career enhancement. • To develop creative and impactful presentation skills. • To analyze personal traits, interests, values, aptitudes and skills. • To understand the importance of integrity and develop a personal code of ethics.	

Course Outcomes	Learner will be able to…
	Learner will be able to… • plan and prepare effective business/ technical documents which will in turn provide solid foundation for their future managerial roles. • strategize their personal and professional skills to build a professional image and meet the demands of the industry. • emerge successful in group discussions, meetings and result-oriented agreeable solutions in group communication situations. • deliver persuasive and professional presentations. • develop creative thinking and interpersonal skills required for effective professional communication. • apply codes of ethical conduct, personal integrity and norms of organizational behaviour.

Module	Contents	Hours
1	**ADVANCED TECHNICAL WRITING :PROJECT/PROBLEM BASED LEARNING (PBL)** **1.1 Purpose and Classification of Reports:** **Classification on the basis of:** • Subject Matter (Technology, Accounting, Finance, Marketing, etc.) • Time Interval (Periodic, One-time, Special) • Function (Informational, Analytical, etc.) • Physical Factors (Memorandum, Letter, Short & Long) **1.2. Parts of a Long Formal Report:** • Prefatory Parts (Front Matter) • Report Proper (Main Body) • Appended Parts (Back Matter) **1.3. Language and Style of Reports** • Tense, Person & Voice of Reports • Numbering Style of Chapters, Sections, Figures, Tables and Equations • Referencing Styles in APA & MLA Format • Proofreading through Plagiarism Checkers **1.4. Definition, Purpose & Types of Proposals** • Solicited (in conformance with RFP) & Unsolicited Proposals • Types (Short and Long proposals) **1.5. Parts of a Proposal** • Elements • Scope and Limitations • Conclusion	06

	1.6. Technical Paper Writing • Parts of a Technical Paper (Abstract, Introduction, Research Methods, Findings and Analysis, Discussion, Limitations, Future Scope and References) • Language and Formatting • Referencing in IEEE Format	
2	**EMPLOYMENT SKILLS** **2.1. Cover Letter & Resume** • Parts and Content of a Cover Letter • Difference between Bio-data, Resume & CV • Essential Parts of a Resume • Types of Resume (Chronological, Functional & Combination) **2.2 Statement of Purpose** • Importance of SOP • Tips for Writing an Effective SOP **2.3 Verbal Aptitude Test** • Modelled on CAT, GRE, GMAT exams **2.4. Group Discussions** • Purpose of a GD • Parameters of Evaluating a GD • Types of GDs (Normal, Case-based & Role Plays) • GD Etiquettes **2.5. Personal Interviews** • Planning and Preparation • Types of Questions • Types of Interviews (Structured, Stress, Behavioural, Problem Solving & Case-based) • Modes of Interviews: Face-to-face (One-to one and Panel) Telephonic, Virtual	**06**
3	**BUSINESS MEETINGS** **1.1. Conducting Business Meetings** • Types of Meetings • Roles and Responsibilities of Chairperson, Secretary and Members • Meeting Etiquette **3.2. Documentation** • Notice • Agenda • Minutes	**02**

4	**TECHNICAL/ BUSINESS PRESENTATIONS** **1.1 Effective Presentation Strategies** • Defining Purpose • Analyzing Audience, Location and Event • Gathering, Selecting &Arranging Material • Structuring a Presentation • Making Effective Slides • Types of Presentations Aids • Closing a Presentation • Platform skills **1.2 Group Presentations** • Sharing Responsibility in a Team • Building the contents and visuals together • Transition Phases	**02**
5	**INTERPERSONAL SKILLS** **1.1. Interpersonal Skills** • Emotional Intelligence • Leadership & Motivation • Conflict Management & Negotiation • Time Management • Assertiveness • Decision Making **5.2 Start-up Skills** • Financial Literacy • Risk Assessment • Data Analysis (e.g. Consumer Behaviour, Market Trends, etc.)	**08**
6	**CORPORATE ETHICS** **6.1Intellectual Property Rights** • Copyrights • Trademarks • Patents • Industrial Designs • Geographical Indications • Integrated Circuits • Trade Secrets (Undisclosed Information) **6.2 Case Studies** • Cases related to Business/ Corporate Ethics	**02**

List of assignments:
(In the form of Short Notes, Questionnaire/ MCQ Test, Role Play, Case Study, Quiz, etc.)

1. Cover Letter and Resume
2. Short Proposal

3. Meeting Documentation
4. Writing a Technical Paper/ Analyzing a Published Technical Paper
5. Writing a SOP
6. IPR
7. Interpersonal Skills
8. Aptitude test (Verbal Ability)

Note:

1. The Main Body of the project/book report should contain minimum 25 pages (excluding Front and Back matter).
2. The group size for the final report presentation should not be less than 5 students or exceed 7 students.
3. There will be an end–semester presentation based on the book report.

Assessment:

Term Work:

Term work shall consist of minimum 8 experiments.

The distribution of marks for term work shall be as follows:

Assignment : 10 Marks
Attendance : 5 Marks
Presentation slides : 5 Marks
Book Report (hard copy) : 5 Marks

The final certification and acceptance of term work ensures the satisfactory performance of laboratory work and minimum passing in the term work.

Internal oral:

Oral Examination will be based on a GD & the Project/Book Report presentation.

Group Discussion : 10 marks
Project Presentation : 10 Marks
Group Dynamics : 5 Marks

Books Recommended:

Textbooks and Reference books:

1. Arms, V. M. (2005). *Humanities for the engineering curriculum: With selected chapters from Olsen/Huckin: Technical writing and professional communication, second edition*. Boston, MA: McGraw-Hill.
2. Bovée, C. L., &Thill, J. V. (2021). *Business communication today*. Upper Saddle River, NJ: Pearson.
3. Butterfield, J. (2017). *Verbal communication: Soft skills for a digital workplace*. Boston, MA: Cengage Learning.
4. Masters, L. A., Wallace, H. R., & Harwood, L. (2011).*Personal development for life and work*. Mason: South-Western Cengage Learning.
5. Robbins, S. P., Judge, T. A., & Campbell, T. T. (2017). *Organizational behaviour*. Harlow,

England: Pearson.

6. Meenakshi Raman, Sangeeta Sharma (2004) Technical Communication, Principles and Practice. Oxford University Press
7. Archana Ram (2018) Place Mentor, Tests of Aptitude For Placement Readiness. Oxford University Press
8. Sanjay Kumar &PushpLata (2018). Communication Skills a workbook, New Delhi: Oxford University Press.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECM501	Mini Project 2A: Embedded System Project	--	04$	--	--	02	--	02

$ Indicates work load of a learner (Not Faculty) for Mini Project 2A. Faculty Load: 1 hour per week per four groups.

Subject Code	Subject Name	Examination Scheme						
		Theory Marks						
		Internal Assessment			End Sem. Exam	Term Work	Practical and Oral	Total
		Test 1	Test 2	Avg. of Test 1 and Test 2				
ECM501	Mini Project 2A: Embedded System Project	--	--	--	--	25	25	50

Course Pre-requisite:

1. ECC402- Microcontrollers
2. ECC403- Linear Integrated Circuits
3. ECM401- Mini Project 1B: Arduino & Raspberry Pi based Projects

Course Objectives

1. To develop background knowledge Embedded Systems.
2. To understand designing of embedded systems.
3. To choose proper microcontroller for Embedded systems
4. To understand use of wireless sensors/communications with Embedded systems
5. To understand communication techniques.
6. To write programs for embedded systems and real time operating systems /IoT

Course Outcomes

After successful completion of the course, the student will be able to

1. Understand the embedded systems with design metrics.
2. Understand microcontrollers and programming in Embedded C.
3. Implementation of Embedded systems with different sensors and peripherals as IoT.
4. Implementation of Embedded systems with different communication protocols as IoT.
5. Analyze concepts of Real time operating systems.
6. Design embedded system applications using sensors, peripherals and RTOS

A. Guideline to maintain quality of mini project are as follows :

1. To achieve proper selection of Mini Projects.Students should do survey of different microcontroller board from given microcontroller series, tools and identify which is most suitable for their selected topic. They should consult with their Guide/Mentors / Internal committee to finalize it.
2. Students shall submit implementation plan in the form of Smart Report/Gantt/PERT/CPM chart, which will cover weekly activity of mini project.
3. A log book to be prepared by each group, wherein group can record weekly work progress. Guide/ supervisor will verify it and will put notes/comments.

4. Guide/supervisor guidance is very much important during mini project activities; however, focus shall be on self-learning.
5. The solution to be verified with standard tools and procedures and report to be compiled in standard format of University of Mumbai.
6. **Suggested steps for mini project selection and implementation**
   i. Mini project should be completely microcontroller based
   ii. Follow these steps
      a) Take specification, using these specifications design project.
      b) Select proper microcontroller board considering features and requirements of project.
      c) Program it using Embedded C and perform verification of each module (sensors/communication protocol)
      d) Test Functional Simulation and verify it using simulation tool.
      e) Make hardware connection on GPP of peripherals with microcontroller board and execute the program.
      f) Troubleshoot if not get expected result.

B. **Project Topic selection and approval :-**

1. The group may be of **maximum FOUR (04)** students.
2. Topic selection and approval by **2 Expert** faculty from department at the start of semester
3. **Log Book** to be prepared for each group to record the work progress in terms of milestones per week by students. Weekly comment, remarks to be put by guiding faculty. Both students and faculty will put signature in it per week. The log book can be managed **online** with proper authentication method using google sheets/forms or open source project management software.

C. **Project Report Format:**

1. Report should not exceed **30 pages**. Simply staple it to discourage use of plastic.
2. Report must contain block diagram, circuit diagram, screenshot of outputs and datasheets of microcontrollers and peripherals (Include **only required** information pages).
3. The recommended report writing format is in **LaTeX.** **(https://youtu.be/YLm3sXlKpHQ)**

<u>**Term Work:**</u>

1. **Term Work evaluation and marking scheme:**
   a. The review/ progress monitoring committee shall be constituted by Head of Departments of each institute.
   b. The progress of mini project to be evaluated on continuous basis, minimum two reviews in each semester.
   c. At end of semester the above 2 expert faculty who have approved the topic will internally **evaluate the performance**.
   d. Students have to give presentation and demonstration on the Embedded Systems Mini Project- 2-A at end of semester before submission to above experts.
   e. In the evaluation each individual student should be assessed for his/her contribution, understanding and knowledge gained about the task completed. Based upon it the marks will be awarded to student.

   f. **<u>Distribution of 25 Marks scheme</u> is as follows:**

      i. Marks awarded by guide/supervisor based on log book and output : 10
      ii. Marks awarded by review committee : 10
      iii. Quality of Project report : 05

2. Guidelines for Assessment of Mini Project Practical/Oral Examination:

- **a.** Report should be prepared as per the guidelines issued by the University of Mumbai.
- **b.** Mini Project shall be assessed through a presentation and demonstration of working model by the student project group to a panel of Internal and **External Examiners preferably from industry or research organisations** having experience of more than five years approved by head of Institution.

Students shall be motivated to present their mini project work done

1. Participate in Project Competition
2. Publish paper in Conferences/Journals.

Module No.	Unit No.	Detailed Content	Hours
1		**Introduction**	**8**
	1.1	Definition of Embedded System, Embedded Systems Vs General Computing Systems, Classification, Major Application Areas. Characteristics and quality attributes (Design Metric) of embedded system.	
	1.2	Identification of Project Title	
2		**Controller boards and Programming – Embedded C**	**8**
	2.1	ARM LPC 21XX (2148), STM32 boards and Texas MSP 430 lunchbox/ Tiva C board and **PIC/PSoc***	
	2.2	Comparison of C and embedded C, Data Types, Variable, Storage Classes, Bit operation , Arrays, Strings, Structure and unions, Classifier	
	2.3	**Exercise:** Identify the suitable board required for the particular application with respect to design metrics. (**Hint:** check clock frequency (speed) , memory (program and data), no. of ports for peripherals, timers/counters and serial communication requirement for project)	
	2.4	**Suggested Way to Identify :** https://predictabledesigns.com/how-to-select-the-microcontroller-for-your-new-product/	
3		**Interfacing Sensors and peripherals using Embedded C**	**10**
	3.1	Sensors and Signal Conditioning Circuits amplifiers /attenuators /filters /comparators/ADC and DAC) , Interfacing with GLCD/TFT display , Relays and Drivers for interfacing Motors (DC and stepper)	
	3.2	Interfacing with BLDC motors and drivers, USB/HDMI camera interfacing	
	3.3	**Exercise :** Understand the Interfacing requirement like drivers, signal condition circuits for sensors, etc. for the selected application	
	3.4	**Study Material : For LCD interfacing with MSP430 LaunchPad** https://microcontrollerslab.com/lcd-interfacing-msp430-launchpad/#:~:text=LCD%20interfacing%20with%20MSP430%20microcontroller,Now%20I%20will&text=It%20requires%205%20volts%20dc,and%20second%20pin%20is%20vcc.	
4		**Communication with programming in Embedded C**	**10**
	4.1	Serial communication, CAN bus, I2C, MOD bus, SPI	
	4.2	Interfacing with Wi-Fi, Bluetooth ,ZigBee, LoRa, RFID and putting data on IoT	
	4.3	Interfacing with GSM module , GPS module, SD card	
	4.4	**Exercise:** Understand Communication requirement for selected application and test it	

	4.5	**Study Material :** Serial Communication Interface: **STM32:**https://controllerstech.com/serial-transmission-in-stm32/#:~:text=Serial%20Transmission%20in%20Stm32&text=UART%20is%20widely%20used%20for,amongst%20which%20communication%20is%20done. **LPC2148:** https://www.electronicwings.com/arm7/lpc2148-uart0 **MSP430:** https://www.ti.com/lit/ml/slap117/slap117.pdf	
5		**Real Time Operating Systems[RTOS]**	**08**
	5.1	Operating system basics , Types of OS , Tasks, process, Threads	
	5.2	Multiprocessing and ,Multitasking , Task scheduling	
	5.3	RTLinux/ Free RTOS and Mbed OS , Implementation with RTOS	
6		**Cloud/Web server**	**08**
	6.1	Implementation on web server ,	
	6.2	Thingspeak, AWS cloud platform for IoT based programming and modelling	
	6.3	**Exercise : perform ESP8266 interface with microcontroller**	
	6.4	**Study Material :** **STM32:** https://circuitdigest.com/microcontroller-projects/interfacing-esp8266-with-stm32f103c8-stm32-to-create-a-webserver **LPC2148:** https://circuitdigest.com/microcontroller-projects/iot-based-ARM7-LPC2148-webserver-to-control-an-led **MSP430:** https://circuitdigest.com/microcontroller-projects/sending-email-using-msp430-and-esp8266	
		Total	**52**

NOTE:

* **Advanced Microcontroller:** Like PSoc and PIC may be used as per the student's intellectual ability and strength.

** **Module 5 and 6 (RTOS and Cloud/Web Server):** Can be included by Guide /supervisor /Mentor depending upon need and scope of the project for selected topic and its application.

Textbooks:

1. Shibu K.V," Introduction to Embedded Systems", Mc Graw Hill, 2nd edition.
2. Frank Vahid, and Tony Givargis, "Embedded System Design: A unified Hardware/Software Introduction", Wiley Publication.
3. Raj Kamal," Embedded Systems Architecture, Programming and design",Tata MCgraw-Hill Publication.
4. Dr. K.V.K.K. Prasad, "Embedded Real Time Systems: Concepts, Design & Programming",Dreamtech Publication.
5. Iyer, Gupta," Embedded real systems Programming", TMH
6. David Simon, "Embedded systems software primer',Pearson
7. Andrew Sloss, Dominic Symes and Chris Wright, "ARM_System_Developers_Guide-Designing_and_Optimizing_System_Software" Elsevier and Morgan Kaufmann Publishers.
8. Michel J Pont "Embedded C" Pearson

Suggested Software tools:

1. Tinkercad : https://www.tinkercad.com/
2. Proteus software
3. KEIL for ARM LPC 2148
4. **STM32Cube software**

5. MSP Flasher - Command Line Programmer
6. msp430 code composer studio

Online Repository:

1. https://circuitdigest.com
2. www. Github.com
3. https://www.electronicshub.org
4. https://www.hackster.io/

NPTEL Courses:

1. **Introduction to Embedded System Design (using MSP430)**
   https://onlinecourses.nptel.ac.in/noc20_ee98/preview
2. **Embedded System Design with ARM**
   https://onlinecourses.nptel.ac.in/noc20_cs15/preview
3. **Embedded systems**
   https://nptel.ac.in/courses/108/102/108102045/
4. **Master Microcontroller and Embedded Driver Development(MCU1) STM32**
   https://www.udemy.com/course/mastering-microcontroller-with-peripheral-driver-
   development/?gclid=CjwKCAjw07qDBhBxEiwA6pPbHslLI-
   EqmAv7E17ysZETbreXe0XMb8Nai4NBqpUAvni5v-
   3fLKsfNBoC8LQQAvD_BwE&matchtype=b&utm_campaign=LongTail_la.EN_cc.INDIA&ut
   m_content=deal4584&utm_medium=udemyads&utm_source=adwords&utm_term=_._ag_8287
   6601447_._ad_511749008336_._kw_%2Bembedded+%2Bsystems+%2Bcourse_._de_c_._dm_
   _._pl__._ti_kwd-671751469914_._li_1007785_._pd__._
5. **Texas Instruments (TI) Trainings**
   https://e2e.ti.com/support/archive/universityprogram/educators/w/wiki/2103/training-support
6. **Texas Instruments (TI) Teaching material/ text books**
   https://e2e.ti.com/support/archive/universityprogram/educators/w/wiki/2035/textbooks

AC 29/6/2021
Item No. 6.5

UNIVERSITY OF MUMBAI

Bachelor of Engineering

in

Electronics and Telecommunication Engineering

Second Year with Effect from AY 2020-21

Third Year with Effect from AY 2021-22

Final Year with Effect from AY 2022-23

(REV- 2019 ‘C’ Scheme) from Academic Year 2019 – 20

Under

FACULTY OF SCIENCE & TECHNOLOGY

(As per AICTE guidelines with effect from the academic year 2019–2020)

AC 29/6/2021
Item No. 6.5

UNIVERSITY OF MUMBAI

Syllabus for Approval

Sr. No.	Heading	Particulars
1	Title of the Course	**Third Year in Bachelor of Electronics and Telecommunication Engineering**
2	Eligibility for Admission	**After Passing Second Year Engineering as per the Ordinance 0.6243**
3	Passing Marks	**40%**
4	Ordinances / Regulations (if any)	**Ordinance 0.6243**
5	No. of Years / Semesters	**8 semesters**
6	Level	~~P.G.~~ / **U.G.**/ ~~Diploma / Certificate~~ (Strike out which is not applicable)
7	Pattern	~~Yearly~~ / **Semester** (Strike out which is not applicable)
8	Status	~~New~~ / **Revised** (Strike out which is not applicable)
9	To be implemented from Academic Year	**With effect from Academic Year: 2021-2022**

Date **29-06-2021**

Dr. S. K. Ukarande
Associate Dean
Faculty of Science and Technology
University of Mumbai

Dr Anuradha Muzumdar
Dean
Faculty of Science and Technology
University of Mumbai

Preamble

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Science and Technology (in particular Engineering)of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth and approach of course to be taught, which will enhance learner's learning process. Choice based Credit and grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. Credit assignment for courses is based on 15 weeks teaching learning process, however content of courses is to be taught in 13 weeks and remaining 2 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

There was a concern that the earlier revised curriculum more focused on providing information and knowledge across various domains of the said program, which led to heavily loading of students in terms of direct contact hours. In this regard, faculty of science and technology resolved that to minimize the burden of contact hours, total credits of entire program will be of 171, wherein focus is not only on providing knowledge but also on building skills, attitude and self learning. Therefore in the present curriculum skill based laboratories and mini projects are made mandatory across all disciplines of engineering in second and third year of programs, which will definitely facilitate self learning of students. The overall credits and approach of curriculum proposed in the present revision is in line with AICTE model curriculum.

The present curriculum will be implemented for Second Year of Engineering from the academic year 2020-21. Subsequently this will be carried forward for Third Year and Final Year Engineering in the academic years 2021-22, 2022-23, respectively.

Dr. S. K. Ukarande
Associate Dean
Faculty of Science and Technology
University of Mumbai

Dr Anuradha Muzumdar
Dean
Faculty of Science and Technology
University of Mumbai

Incorporation and Implementation of Online Contents from NPTEL/ Swayam Platform

The curriculum revision is mainly focused on knowledge component, skill based activities and project based activities. Self learning opportunities are provided to learners. In the revision process this time in particular Revised syllabus of 'C ' scheme wherever possible additional resource links of platforms such as NPTEL, Swayam are appropriately provided. In an earlier revision of curriculum in the year 2012 and 2016 in Revised scheme 'A' and 'B' respectively, efforts were made to use online contents more appropriately as additional learning materials to enhance learning of students.

In the current revision based on the recommendation of AICTE model curriculum overall credits are reduced to 171, to provide opportunity of self learning to learner. Learners are now getting sufficient time for self learning either through online courses or additional projects for enhancing their knowledge and skill sets.

The Principals/ HoD's/ Faculties of all the institute are required to motivate and encourage learners to use additional online resources available on platforms such as NPTEL/ Swayam. Learners can be advised to take up online courses, on successful completion they are required to submit certification for the same. This will definitely help learners to facilitate their enhanced learning based on their interest.

Dr. S. K. Ukarande
Associate Dean
Faculty of Science and Technology
University of Mumbai

Dr Anuradha Muzumdar
Dean
Faculty of Science and Technology
University of Mumbai

Preface By BoS

Technological developments in the field of electronics and telecommunication engineering have revolutionized the way people see the world today. Hence, there is a need for continuously enriching the quality of education by a regular revision in the curriculum, which will help our students achieve better employability, start-ups, and other avenues of higher studies. The current revision in the Bachelor of Engineering program (REV- 2019 'C' Scheme) aims at providing a strong foundation with required analytical concepts in the field of electronics and telecommunication engineering.

Some of the salient features of this revised curriculum are as below and they fall in line with the features in AICTE Model Curriculum.

1. The curriculum is designed in such a way that it encourages innovation and research as the total number of credits has been reduced from around 200 credits in an earlier curriculum to 171 credits in the current revision.
2. In the second and third-year curriculum, skill-based laboratories and mini-projects are introduced.
3. It will result in the students developing a problem-solving approach and will be able to meet the challenges of the future.
4. The University of Mumbai and BoS – Electronics and Telecommunication Engineering will ensure the revision of the curriculum on regular basis in the future as well and this update will certainly help students to achieve better employability; start-ups and other avenues for higher studies.

The BoS would like to thank all the subject experts, industry representatives, alumni, and various other stakeholders for their sincere efforts and valuable time in the preparation of course contents, reviewing the contents, giving valuable suggestions, and critically analyzing the contents.

Board of Studies in Electronics and Telecommunication Engineering

Dr. Faruk Kazi: Chairman

Dr. V. N. Pawar: Member

Dr. Ravindra Duche: Member

Dr. Milind Shah: Member

Dr. R. K. Kulkarni: Member

Dr. Baban U. Rindhe: Member

Dr. Mrs. Nair: Member

Dr. Nalbarwar: Member

Dr. Sudhakar Mande: Member

Dr. S. D. Deshmukh: Member

Semester VI

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ECC601	Electromagnetics and Antenna	3	--	--	3	--	--	3
ECC602	Computer Communication Networks	3	--	--	3	--	--	3
ECC603	Image Processing and Machine Vision	3	--	--	3	--	--	3
ECC604	Artificial Neural Network and Fuzzy Logic	3	--	--	3	--	--	3
ECCDLO 601X	Department Level Optional Course-2	3	--	--	3	--	--	3
ECL601	Electromagnetics and Antenna Lab	--	2	--	--	1	--	1
ECL602	Computer Communication Networks Lab	--	2	--	--	1	--	1
ECL603	Image Processing and Machine Vision Lab	--	2	--	--	1	--	1
ECL604	Skill Lab: Linux and Networking and Server Configuration	--	4	--	--	2	--	2
ECM601	Mini Project 2B- FPGA based Project	--	$4^{\$}$	--	--	2	--	2
Total		**15**	**14**	**--**	**15**	**07**	**--**	**22**

$ Indicates work load of a learner (Not Faculty) for Mini Project 2B. Faculty Load: 1 hour per week per four groups.

Course Code	Course Name	Examination Scheme							
		Theory					Term Work	Pract. & oral	Total
		Internal Assessment			End Sem. Exam.	Exam. Duration (in Hrs)			
		Test 1	Test 2	Avg.					
ECC601	Electromagnetics and Antenna	20	20	20	80	3	--	--	100
ECC602	Computer Communication Networks	20	20	20	80	3	--	--	100
ECC603	Image Processing and Machine Vision	20	20	20	80	3	--	--	100
ECC604	Artificial Neural Network and Fuzzy Logic	20	20	20	80	3	--	--	100
ECCDLO 601X	Department Level Optional Course-2	20	20	20	80	3	--	--	100
ECL601	Electromagnetics and Antenna Lab	--	--	--	--	--	25	25	50
ECL602	Computer Communication Networks Lab	--	--	--	--	--	25	25	50
ECL603	Image Processing and Machine Vision Lab	--	--	--	--	--	25	25	50
ECL604	Skill Lab: Linux and Networking and Server Configuration	--	--	--	--	--	25	25	50
ECM601	Mini Project 2B- FPGA based Project	--	--	--	--	--	25	25	50
Total		**--**	**--**	**100**	**400**	--	**125**	**125**	**750**

Department Level Optional Course-2

Course Code	Department Level Optional Course-2
ECCDLO6011	Mixed Signal VLSI
ECCDLO6012	Computer Organization and Architecture
ECCDLO6013	Digital Forensic
ECCDLO6014	Database Management System
ECCDLO6015	IoT and Industry 4.0
ECCDLO6016	Radar Engineering

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC601	Electromagnetics and Antenna	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam. Duration (in Hrs)	Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ECC601	Electromagnetics and Antenna	20	20	20	80	03	--	--	100

Pre-requisites:

1. Vector Calculus
2. Fundamental concepts of electricity and magnetism

Course Objective: The objective of the course is to make student familiar with Maxwell's equation and its usefulness to describe different electromagnetic phenomena such as wave propagation, radiations from antenna etc.

Course Outcome: Student will be able to:

1. Students will be able to describe electromagnetics field including static and dynamic in terms of Maxwell's equations.
2. Students will be able to apply Maxwell's equation to solve various electromagnetic phenomenon such as electromagnetic wave propagation in different medium, power in EM wave.
3. Students will derive the field equations for the basic radiating elements and describe basic antenna parameters like radiation pattern, directivity, gain etc.
4. Students will be able to implement different types of the antenna structures such as Antenna arrays, Microstrip antenna and reflector antenna etc.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Static fields**	**06**
	1.1	Charge, Coulomb's law, Charge configurations, Electric field intensity, Electric flux density, Gauss's law and applications, Current density, and Continuity equation	
	1.2	Scalar Electric Potential, Potential gradient, Laplace's and Poison's equations	
	1.3	Biot Savart Law, Ampere Circuit law, Gauss's law for magnetic field, Vector magnetic potential	
2.0		**Electromagnetic Field and Maxwell's Equations**	**09**
	2.1	Faraday's Law, Displacement current density, Maxwell's equation for time varying filed, Boundary conditions.	
	2.2	EM wave propagation through lossy, perfect dielectric and conducting medium.	
	2.3	Power in EM Wave: Poynting theorem and Poynting vector	
3.0		**Basic of Antennas**	**08**
	3.1	Basic concepts: Radiation mechanism, Near field and far field radiation, retarded potential	
	3.2	Antenna Parameters: Isotropic antenna, Radiation pattern, radiation intensity, Beamwidth, directivity, Gain, beam efficiency, bandwidth, polarization, Input impedance, Antenna efficiency, Radiation resistance, Loss resistance, aperture concept, FRII's transmission formula	
	3.3	Wire Elements: Infinitesimal dipole, Wire dipole, Monopole antennas: radiation field derivations and related parameters, Introduction to loop antenna	
4.0		**Antenna Arrays**	**06**
	4.1	Linear arrays of two isotropic point sources, linear arrays of N elements, Principle of pattern multiplication	
	4.2	Introduction to Planner and circular arrays Introduction to array synthesis using Binomial array	
5.0		**Types of antennas**	**06**
	5.1	Yagi antenna, Broadband antenna like Helical and Log Periodic antenna Horn Antennas**:** E-Plane Sectoral Horn, H-Plane Sectoral Horn, Pyramidal Horn and Conical Horn	
	5.2	Reflector Antennas: Plane Reflectors, Corner Reflectors and Parabolic Reflector	
	5.3	Patch Antenna: Microstrip antenna, Feeding Techniques, Introduction to design of Microstrip antenna (Rectangular and circular patch)	
6.0		**Electromagnetic Wave Propagation**	**04**
	6.1	Ground Wave Propagation, Sky Wave Propagation and Space Wave Propagation	
		Total	**39**

Textbooks:

1. Electromagnetic Waves and Radiating Systems- Jordan and Balmain, PHI, 2nd edition
2. Principles of Electromagnetics Engineering- Matthew N. O.Sadiku , S.V.Kulkarni, Oxford university press, 6th edition
3. Antenna Theory: Analysis and Design, Costantine A. Balanis, John Wiley Publication, 4th edition
4. Antenna and wave Propagation, John D Kraus, A S Khan, McGraw Hill, 4th edition
5. Antenna Theory and Design. Stutzman, Theile, John Wiley and Sons, 3rd edition

Reference Books:

1. Engineering Electromagnetics, William H Hayt and John A Buck, Tata McGraw-Hill Publishing Company Limited, 7th edition
2. Antennas and Radio Wave Propagation, R. E. Collin, McGraw Hill, International Student Edition

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Subject Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC602	Computer Communication Networks	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC602	Computer Communication Networks	20	20	20	80	03	--	--	100

Course pre-requisite:

ECC: 405– Principles of communication engineering

ECC: 501-Digital communication

Course Objectives:

1. To introduce networking architecture and protocols.
2. To understand and recognize the layer-wise functions, services, data formats, protocols, hardware devices and addresses in the TCP/IP architecture
3. To build an understanding of application layer protocols.
4. To apply different addressing and routing schemes.

Course Outcomes:

After successful completion of the course student will be able to:

1. Analyze network topologies, hardware devices, addressing schemes and the protocol stacks
2. Compare various transmission media and broadband technologies
3. Analyze the flow control, error control and the medium access control techniques
4. Judge network layer addressing and routing schemes
5. Analyze connection oriented and connectionless services
6. Apply the knowledge of application layer protocols

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Network Architectures, Protocol Layers, and Service models**	**06**
	1.1	Applications of computer networks. Network types: LAN, MAN, and WAN, Network topologies.	
	1.2	Protocols and standards, need of layered protocol architecture, OSI reference model.	
	1.3	TCP/IP architecture: protocol suite, comparison of OSI and TCP/IP	
	1.4	Layer wise network hardware devices (NIC, Repeaters, Hubs, Bridges, Switches, Routers, Gateway and their comparison)	
	1.5	Addressing: physical / logical /port addressing/socket addressing.	
2.0		**Physical Layer**	**04**
	2.1	Guided transmission media: comparison among coaxial, optical fiber and twisted pair cables.	
	2.2	Unguided transmission media	
	2.3	Transmission impairments	
	2.4	Broadband standards: Cable modem, DSL, and HFC	
3.0		**Data Link Layer**	**07**
	3.1	Data link services: Framing, Flow control, Error control	
	3.2	ARQ methods: transmission efficiency, Piggybacking	
	3.3	High Level Data Link Control (HDLC): HDLC configurations, Frame formats, HDLC bit stuffing and de-stuffing, Typical frame exchanges.	
	3.4	Medium Access Control Protocols: ALOHA, Slotted ALOHA, CSMA, CSMA/CD	
4.0		**Network Layer**	**12**
	4.1	Introduction to telephone networks and circuit switching principles.	
	4.2	Introduction to data networks and packet switching principles.	
	4.3	Network layer services and functions.	
	4.4	Internet Protocol: Principles of Internetworking, requirements, IPv4 packet, IPv4 addressing (classful and classless (CIDR))	
	4.5	Routing in Packet Switching Networks: Characteristics, Routing strategies	
	4.6	Routing algorithms: Link state Routing, Distance vector Routing and Path vector routing, Routing protocols: RIP, OSPF, BGP and EIGRP.	
	4.7	Subnetting, supernetting, VLSM, and NAT	
	4.8	Introduction to ICMP, ARP, RARP	
	4.9	IPv6 (IPv6 Datagram format, comparison with IPv4, and transition from IPv4 to IPv6).	
	4.10	Quality of service	
5.0		**Transport Layer**	**06**
	5.1	Connectionless and Connection–oriented services at transport layer, Transmission Control Protocol (TCP): TCP Services, TCP Segment, TCP three way handshake	
	5.2	User datagram Protocol (UDP), UDP Services, UDP Datagram	
	5.3	TCP and UDP checksum calculation	
	5.4	Flow control, error control and congestion control	

6.0		**Application Layer**	**04**
	6.1	Introduction to Application layer Protocols: HTTP, FTP, DNS, SMTP, TELNET, SSH, DHCP.	
Total			**39**

Text books:

1. Data Communications and Networking – Behrouz A. Forouzan, Fifth Edition TMH, 2013.
2. Computer Networks -- Andrew S Tanenbaum, 5th Edition, Pearson Education, 2013.
3. J J. F. Kurose and K. W. Ross," Computer Networking: A Top-Down Approach", Addison Wesley, 5th Edition, 2010

Reference books:

1. Alberto Leon Garcia, "Communication Networks", McGraw Hill Education, Second Edition, Fourth Edition, 2008.
2. An Engineering Approach to Computer Networks-S.Keshav, 2nd Edition, Pearson Education, 2015.
3. Understanding communications and Networks, 3rd Edition, W.A.Shay, Cengage Learning
4. Data and Computer Communications, William Stallings, 10th Edition, Pearson Education, 2014.

Internal Assessment (IA):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of total 06 questions, each carrying 20 marks.

2. Question No: 01 will be compulsory and based on entire syllabus wherein 4 to 5 sub-questions will be asked.

3. Remaining questions will be mixed in nature and randomly selected from all the modules.

4.Total 04 questions need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC603	Digital Image Processing and Machine Vision	03	--	--	03	--	-	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC603	Digital Image Processing and Machine Vision	20	20	20	80	03	--	--	100

Prerequisites:

1. Signals and Systems
2. Discrete Time Signal Processing
3. Python Programming Skill Lab

Course Objectives:

1. To teach the fundamentals and mathematical models in digital image processing and Machine Vision
2. To teach quality enhancement of image through filtering operations
3. To teach the students image morphology and restoration techniques
4. To expose the students to segmentation techniques in image processing and Machine Vision
5. To teach the techniques of extracting image attributes like regions and shapes
6. To learn classification and recognition algorithms for machine vision

Course Outcomes:

After successful completion of the course student will be able to

1. Understand fundamentals of image processing and machine vision
2. Enhance the quality of image using spatial and frequency domain techniques for image enhancement
3. Learn image morphology and restoration techniques
4. Learn image segmentation techniques based on principle of discontinuity and similarity using various algorithms
5. Represent boundaries and shapes using standard techniques.
6. Classify the object using different classification methods

Module No.	Unit No.	Topics	Hrs.
1		**DIGITAL IMAGE FUNDAMENTALS AND POINT PROCESSING**	**04**
	1.1	Introduction –Steps in Digital Image Processing, concept of spatial and intensity resolution, Relationships between pixels	02
	1.2	**Point Processing :** Image Negative, Log Transform, Power Law transform, Bit plane slicing, Contrast stretching , Histogram equalization and Histogram Specification	02
2		**IMAGE ENHANCEMENT**	**08**
	2.1	**Spatial Domain filtering** : The Mechanics of Spatial Filtering, **Smoothing Spatial Filters-**Linear Filters-Averaging filter, Order-Statistic Filters- Median filter, Application of Median filtering for Noise removal **Sharpening Spatial Filters-** The Laplacian, Unsharp Masking and Highboost Filtering, Using First-Order Derivatives —The Gradient- Sobel, Prewitt and Roberts	03
	2.2	**Frequency Domain Filtering:** Introduction to 2-D DFT and its application in frequency domain filtering, Wavelet transform, Haar transform	02
	2.3	**Frequency Domain Filtering Fundamentals**, Fourier Spectrum and Phase angle ,Steps for Filtering in the Frequency Domain, Correspondence Between Filtering in the Spatial and Frequency Domains, Frequency domain Image Smoothing and sharpening filter - Ideal, Butterworth , Gaussian	03
3		**IMAGE MORPHOLOGY AND RESTORATION**	**06**
	3.1	**Morphology:** Erosion and Dilation, Opening and Closing, The Hit-or-Miss Transformation, Boundary extraction , Hole filling, Thinning and thickening	04
	3.2	**Restoration** : A Model of the Image Degradation/Restoration Process, Noise models, Removal periodic noise, Principle of Inverse filtering	02
4		**IMAGE SEGMENTATION**	**08**
	4.1	**Point, Line, and Edge Detection:** Detection of Isolated Points, Line detection, edge models, Canny's edge detection algorithm , Edge linking : Local processing and boundary detection using regional processing (polygonal fitting)	05
	4.2	**Thresholding** : Foundation, Role of illumination and reflectance, Basic global thresholding	01
	4.3	**Region Based segmentation**: Region Growing, Region Splitting and merging	02
5		**INTRODUCTION TO MACHINE VISION AND DESCRIPTORS**	**05**
	5.1	Principle of machine vision , real world applications, chain code, simple geometric border representation, Fourier Transform of boundaries, Boundary description using segment sequences	03
	5.2	Introduction to Texture, co-occurrence matrix	02
6		**MACHINE VISION ALGORITHMS**	**08**
	6.1	Knowledge representation, Classification Principles, Classifier setting, Classifier Learning, Confusion Matrix	02
	6.2	K-means clustering algorithm, Introduction, bays decision theory continuous case, two category classification, Bayesian classifier ,Support vector machine	06
		TOTAL	**39**

Text Books:

1. Milan Sonka ,Vaclav Hlavac, Roger Boyle, “Image Processing, Analysis, and Machine Vision” Cengage Engineering, 3rd Edition, 2013
2. Gonzales and Woods, “Digital Image Processing”, Pearson Education, India, Third Edition,
3. R. O. Duda and P. E. hart, Pattern classification and scene analysis, Wiley Interscience publication
4. Christopher M. Bishop; Pattern Recognition and Machine Learning, Springer, 2006

Reference books:

1. Anil K.Jain, “Fundamentals of Image Processing”, Prentice Hall of India, First Edition, 1989.
2. W Pratt, “Digital Image Processing”, Wiley Publication, 3rd Edition, 2002
3. Forsyth and Ponce, Computer vision: A modern approach, PHI
4. Frank Y Shish ,Image Processing and Pattern Recognition: Fundamentals and Techniques, Wiley Wiley-IEEE Press, 2010

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC604	Artificial Neural Networks and Fuzzy Logic	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC604	Artificial Neural Networks and Fuzzy Logic	20	20	20	80	03	--	--	100

Course Prerequisites:

1. Basic linear Algebra
2. Engineering Mathematics-I to IV

Course Objectives:

1. To introduce the concepts and understanding of artificial neural networks
2. To provide adequate knowledge about supervised and unsupervised neural networks
3. To introduce neural network design concepts
4. To expose neural networks based methods to solve real world complex problems
5. To study the architecture of CNN and its application in image classification.
6. To introduce fuzzy logic and fuzzy inference systems

Course Outcomes:

After successful completion of the course, the student will be able to:

1. Comprehend the concepts of biological neurons and artificial neurons
2. Analyze the feed-forward and feedback neural networks and their learning algorithms.
3. Comprehend the neural network training and design concepts
4. Build a simple CNN model and apply in image classification
5. Analyze the application of neural networks and fuzzy logic to real world problems.

Module No.	Topics	Hrs.
1.0	**Introduction to Neural Networks and their Basic Concepts**	**07**
	Biological neuron and Artificial neuron, McCulloch-Pitts Model, Activation Function, various types of Activation Functions and types of Neural Network Architectures, Prerequisites for Training of Neural Networks. Linearly Separable and Linearly Non-Separable Systems with examples, Concepts of Supervised Learning, Unsupervised Learning, and Reinforcement Learning. Brief survey of applications of Neural Networks.	
2.0	**Supervised Learning Neural Networks**	**07**
	Perceptron - Single Layer Perceptron, Multilayer Perceptron and their Architecture. Error Functions: Mean Square Error and Sum Squared Error. Gradient Descent, Generalized delta rule, Error back propagation, Stopping Criterions for Training.	
3.0	**Unsupervised Learning Neural Networks**	**07**
	Competitive Learning Network – Kohonen Self-Organizing Networks – Architecture, Training Algorithm, Discrete Hopfield Network- Hopfield Matrix, Testing Algorithm, K-Means Clustering Algorithm.	
4.0	**Algorithms of Neural Networks**	**04**
	Basic concept of Machine Learning, Support Vector Machine (SVM) - Introduction and SVM based Binary Classifier, LMS Algorithm.	
5.0	**Convolution Neural Network (CNN)**	**07**
	Basic concept of Deep Learning, Convolution Operation, Overview of CNN Architecture, Input layer, Convolution layers, Pooling layers, Padding, Strided Convolutions, Rectified Linear Unit (ReLU), One Layer of a Convolutional Network, Fully Connected Layers, Complex Image Classification using CNN.	
6.0	**Introduction to Fuzzy Inference System**	**07**
	Introduction to Fuzzy Logic, Fuzzy Rules, Fuzzy Properties - Operations, Membership Functions, Fuzzification - Membership Value Assignments using Intuition Method, Defuzzification Methods -- Mean of Maxima and Centroid (Centre of Area) Methods, Fuzzy Inference System with reference to Mamdani Model, Brief Review of Applications of Fuzzy Logic to Speed Control of DC Motor and Washing Machine.	
	Total	**39**

Text Books:

1. S. N. Sivanandam and S. N. Deepa, Introduction to Soft Computing, Wiley India Publications, 3rd Edition.
2. Simon Haykin, Neural Networks and Learning Machines, Pearson Prentice Hall, 3rd Edition
3. S. Rajasekaran and G. A. Vijayalakshmi Pai, Neural Networks, Fuzzy Logic, and Genetic Algorithms, PHI Learning Pvt. Ltd, 2003.
4. Practical Convolutional Neural Networks by Mohit Sewak, Md. Rezaul Karim, Pradeep Pujari, Packt Publishing, 2018.
5. Timothy J. Ross, Fuzzy Logic with Engineering Applications, Wiley India Publications, 3rd Edition.

References:

1. Hagan, Demuth, and Beale, Neural Network Design, Thomson Learning, 2nd Edition.
2. Simon Haykin, Neural Network- A Comprehensive Foundation, Pearson Education, 2nd Edition.
3. Christopher M. Bishop, Neural Networks for Pattern Recognition, Oxford University Press, 2005.
4. William W. Hsieh, Machine Learning Methods in the Environmental Sciences: Neural Network and Kernels, Cambridge University Press, 2009.
5. Ian Goodfellow, Yoshua Bengio, Aaron Courville, Deep Learning, MIT Press, 2016
6. S. N. Sivanandam, S. Sumathi, and S. N. Deepa, Introduction to Neural Network using Matlab, Tata McGraw-Hill Publications, 2006.
7. Mehrotra Kishan, Mohan C. K. Ranka Sanjay, Elements of Artificial Neural Networks, Penram International Publishing Pvt. Ltd, 2nd Edition.
8. J. M. Zurada, Introduction to Artificial Neural Systems, Jaico Publishers, 2006.
9. Bart Kosko, Neural Networks and Fuzzy Systems, Pearson Education, 2007.

Recommended NPTEL / Swayam Course and Online resources:

1. Course: Fuzzy Logic and Neural Networks by Prof. Dilip Kumar Pratihar, IIT Kharagpur
2. Course: Neural Network and Applications by Prof. Somnath Sengupta, IIT Kharagpur
3. Michael Nielsen, "Neural Networks and Deep Learning", Determination Press, 2015. http://neuralnetworksanddeeplearning.com/

List of Suggested Experiments to be conducted in IPMV Laboratory (ECL 603):

1. Classification of Non-linearly Separable Binary Pattern using Multilayer Perceptron Neural Network.
2. Pattern Clustering using K-means Algorithm.
3. Binary Pattern Restoration using Discrete Hopfield Neural Network.
4. Image Classification using Support Vector Machine.
5. Object Recognition using Convolutional Neural Network.
6. Design Fuzzy Controller for Washing Machine

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus completed, and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on the entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6011	Mixed Signal VLSI	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 6011	Mixed Signal Design	20	20	20	80	03	--	--	100

Course Pre-requisite:

ECC302 – Electronic Devices and Circuits
ECC303 –Digital System Design
ECC403 – Linear Integrated Circuits
ECC503 – Digital VLSI

Course Objectives:

1. To know importance of Mixed Signal VLSI design in the field of Electronics and Telecommunication and emerging technologies.
2. To understand various methodologies for analysis and design of fundamental CMOS analog and mixed signal Circuits.
3. To learn various issues associated with high performance Mixed Signal VLSI Circuits
4. To design, implement and verify various mixed signal VLSI circuits using open source tools like Ngspice and Magic.

Course Outcomes:

After successful completion of the course student will be able to:

1. Know operation of the various building blocks of analog and mixed signal VLSI circuits.
2. Demonstrate the understanding of various building blocks and their use in design of analog and mixed signal circuits.
3. Derive expression for various performance measures of analog and mixed signal circuits in terms of parameters of various building blocks used to build the circuit.
4. Analyze and relate performance of analog and mixed signal VLSI circuits in terms of design parameters.
5. Evaluate and select appropriate circuit/configuration for given application.
6. Design analog and mixed signal VLSI circuits for given application.

Module No.	Unit No.	Topics	Hrs.
1.0		**Integrated Circuit Biasing Techniques**	**06**
	1.1	Active resistance, current source, current sink, simple current mirror, cascode current mirror	03
	1.2	Current and voltage references, Band gap reference generator	03
2.0		**Single Stage MOS Amplifiers**	**08**
	2.1	Common-source stage (with resistive load, diode connected load, current-source load, triode load, source degeneration), source follower, common-gate stage, cascode stage, folded cascade stage, simulation of CMOS amplifiers using SPICE	04
	2.2	Single-ended operation, differential operation, basic differential pair, large-signal and small-signal behavior, common-mode response, differential pair with MOS loads, simulation of differential amplifiers using SPICE	04
3.0		**Noise in MOS Circuits**	**06**
	3.1	Noise spectrum, correlated and uncorrelated noise sources, thermal noise, flicker noise, shot noise	02
	3.2	Representation of noise in circuits, noise in single stage CS, CD and CG amplifier	02
	3.3	Noise in differential pairs, noise bandwidth	02
4.0		**CMOS Operational Amplifier**	**05**
	4.1	Design of Current Mirror Load Differential Amplifier	02
	4.2	Design of two stage Operational Transconductance Amplifier, OpAmp Compensation techniques	03
5.0		**Data Converter Fundamentals**	**06**
	5.1	Analog versus digital discrete time signals, converting analog signals to data signals, sample and hold characteristics	03
	5.2	Mixed signal Layout issues, Floor planning, power supply and Ground issues, other interconnect Considerations	03
6.0		**Data Converter Architectures**	**08**
	6.1	DAC architectures, digital input code, charge scaling DACs, Cyclic DAC, pipeline DAC	04
	6.2	ADC architectures, flash, pipeline ADC, integrating ADC, and successive approximation ADC	04
		Total	**39**

Text Books:

1. B. Razavi, "*Design of Analog CMOS Integrated Circuits*", first edition, McGraw Hill,2001.
2. P.E.Allen and D R Holberg, "*CMOS Analog Circuit Design*", second edition, Oxford University Press, 2002.
3. R. Jacob Baker, "*CMOS Circuit Design, Layout and Simulation*", Wiley, 2nd Edition, 2013

Reference Books:

1. Adel S. Sedra, Kenneth C. Smith, A.N. Chandorkar, "Microelectronics Circuits Theory and Applications", Fifth Edition, Oxford University Press.
2. Gray, Meyer, Lewis and Hurst "*Analysis and design of Analog Integrated Circuits*", 4th Edition Willey International, 2002
3. Tony Chan Carusone, David Johns, Kenneth Martin, " Analog Circuit Design", Second Edition, Wiely

NPTEL / Swayam Course:

1. https://nptel.ac.in/courses/117/101/117101105/

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on completion of approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6012	Computer Organisation and Architecture	3	--	--	3	--	--	3

Course Code	Course Name	Examination Scheme								
		Theory Marks					Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam	Exam. Duration (in Hrs)			
		Test 1	Test 2	Avg.					
ECCDLO 6012	Computer Organisation and Architecture	20	20	20	80	03	--	--	100

Course Pre-requisites:

ECC303-Digital System Design

ECC402-Microcontrollers

Course objectives:

1. To have a thorough understanding of the basic structure and operation of a digital computer.
2. To understand memory systems, processor organization and generation of control unit signals.
3. To demonstrate the operation of various arithmetic algorithm including integer and floating point representation.
4. To understand the working principles of multiprocessor and parallel organization's as advanced computer architectures.

Course outcomes:

After successful completion of the course student will be able to -

1. Describe Computer system along with I/O operations and performance measures.
2. Demonstrate data representation and different arithmetic algorithm for solving ALU operations.
3. Categorize memory organization and identify the function of each element of memory hierarchy.
4. Demonstrate control unit operations.
5. Articulate design issues in the development of Multiprocessor organization & architecture

Module No.	Unit No.	Topics	Hrs
1		**Computer Organization, Architecture and Performance**	8
	1.1	Organization and Architecture,	
	1.2	Structure and Function,	
	1.3	Designing for Performance,	
	1.4	Multicore, MICs, and GPGPUs	
	1.5	Two Laws that Provide Insight: Amdahl's Law and Little's Law	
	1.6	Basic Measures of Computer Performance,	
	1.7	Calculating the Mean	
	1.8	Benchmarks and SPEC	
2		**Computer System**	6
	2.1	Computer Components	
	2.2	Computer Function	
	2.3	Interconnection Structures	
	2.4	Bus Interconnection	
3		**Data Representation and Arithmetic Algorithms**	5
	3.1	Unsigned & Signed multiplication- Add & Shift Method, Booth's algorithm. Unsigned & Signed division, Restoring and non-restoring division.	
	3.2	Integer and floating point representation, IEEE 754 standard for floating point (Single & double precision) number representation.	
4		**Memory System Organization**	7
	4.1	Classification and design parameters, Memory Hierarchy ,Internal Memory: RAM, SRAM and DRAM	
	4.2	Cache Memory: Characteristics of Memory Systems, Cache Memory Principles, Elements of Cache, Cache Coherence. Design problems based on mapping techniques	
	4.3	Virtual Memory, External Memory : Magnetic Discs, Solid State Drive, Optical Memory, Flash Memories, RAID Levels	
5		**Control Unit Design**	8
	5.1	Micro- Operations: The Fetch Cycle, The Indirect Cycle, The Interrupt Cycle, The Execute cycle, The Instruction Cycle	
	5.2	Control of the Processor: Functional Requirements, Control Signals, Internal Processor Organization	
	5.3	Hardwired Control Unit	
	5.4	Microinstructions Microprogrammed Control Unit, Advantages & disadvantages	
6		**Fundamentals of Advanced Computer Architecture**	5
	6.1	Parallel Architecture: Classification of Parallel Systems,	
	6.2	Flynn's Taxonomy, Array Processors, Clusters, and NUMA Computers	
	6.3	Multiprocessor Systems : Structure & Interconnection Networks	
	6.4	Multi-Core Computers: Introduction, Organization and Performance.	
		Total	**39**

Text Books:

1. William Stallings "Computer Organization and Architecture Designing for Performance" Tenth Edition, Pearson Education.
2. C. Hamacher, Z. Vranesic and S. Zaky, "Computer Organization", McGrawHill,
3. Andrew S. Tanenbaum "Structured Computer Organization", Pearson, Sixth Edition

Reference books:

1. D. A. Patterson and J. L. Hennessy, "Computer Organization and Design -
2. Morris Mano. "Computer System Architecture" Pearson Publication, 3rd Edition, 2007
3. J .P. Hayes, "Computer Architecture and Organization", McGraw-Hill, 1998
4. B. Govindarajulu, "Computer Architecture and Organization: Design Principles and Applications", Second Edition, Tata McGraw-Hill.

Internal Assessment:

Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal assessment. Duration of each test shall be of one hour.

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6013	Digital Forensic	3	--	--	3	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 6013	Digital Forensic	20	20	20	80	03	--	--	100

Course prerequisite:

ECC602: Computer Communication Networks

Course Objectives:

1. To understand cyber attacks and various categories of Cybercrime.
2. To discuss the need and process of digital forensics and Incident Response Methodology.
3. To explore the procedures for identification, preservation, and extraction of digital evidence.
4. To explore techniques and tools used in digital forensics for system investigation.
5. To discuss the investigation process of network and host based system intrusions.
6. To understand the laws related to Cybercrime

Course Outcomes:

On successful completion of the course, students will be able to

1. Study the various cybercrimes and its prevention methods.
2. Discuss the phases of Digital Forensics and methodology to handle the computer security incident.
3. Understand the process of collection, analysis and recovery of the digital evidence.
4. Explore various tools to perform the investigation of the crime scenario.
5. Investigate the process of monitoring and analysis of computer network traffic for network investigation.
6. Discuss the legal issues associated with the cyber laws.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Cybercrime and Hacking**	**08**
	1.1	Cybercrime, Categories of Cybercrime (Cybercrime against people, Cybercrime Against property, Cybercrime Against Government), Types of cybercrime (Violent- Cyber terrorism, Assault by Threat, Cyberstalking, Child Pornography, Non-violent - Cybertrespass, Cyber Theft, Cyberfraud, Destructive Cybercrimes), Computers' role in crimes	
	1.2	Hacking, Life cycle of Hacking, Types of Hackers (White Hat hackers, Black Hat hackers, Grey Hat hackers), Hacking techniques, Passive and Active Attacks, Social Engineering, Attacks vs Vulnerabilities, Prevention of Cybercrime	
		Self-learning topics: Distinction between computer crimes and conventional crimes.	
2.0		**Introduction to Digital Forensics**	**07**
	2.1	Objectives of digital forensics, Process of digital forensics, Types of digital forensics, Challenges faced by digital forensics	
	2.2	Introduction to Incident - Computer Security Incident, Goals of Incident Response, CSIRT, Incident Response Methodology, Phase after detection of an incident	
		Self-learning topics: Distinction between Computer virus, worm, Trojan horse and trap door.	
3.0		**Digital Evidence and Forensics Duplication**	**07**
	3.1	Digital evidence, Admissibility of evidence, Challenges in evidence handling, collecting digital evidence, Preserving digital evidence, Documenting evidence	
	3.2	Necessity of forensic duplication, Forensic duplicates as admissible evidence, Forensic image formats, Forensic duplication techniques, Disk imaging, Analysis of forensic images using FTK Imager	
		Self-learning topics: Digital Evidence Investigation using Autopsy	
4.0		**System Investigation**	**08**
	4.1	Live/volatile data collection from Windows and Unix Systems	
	4.2	Investigating Windows systems, Investigating UNIX systems, Investigating applications, Web browsers, Email tracing	
	4.3	Recovering digital evidence, Acquiring, Analyzing and duplicating data: dd, dcfldd, foremost, scalpel	
		Self-learning topics: Methods of storing data (RAM and Hard disk)	
5.0		**Network Forensics**	**05**
	5.1	Introduction to intrusion detection systems, Types of IDS, Understanding network intrusion and attacks	
	5.2	Analyzing network traffic, collecting network based evidence, Evidence handling. Investigating routers	
		Self-learning topics: Use of packet sniffing tools like Wireshark	
6.0		**Laws related to cyber crime**	**04**
		Constitutional law, Criminal law, Civil law, Levels of law: Local laws, State laws, Federal laws, International laws. Levels of culpability: Intent, Knowledge, Recklessness, Negligence. CFAA, DMCA, CAN Spam	
		Self-learning topics: Relevant law to combat computer crime –Information Technology Act	
		Total	**39**

Text books

1. Kevin Mandia, Chris Prosise, "Incident Response and computer forensics", Tata McGrawHill, 2006
2. "Scene of the Cybercrime: Computer Forensics" Handbook 1st Edition, Kindle Edition
3. "Digital Forensics", Nilakshi Jain & Kalbande, Wiley Publication
4. "Cyber Security", Nina Godbole, Sunit Belapure, Wiley Publication

Reference books

1. Bill Nelson,Amelia Phillips,Christopher Steuart, "Guide to Computer Forensics and Investigations" . Cengage Learning, 2014
2. Debra Littlejohn Shinder Michael Cross "Scene of the Cybercrime: Computer Forensics Handbook", 2nd Edition Syngress Publishing, Inc.2008.
3. Marjie T. Britz, Computer Forensics and Cyber Crime, Pearson, Third Edition.

Suggested MOOCs for Self-Learning:

1. Course on "Ethical Hacking"
   https://nptel.ac.in/courses/106/105/106105217/
2. Course on "Digital Forensics"
   https://onlinecourses.swayam2.ac.in/cec20_lb06/preview
3. Course on "Computer Forensics"
   https://www.edx.org/course/computer-forensics
4. Course on Cyber Incident Response
   https://www.coursera.org/learn/incident-response
5. Course on "Penetration Testing, Incident Responses and Forensics"
   https://www.coursera.org/learn/ibm-penetration-testing-incident-response-forensics

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6014	Database Management System	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (in Hrs.)	Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ECCDLO 6014	Database Management System	20	20	20	80	03	--	--	100

Course Pre-requisite:

FEC 205 : C Programming
ECL 304 : Skill Lab :- C++ and Java Programming
ECL 405 : Skill Lab :- Python Programming

Course Objectives:

1. Learn and practice data modeling using the entity-relationship and developing database designs.
2. Understand the use of Structured Query Language (SQL) and learn SQL syntax.
3. Understand the needs of database processing and learn techniques for controlling the consequences of concurrent data access
4. Understand the concept of database security and privacy

Course Outcome:

After successful completion of the course student will be able to

1. Describe the fundamentals of database systems, different data models and design issues in database.
2. Understand the basics model of relational Algebra, calculus, transaction management, concurrency control , database security and privacy
3. Design ER diagram, relational schemas, apply concepts of normalization to relational database design.
4. Implement views, triggers and querying the database using SQL.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Databases**	**02**
	1.1	Introduction to databases, History of database system, Benefits of Database system over traditional file system, relational databases, Three tier database architecture, Data independence	
2.0		**Data Models**	**03**
	2.1	The importance of data models, Introduction to various data models (hierarchical, Network, Relational, Entity relationship and object model), Basic building blocks, Business rules, Degrees of data abstraction	
3.0		**Database Design, ER-Diagram and Unified Modeling Language**	**08**
	3.1	Database design and ER Model: overview, ER-Model and its Constraints, ER-Diagrams, ERD Issues, weak entity sets	
	3.2	Codd‘s rules, Relational Schemas, Introduction to UML Relational database model: Logical view of data, keys, integrity rules. Relational Database design: features of good relational database design, atomic domain	
4.0		**Relational Algebra and Calculus**	**09**
	4.1	Relational algebra: Introduction, Selection and projection, set operations, renaming, Joins, Division, syntax, semantics.	
	4.2	Operators, grouping and ungrouping, relational comparison. Calculus: Tuple relational calculus, Domain relational Calculus, calculus vs algebra, computational capabilities.	
	4.3	Normalization methods : 1NF, 2NF, 3NF, BCNF, 4NF, 5NF	
5.0		**Constraints, Views and SQL**	**10**
	5.1	What is constraints, types of constrains, Integrity constraints,	
	5.2	SQL: data definition, aggregate function, Null Values, nested sub queries, Joined relations. Triggers.	
	5.3	Views: Introduction to views, data independence, security, updates on views, comparison between tables and views	
	5.4	** SQL Tools : MySQL, ORACLE 10G, POSTGRESQL	
6.0		**Transaction management and Concurrency control**	**07**
	6.1	Transaction management: ACID properties, serializability and concurrency control, Lock based concurrency control (2PL, Deadlocks), Time stamping methods, optimistic methods, database recovery management.	
	6.2	Database Security and privacy : Issues, Access Control based on grant and revoke privileges	
		Total	**39**

**** Teacher can select any one SQL Tool for implementation of SQL query**

Textbooks:

1. A Silberschatz, H Korth, S Sudarshan, "Database System and Concepts‖", Fifth Edition McGraw-Hill
2. Rob, Coronel, "Database Systems", Seventh Edition, Cengage Learning.
3. Ramez Elmasri, Shamkant B. Navathe, "Fundamentals of Database System‖", Seventh Edition, Person.
4. G. K. Gupta, "Database Management Systems‖", McGraw – Hill.

Reference Books:

1. Peter Rob and Carlos Coronel, "Database Systems Design, Implementation and Management", Thomson Learning, 5th Edition.
2. P.S. Deshpande, "SQL and PL/SQL for Oracle 11g, Black Book", Dreamtech Press
3. Mark L. Gillenson, Paulraj Ponniah, "Introduction to Database Management", Wiley
4. Raghu Ramkrishnan and Johannes Gehrke, "Database Management Systems", TMH
5. Debabrata Sahoo "Database Management Systems‖ Tata McGraw Hill, Schaum's Outline

E-Resources:

1. https://www.w3schools.in/dbms/
2. https://www.tutorialspoint.com/dbms/index.htm
3. https://www.studytonight.com/dbms/

Self-Learning: Suggested Case Studies (Any such cases can be selected by Teacher)

1. Library Management System
2. Hospital Management System
3. Pharmacy Management System
4. Human Resource Database Management System in Java
5. Students Database Management System
6. Employee Management System
7. Inventory Control Management database

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6015	IoT and Industry 4.0	3	-	--	3	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work Oral	Practical and Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ECCDLO 6015	IoT and Industry 4.0	20	20	20	80	03	--	--	100

Course pre-requisite:
ECM401: Mini Project – 1 B
ECC402: Microcontrollers
ECL404: Skill based Lab Course
ECM501: Mini Project 2A Embedded System Project

Course Objectives:

1. To offer introduction to Internet of Things and industry 4.0 standard
2. To understand the design features of Internet of Things (IoT)
3. To understand concepts of data management and data analytics in IoT
4. To understand the concept and framework of industry 4.0 standard
5. To understand the application of IoT and Industry 4.0 standard.

Course Outcome:

On successful completion of the course the students will be able to:

1. Discuss case studies and use cases of IoT design.
2. Illustrate various protocols of web connectivity.
3. Understand and use tools for data management and analytics in IoT.
4. Explain various frameworks for industry 4.0 standards.
5. Prepare case studies on applications of IIOT.
6. Understand advanced concepts and applications of industry 4.0

Module No.	Unit No.	Topics	Hrs.
1		**Introduction to IoT**	**04**
	1.1	**Introduction -** Defining IoT, Characteristics of IoT, Physical design of IoT, Logical design of IoT, Functional blocks of IoT, Sources of IoT, **IoT and M2M -** IoT/M2M System layers and Design standardization, Difference between IoT and M2M	
	1.2	**Defining Specifications About -** Purpose & requirements, process, domain model, information model, service, IoT level, Functional view, Operational view, Device and Component Integration, Application Development, Case Study	
2		**Network & Communication aspects**	**08**
	2.1	**Design Principles & Web Connectivity -** Web Communication Protocols for connected devices, Web connectivity using Gateway, SOAP, REST, HTTP, RESTful and Web Sockets (Publish —Subscribe), MQTT, AMQP, CoAP Protocols	
	2.2	**Internet Connectivity: -** Internet connectivity, Internet based communication, IP addressing in IoT, Media Access Control, Application Layer Protocols. LPWAN Fundamentals: LORA, NBIoT, CAT LTE Ml, SIGFOX, Case Study	
3		**Data Management and Analytics for IoT**	**08**
	3.1	Introduction, Apache Hadoop, Using Hadoop MapReduce for Batch Data Analysis, Apache Oozie, Apache Spark, ApacheStorm, Using Apache Storm for Real-time Data Analysis	
	3.2	Analysis, Structural Health Monitoring Case Study, Tools for IoT:- Chef, Chef Case Studies, Puppet, Puppet Case Study- Multi-tier Deployment, NETCONF-YANG Case Studies, IoT Code Generator	
4.0		**Introduction to Industry 4.0**	**08**
	4.1	Industry 4.0: Managing the Digital Transformation, Conceptual framework for Industry 4.0, Industrial IoT (IIoT) - Introduction, Business Model and Reference Architecture, Industrial IoT-Layers, Sensing, Processing, Communication.	
	4.2	Cyber Physical Systems and Next Generation Sensors, Collaborative Platform and Product Lifecycle Management, Augmented Reality and Virtual Reality	
5.0		**Introduction to Industrial IoT (IIoT)**	**06**
	5.1	Industrial IoT- Application Domains: Healthcare, Power Plants, Inventory Management & Quality Control, Plant Safety and Security, Facility Management.	
	5.2	Artificial Intelligence, Cybersecurity in Industry 4.0, Internet of Things for Industry 4.0 Design, Challenges and Solutions	
6.0		**Industry 4.0 Technologies and Applications**	**05**
	6.1	Internet of Things and New Value Proposition.: Examples for IoTs Value Creation in Different Industries., IoTs Value Creation Barriers: Standards, Security and Provacy Concerns	
	6.2	Introduction to Industry 5.0, Human Machine Interaction, cognitive computing with human intelligence, Case study on AI based solutions	
		Total	**39**

Text books:

1. ArshdeepBahga and Vijay Madisetti, "Internet of Things: A Hands-on Approach, Universities Press.
2. Raj Kamal, " Internet of Things: Architecture and Design Principles", McGraw Hill Education ,First edition
3. Radha Shankarmani, M Vijayalakshmi, "Big Data Analytics", Wiley Publications,
4. Andrew Minteer ,"Analytics for the Internet of Things(IoT)",Kindle Edition
5. Giacomo Veneri , Antonio Capasso," Hands-On Industrial Internet of Things: Create a powerful Industrial IoT infrastructure using Industry 4.0", Packt

Suggested reference material (research papers):

1. https://www.mdpi.com/2071-1050/11/16/4371/pdf- Industry 5.0—A Human-Centric Solution – MDPI (open access)
2. https://www.researchgate.net/profile/Mary-Doyle-Kent/publication/336819748_Industry_50_Is_the_Manufacturing_Industry_on_the_Cusp_of_a_New_Revolution/links/5e84b810a6fdcca789e5ff75/Industry-50-Is-the-Manufacturing-Industry-on-the-Cusp-of-a-New-Revolution.pdf - Industry 5.0: Is the Manufacturing Industry on the Cusp of a New Revolution?

Reference books

1. Alp Ustundag Emre Cevikcan," Industry 4.0: Managing The Digital Transformation", Springer Series in Advanced Manufacturing
2. G. R. Kanagachidambaresan, R. Anand, E. Balasubramanian, V. Mahima, Internet of Things for Industry 4.0. EAI/Springer Innovations in Communication and Computing
3. The Internet of Things (Connecting objects to the web) by Hakima Chaouchi (Wiley Publications).
4. The Internet of Things (MIT Press) by Samuel Greengard
5. Adrian McEwen, Hakim Cassimally, : Designing the Internet of Things", Paperback,First Edition

Suggested MOOCs:

1. https://onlinecourses.nptel.ac.in/noc20_cs69 - Introduction to Industry 4.0 and Industrial Internet of Things, By Prof. Sudip Misra, IIT Kharagpur
2. https://www.edx.org/course/industry-40-how-to-revolutionize-your-business - Industry 4.0: How to Revolutionize your Business
3. https://onlinecourses.nptel.ac.in/noc21_cs17 - Introduction to internet of things, by Prof. Sudip Misra , IIT Kharagpur
4. https://onlinecourses.nptel.ac.in/noc21_cs08 - Embedded Systems Design
5. By Prof. Anupam Basu, IIT Kharagpur

Recommended list of tools for self learning:

1. Node Red - https://nodered.org/
2. M2MLabs Mainspring - http://www.m2mlabs.com/
3. Tensor Flow - https://www.tensorflow.org/
4. Things Speak - https://thingspeak.com/

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6016	Radar Engineering	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme								
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 6016	Radar Engineering	20	20	20	80	03	--	--	100

Pre requisites:

ECC405 - Principles of Communication Engineering

Course objectives:

1. To interpret Radar equations
2. To explain different types of radar
3. To introduce RADAR transmitters and receivers for given conditions
4. To understand/ implement the plotting for given RADAR target

Course outcomes:

After successful completion of the course student will be able to

1. Explain generalized concept of RADAR.
2. Solve problems using radar equations.
3. Describe different types of radar for specific application.
4. Explain concept of tracking radar.
5. Plot the RADAR target from given specification.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Radar and Radar Equation**	**08**
	1.1	Basics Radar, Radar equation, Block Diagram, Radar Frequencies	
	1.2	Detection of signal in noise, Receiver Noise and Signal-to-noise Ratio	
	1.3	Probability of detection and false alarm: Simple, complex Targets, Pulse Repetition Fr	
2.0		**MTI and Pulse Doppler Radar**	**08**
	2.1	Introduction to Doppler and MTI radar, Doppler frequency shift	
	2.2	Simple CW Doppler radar, MTI radar block diagram	
	2.3	Delay line canceler	
	2.4	Moving-target-detection	
	2.5	Pulse Doppler radar	
3.0		**Tracking Radar**	**06**
	3.1	Monopulse tracking	
	3.2	Conical scan and sequential lobbing	
	3.3	Limitation of tracking accuracy , Low angle tracking	
4.0		**Radar Transmitters and Receviers**	**06**
	4.1	Radar RF power sources: Klystron	
	4.2	Travelling wave tube	
	4.3	Magnetron	
	4.4	Radar Receiver: Superheterodyne Receiver	
5.0		**Radar Clutters and landing system**	**06**
	5.1	Types of clutter : surface clutter, sea clutter, land clutter	
	5.2	Instrument landing system	
	5.3	Ground controlled approach, Microwave landing system	
	5.4	Radar altimeter	
6.0		**General ideas on RADAR plotting**	**05**
	6.1	Radar plotting -general ideas	
	6.2	Relative plotting (passive derivations), Relative plotting (action taken by target)	
	6.3	Radar Display: Types of displays	
		Total	**39**

Text Books:

1. Merill Skolnik,—Introduction to RADAR Systems,Tata McGrawHill, Third Edition
2. Merill Skolnik,—RadarHandbook, TataMcgrawHill, Second Edition
3. Dr. A. K. Sen, Dr. A. B. Bhattacharya- Radar Systems and Radio Aids to Navigation Khanna Publishers

Reference books:

1. Mark A.Richards,James A.Scheer, William A.Holm, —Principles of Modern Radar Basic Principals‖, ScitechPublishing.
2. SimonKingsley,ShaunQuegon,—UnderstandingRadarSystems‖,ScientechPublishing Inc.
3. G.S. N.Raju, —Radar Engineering and Fundamentals of Navigational Aids‖, I. K International publishing House Pvt.Ltd.
4. Dr. Arjun Singh -Radar Systems and Radio Aids to Navigation,McGraw-Hill Education Private Limited
5. CAPT. H. SUBRAMANIAM- Shipborne Radar And Arpa Nutshell Series Book3

Online Resource:

1. NPTEL online Course: https://nptel.ac.in/courses/108/105/108105154/

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus completed and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ECL601	Electromagnetics and Antenna Lab	--	02	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam			
		Test 1	Test 2	Ave. of Test 1 and Test 2				
ECL601	Electromagnetics and Antenna Lab	--	--	--	--	25	25	50

Prerequisites:

1. Vector Calculus
2. Fundamental concepts of electricity and magnetism

Course Objective:

The objective of the course is to make student familiar with Maxwell's equation and its usefulness to describe different electromagnetic phenomena such as wave propagation, radiations from antenna etc.

Course Outcomes:

After successful completion of the course student will be able to

1. Students will be able to describe electromagnetics field including static and dynamic in terms of Maxwell's equations.
2. Students will be able to apply Maxwell's equation to solve various electromagnetic phenomenon such as electromagnetic wave propagation in different medium, power in EM wave.
3. Students will derive the field equations for the basic radiating elements and describe basic antenna parameters like radiation pattern, directivity, gain etc.
4. Students will be able to implement different types of the antenna structures such as Antenna arrays, Microstrip antenna and reflector antenna etc.

SUGGESTED LIST OF EXPERIMENTS

Sr. No.	NAME OF EXPERIMENTS
1.	Study different Antenna parameters (compulsory to use: FSM, Spectrum Analyzer and VNA)
2.	Introduction to Different Antenna Types
3.	Study of Wire Antenna, (Radiation pattern of dipole, folded dipole and Monopole antenna, various loops)
4.	Study of Directive antenna, Yagi-Uda Antenna
5.	Study of Broad-band Antenna, Log-periodic Antenna
6.	Study of Antenna Arrays (Broadside, End-fire, Parametric study for various arrays parameters)
7.	Study of Aperture Antennas (Parabolic/ Hyperbolic/ Horn , with or without Reflector)
8.	Study of Regular shaped Microstrip Antenna
9.	Small Project report can be considered as a part of term-work (Design, Simulation and validation).
10.	Case Study of Recent reported variations of Antenna types (Paper from reputed journal is to be referred and thoroughly study and present the report, maximum four students per group)

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the ― Laboratory session batch wise".

Computation/ simulation-based experiments are also encouraged. The experiments should be students centric, and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini project can be conducted for maximum batch of four students.

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL602	Computer Communication Network Laboratory	-	02	-	--	01	--	01

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam			
		Test 1	Test 2	Avg. of Test 1 and Test 2				
ECL602	Computer Communication Network Laboratory	--	--	--	--	25	25	50

Lab Course Outcomes: -

Upon completion of the computer communication networks lab, the students will be able to:

- Design a small or medium sized computer network including media types, end devices, and interconnecting devices that meets a customer's specific needs.
- Perform configurations on routers and Ethernet switches.
- Demonstrate knowledge of programming for network communications.
- Simulate computer networks and analyze the simulation results.
- Troubleshoot connectivity problems in a host occurring at multiple layers of the OSI model.
- Develop knowledge and skills necessary to gain employment as computer network engineer and network administrator.

Laboratory plan

Minimum of 8 practicals should be conducted and a mini project.

Suggested list of experiments:

1. To study basic networking commands. (Linux/Netkit)
2. To prepare a patch cable (straight-through, crossover, rollover) using UTP, RJ-45 and crimping tool. Test the cable using a cable tester and use it in LAN.
3. To configure and compare different network topologies using Cisco Packet Tracer
4. To study and compare network hardware components using Cisco Packet Tracer
5. To configure static routes in a network using Cisco Packet Tracer.
6. To configure a network with Distance Vector Routing Protocol-RIP using Cisco Packet Tracer and check the updated routing tables.
7. To configure a network with Path Vector Routing Protocol- BGP using Cisco Packet Tracer and check the updated routing tables.

8. To configure a network with Link state Routing Protocol- OSPF using Cisco Packet Tracer and check the updated routing tables.
9. To configure a network with Hybrid Routing Protocol- EIGRP using Cisco Packet Tracer and check the updated routing tables.
10. To perform subnetting using Cisco Packet Tracer/Netkit
11. To install a network simulator (NS2.35), create a wired network and compare the performance of TCP and UDP **or** Compare TCP and UDP performance using Netsim
12. To Simulate and study stop and Wait protocol using NS 2.35/ C++
13. To Simulate Sliding Window protocol using NS 2.35/C++
14. To Simulate and study the implementation of TCP/IP stack using wireshark (observe the protocols, data formats, header structures, addresses, payload sizes and encapsulation at each layer)
15. To perform HDLC bit stuffing and de-stuffing using C++
16. To configure DNS, DHCP, TELNET, FTP, SMTP server (any one) on Cisco Packet Tracer
17. To compare performance of ALOHA and Slotted ALOHA using Netsim.

Term Work: At **least 08 Experiments** covering entire syllabus must be given during the "Laboratory session batch wise". Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented **one mini-project** can be conducted for a batch of maximum four students.

Term work assessment must be based on the overall performance of the student with every experiment and assignment graded from time to time. The grades will be converted to marks as per **"Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done. The practical and oral examination will be based on entire syllabus.

Termwork marks distribution: Journal and practical Performance: 15 marks

Attendance: 5 marks

Assignment: 5 marks

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL603	Image Processing and Machine Vision Laboratory	--	02	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam			
		Test 1	Test 2	Avg.				
ECL603	Image Processing and Machine Vision Laboratory	--	--	--	--	25	25	50

Prerequisites:

1. Python Programming Skill Lab

Course Objectives:

1. To teach implementing basic theoretical concepts in Image Processing and Machine Vision using relevant software.
2. To give an exposure to students to object recognition/ classification techniques in Machine Vision.
3. To facilitate students for understanding practical aspects of Image Processing and Machine Vision through an application.

Course Outcomes:

After successful completion of the course student will be able to

1. perform enhancement of digital images in spatial and frequency domain
2. perform edge detection and morphological operations on digital images
3. classify patterns using standard Machine vision classification techniques like SVM
4. apply theoretical knowledge in image processing and machine vision to practical case studies

SUGGESTED LIST OF EXPERIMENTS

1. Eight experiments covering the whole syllabus with proportional weightage to Image Processing and Machine Vision, to be set with predefined and concrete objective problem statement.
2. At least 5 programs to be conducted in python programming
3. At least 1 case study from suggested ones to be conducted in lab.
4. An attempt should be made to make experiments more meaningful, interesting and innovative.
5. Conduct three experiment based on application of **Neural Network and Fuzzy logic** for Image Processing.

Sr. No.	NAME OF EXPERIMENTS
1.	Point Processing Methods - Negative, Log, Power law, Contrast stretching, Bit plane slicing
2.	Histogram calculation and equalization
3.	Spatial Domain Filtering: 1. Smoothening filters 2. Sharpening with Laplacian 3. Unsharp masking & high boost filtering 4. Edge detection using 1^{st} and 2^{nd} order derivatives
4.	Frequency Domain Filtering : Ideal, Butterworth and Gaussian
5.	Morphological operation – Erosion, dilation, opening, closing, hit-miss transform, Boundary extraction
6.	Image segmentation using global Thresholding Algorithm
7.	Shape representation using chain code
8.	Canny edge detection
9.	Feature extraction using co-occurrence matrix
10.	Classification using k-means algorithm
11.	Classification using Basiyan classifier
12.	Basic binary classification of any data or pattern using Support Vector Machine.
13.	Case Study : 1. Face recognition 2. Finger print identification 3. License plate recognition

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL604	Skill Laboratory: Linux & Networking & Server Configuration	--	04	--	--	02	--	02

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam			
		Test 1	Test 2	Avg. of Test 1 and Test 2				
ECL604	Skill Laboratory: Linux & Networking & Server Configuration	--	--	--	--	25	25	50

Course pre-requisite:
FEL204__C-Programming

Course Objectives:
1. Install Linux and implement standard Linux commands
2. Study basic theory of Linux Operating System
3. Implement the system administrative functionality
4. To write shell script programs to solve problems
5. Study basic commands of networking
6. Develop implementation skill of different servers on Linux

Course Outcome:
After successful completion of the course student will be able to :-
1. Install Linux using different platform and execute standard Linux commands.
2. Describe the basic knowledge of Linux Operating System
3. Deploy the system administrative functionality
4. Solve the problems using shell script programming
5. Develop network based applications
6. Apply the Linux commands using programming skill to deploy different servers like ftp, telnet etc.

Module No.	Unit No.	Topics	Hrs.
1.0		**Overview of Linux**	**08**
	1.1	Installing Software on Debian Based Linux: Debian, Ubuntu, Kali Linux	
	1.2	Overview of Unix and Linux architectures, Linux files system, Linux standard directories, Linux Directory Structure, Basic Linux Commands, Linux Networking commands, Viewing Files and the Nano Editor, Editing Files in Vi, Graphical Editors, Deleting, Copying, Moving, and Renaming Files	
2.0		**Linux OS**	**06**
	2.1	Linux Design Principles, Linux Booting Process, Kernel Modules, Process Management, Scheduling, Memory Management, Input and Output, Inter-process Communication.	
3.0		**System Administration**	**08**
	3.1	Common administrative tasks, Configuration and log files, Role of system administrator, Managing user accounts –adding, deleting users, Changing permissions and ownerships, Creating and managing groups, Modifying group attributes.	
	3.2	Temporary disabling of users accounts, Creating and mounting file system, becoming super user using su, Getting system information with uname, host name. Disk partitions & sizes, users, kernel, installing and removing packages, rpm command	
4.0		**Shell programming**	**12**
	4.1	Basics of shell programming, various types of shell available in Linux, Shell programming in bash, Conditional statements, Looping statements, Case statements, Parameter passing and arguments	
	4.2	System shell variables, Shell variables, shell keywords, Creating Shell programs for automating system tasks, Scheduling repetitive jobs using cron.	
5.0		**Linux Networking**	**08**
	5.1	Basics of Network Management, Setting up Dynamic and Static Addressing, Monitoring network services, Talking with DNS Servers, Remote System Administration with OpenSSH-Server & Putty.	
	5.2	TCP/IP Networking for Linux System Administrators, DNS and hostnames, DHCP, , Network Troubleshooting.	
6.0		**Servers and Configurations**	**10**
	6.1	Create and configure DHCP, Mail, DNS, FTP, Squid, Apache, Telnet, Samba servers	
		Total	**52**

Suggested List of Experiments:

Sr.	Title
1	Linux Installation process using following method CD-ROM, Network Installation or Kickstart Installation.
2	Basic commands to create users, change permission, software selection and installation and do changes in Grub file.
3	Practical on configuration of Linux disk Management such as SWAP, LVM, RAID, Primary Partition, Extended Partition and Linux files system.
4	Write a shell script to show various system configuration like currently logged user and his logname, your current shell, home directory, operating system type, current path setting, current working directory, show currently logged number of users, show memory information, Hard disk information like size of hard-disk, cache memory, model etc, and file system mounted.
5	Write a shell script to add user and password on Linux system.
6	Write a shell script to print last login details.

7	Write a shell script to upgrade and cleans the system automatically instead of doing it manually.
8	Write a shell script to delete all log files present inside your var/log directory.
9	Write a script that accepts the hostname and IP address as command-line arguments and adds them to the /etc/hosts file.
10	Write a awk script to find the number of characters, words and lines in a file?
11	Write a shell script that delete all lines containing a specified word
12	write a shell script to find the factorial of given integer
13	Configuration of DHCP Server and Client
14	Configuration of DNS Server with Domain Name.
15	Configuration of NFS File server and transfer files to a windows client.
16	Setting up a Samba Server and creating a print server.
17	Configuration of Internet Server by creating a Proxy Server and configure browser to use as a proxy.
18	Configuration of Mail Server
19	Configuration of Web Server.
20	Configuration of FTP server and transfer files to demonstrate the working of the same.

Text books:

1. YeswantKanethkar – "UNIX Shell Programming", First edition, BPB.

2. Cristopher Negus – "Red Hat Linux Bible", Wiley Dreamtech India 2005 edition..

3. Jason Cannon ,"Linux for Beginners: An Introduction to the Linux Operating System and Command line"

4. W. Stevens , Stephen Rago , "Advanced Programming in the UNIX Environment", Addison-Wesley Professional Computing Series

Reference books:

1. Official Red Hat Linux Users guide by Redhat, Wiley Dreamtech India

2. Graham Glass & King Ables – UNIX for programmers and users, Third Edition, Pearson Education.

3. Neil Mathew & Richard Stones – Beginning Linux Programming, Fourth edition, Wiley Dreamtech India.

4. Richard Petersen, Linux: The Complete Reference, Sixth Edition

Software Tools:
1. Install Ubuntu desktop | Ubuntu
2. Chapter 4. Quick Installation Guide Red Hat Enterprise Linux 7 | Red Hat Customer Portal
3. Installation | Kali Linux Documentation

Online Repository:
1. How to Install a DHCP Server in Ubuntu and Debian (tecmint.com)
2. How to Install and Configure Postfix as a Send-Only SMTP Server on Ubuntu 16.04 | DigitalOcean
3. Network - DHCP | Ubuntu

Term Work: At least **12 experiments** covering entire syllabus should be set to have well predefined inference and conclusion. Teacher should refer the suggested experiments and can design additional experiment to maintain better understanding and quality. The experiments should be students centric as well as real time and attempt should be made to make experiments more meaningful, interesting and innovative.
Term work assessment must be based on the overall performance of the student with every Experiments are graded from time to time.
The grades will be converted to marks as per "Choice Based Credit and Grading System" manual and should be added and averaged. Based on above scheme grading and term work assessment should be done. **The practical and oral examination will be based on entire syllabus.** Students are encourages to share their experiments codes on online repository. **Practical exam should cover all 12 experiments for examination.**

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECM601	Mini Project 2B: FPGA based Project	--	$04^{\$}$	--	--	2	--	2

$ Indicates work load of a learner (Not Faculty) for Mini Project 2B. Faculty Load: 1 hour per week per four groups.

Course Code	Course Name	Examination Scheme							
		Theory Marks					Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam	Exam. Duration (in Hrs)			
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ECM601	Mini Project 2B: FPGA based Project	--	--	--	--	--	25	25	50

Course Pre-requisite:

1. ECC303 - Digital Design
2. ECM401- Mini Project 1B
3. ECC503- Digital VLSI

Course Objectives :-

1. To train students for FPGA based project implementation and management
2. To make students VLSI industry ready
3. To make students familiar with the Verilog Programming
4. To make students familiar with the targeted FPGA design and implementation
5. To familiarize students with the numerous FPGA solutions available in Market
6. To familiarize the students with the Interfacing of FPGA boards

Course outcomes :

1. Understand various FPGA families and method of FPGA synthesis and implementation
2. Learn the working of basic EDA tools like Xilinx, Modelsim cadence , etc
3. Able to program, simulate and synthesize circuits in Verilog HDL.
4. Learn the technique of interfacing of LED, switches and seven segment with FPGA.
5. Learn the project documentation, designing and handling techniques
6. Analysis of FPAG fault detection and verification principles

1. Guideline to maintain quality of mini project are as follows :

1. To achieve proper selection of Mini Projects. Students should do survey of FPGA boards, tools and identify needs, which shall be converted into problem statement for mini project in consultation with faculty supervisor/head of department/ internal committee of faculties.
2. Students shall submit implementation plan in the form of Smart Report/Gantt/PERT/CPM chart, which will cover weekly activity of mini project.
3. A log book to be prepared by each group, wherein group can record weekly work progress, guide/ supervisor can verify and record notes/comments.
4. Faculty supervisor may give inputs to students during mini project activity; however, focus shall be on self-learning.

5. The solution to be verified with standard tools and procedures and report to be compiled in standard format of University of Mumbai.
6. **Suggested steps for mini project selection and implementation**
    i. Mini project should be completely FPGA based
    ii. Follow these steps
        1. Take specification, using these specifications design project.
        2. Select proper FPGA considering features and requirements of project. Create UCF file
        3. Program it using Verilog and write test benches for verification of each module
        4. Test Functional Simulation and verify it using simulation tool
        5. Synthesize, map and place and rout the design using synthesis tool
        6. Generate bit stream and download on FPGA
        7. Verify results on FPGA hardware/hardware setup made for project

2. **Project Topic selection and approval :-**
    1. The group may be of maximum **FOUR (04)** students.
    2. Topic selection and approval by **2 Expert** faculty from department at the start of semester
    3. **Log Book** to be prepared for each group to record the work progress in terms of milestones per week by students. Weekly comment, remarks to be put by guiding faculty. Both students and faculty will put signature in it per week. The log book can be managed **online** with proper authentication method using google sheets/forms or open source project management software.
3. **Project Report Format:**
    1. Report should not exceed **15 pages**. Simply staple it to discourage use of plastic.
    2. The recommended report format is in LaTeX.

<u>Term Work:</u>

1. **Term Work evaluation and marking scheme:**
    a. The review/ progress monitoring committee shall be constituted by Head of Departments of each institute.
    b. The progress of mini project to be evaluated on continuous basis, minimum two reviews in each semester.
    c. At end of semester the above 2 expert faculty who have approved the topic will internally **evaluate the performance**.
    d. Students have to give presentation and demonstration on the FPGA Based Mini Project- 2-B
    e. In the evaluation each individual student should be assessed for his/her contribution, understanding and knowledge gained about the task completed. Based upon it the marks will be awarded to student.
    f. **<u>Distribution of 25 Marks scheme</u> is as follows:**
        i. Marks awarded by guide/supervisor based on log book : 10
        ii. Marks awarded by review committee : 10
        iii. Quality of Project report : 05
2. **Guidelines for Assessment of Mini Project Practical/Oral Examination:**
    a. Report should be prepared as per the guidelines issued by the University of Mumbai.
    b. Mini Project shall be assessed through a presentation and demonstration of working model by the student project group to a panel of Internal and **External Examiners preferably from industry or research organisations** having experience of more than five years approved by head of Institution.
    c. Students shall be motivated to publish a paper based on the work in Conferences/students competitions.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to FPGA and Synthesis**	04
	1.1	Compare FPGA, ASIC, SOC, Basic FPGA architecture, Compare various FPGA Boards**, Understanding VLSI Design flow	
	1.2	**Understanding Tools :** Functional simulation , Synthesis and implementation, Synthesis tool flow, Implementation and bit generation, making User constraint files (UCF)	
	1.3	**Study Material** : https://www.xilinx.com/support/university/ise/ise-workshops/ise-fpga-design-flow.html	
2.0		**Writing First program in Verilog**	04
	2.1	**Introduction to Verilog**: Module definition, port declaration, connecting ports, Writing first Testbench	
	2.2	**Exercise :** Program for All gates, Writing Test bench and UCF	
	2.3	**Study Material**: https://www.xilinx.com/support/university/ise/ise-teaching-material/hdl-design.html	
3.0		**Combinational design Using VERILOG**	08
	3.1	Gate Level Modelling, hierarchical name referencing, **Data Flow Modelling**: Continuous assignments, delay specification, expressions, operators, operands, operator types	
	3.2	**Exercise:** Programming and FPGA implementation of Adders, 4-bit adders, Mux and decoders, **Interfacing** LED, switches with FPGA	
	3.3	**Study Material :** https://onlinecourses.nptel.ac.in/noc20_cs63/preview	
4.0		**Sequential design Using VERILOG**	08
	4.1	**Behavioral Modelling :** Structured procedures, initial and always, blocking 'and non-blocking statements, delay control, event control, conditional statements, multi way branching, loops, sequential and parallel blocks **Advanced topics:** Tasks and Functions, generic programming with parameters.	
	4.1	**Exercise:** Programming and FPGA implementation of Counters FFs and Shift registers Interfacing Seven Segment Display, UART with FPGA	
5.0		**Project Outline**	08
	5.1	Clocked Synchronous State-Machine Analysis, State-Machine Structure, Output Logic, Characteristic Equations Analysis of State Machines with D Flip-Flops, Clocked Synchronous State-Machine Design, Designing State Machines Using State Diagrams, State Tables	
	5.2	**Project Design Steps**: Designing state diagram, block diagram of project, Selection of FPGA for project, Selection of synthesis and simulation tool.	
6.0		**Project Implementation and management**	20
	6.1	Git Repositories, Learning of Project management software's like CVS, SVN etc	
	6.2	Project Implementation: Verilog coding, simulation, Synthesis, Bit generation and downloading on FPGA. .	
	6.3	Result verification and testing	
		Total	**52**

Reference books:

1. Samir Palnitkar, "Verilog HDL A guide to Digital Design and Synthesis" , 2nd Edition, Pearson Education, 2009
2. Simon D Monk, "Programming FPGAs : Getting started with Verilog", 1st Edition, McGraw Hill Eduction-2016
3. M. Morris Mano, Michael D. Ciletti , "Digital Design: With a Introduction to the Verilog Hdl", Pearson Prentice Hall, 2013
4. David Romano, "Make: FPGAs: Turning Software into Hardware with Eight Fun and Easy DIY", Shroff/Maker Media; First edition,2016
5. Frank Vahid, "Digital Design", Wiley India Private Limited; Preview edition, 2009
6. Behrooz Parhami ,"COMPUTER ARITHMETIC Algorithms and Hardware Designs", , Oxford University Press, 2010
7. Clive Maxfield ,"Design Warrior's Guide to FPGA", 2004, Elsevier

Reference links:

1. https://www.sanfoundry.com/vlsi-questions-answers-aptitude-test/
2. Free Tool : https://www.edaplayground.com/
3. https://github.com/

****Suggested FPGA Hardware Boards:**

1. Numato FPGA boards - https://numato.com/shop/
2. Papilio FPGA boards - http://store.gadgetfactory.net/fpga/
3. CMOD s6 - https://store.digilentinc.com/cmod-s6-breadboardable-spartan-6-fpga-module/
4. TinyFPGA - https://tinyfpga.com/
5. Zync,Zed Board - https://www.xilinx.com/products/silicon-devices/soc/zynq-7000.html
6. Artix -7, Kinetex Boards - https://store.digilentinc.com/arty-a7-artix-7-fpga-development-board/

Suggested Software tools:

1. Xilinx ISE Webpack
2. Modelsim/Questasim
3. Leonardo spectrum
4. MATLAB
5. Quartus
6. Actel
7. Icarus Verilog Simulator

Suggested Projects (FPGA downloading is must)

1) Shift-Add Multiplication,
2) Hardware Multipliers
3) Programmed Multiplication
4) Shift-Subtract Division
5) CORDIC Algorithm
6) Design of functions such as reciprocal, square root, sine, cosine, exponential
7) Wallace Multiplier
8) 8- Bit ALU
9) Matrix Multiplication
10) Booths Multiplier
11) NRZ,NRZI etc coding techniques

Suggested Courses

1. NPTEL Verilog Programming - Free
2. Workshops -Xilinx University Program- Freely available

Suggested Competitions for Funding

1. Government Swadeshi Microprocessor Challenge
2. IICDC – TI challenge
3. Sankalp Semiconductors Hackathons

University of Mumbai

No. AAMS_UGS/ICC/2022-23/ 118

CIRCULAR :-

Attention of the Principals of the Affiliated Colleges and Directors of the recognized Institutions in Faculty of Science & Technology is invited to this office circular No.UG/42 of 2018-19 dated 25th June, 2018 relating to the revised syllabus for the T.E. & B.E. in Electronics & Telecommunication Engineering (Sem.– V to VIII) (CBCS).

They are hereby informed that the recommendations made by the Ad-hoc Board of Studies in Electronics and Telecommunication Engineering at its meeting held on 11th May, 2022 and subsequently passed in the Faculty and then by the Board of Deans at its meeting held on 5th July, 2022 vide item No. 6.30 (R) have been accepted by the Academic Council at its meeting held on 11th July, 2022 **vide** item No. 6.30 (R) and that in accordance therewith, the revised syllabus of B.E.(Electronics and Telecommunication Engineering) (Sem. – VII & VIII) (CBCS) (REV-2019 'C' Scheme) has been brought into force with effect from the academic year 2022-23. (The circular is available on the University's website www.mu.ac.in).

MUMBAI – 400 032
20th October, 2022

(Dr. Shailendra Deolankar)
I/c Registrar

To

The Principals of the Affiliated Colleges and Directors of the recognized Institutions in Faculty of Science & Technology.

A.C/6.30(R)/11/07/2022

****

No. AAMS_UGS/ICC/ 2022-23/ 118   20th October, 2022

Copy forwarded with Compliments for information to:-

1) The Dean, Faculty of Science & Technology,
2) The Chairman, Ad-hoc Board of Studies in Electronics and Telecommunication Engineering,
3) The Director, Board of Examinations and Evaluation,
4) The Director, Board of Students Development,
5) The Director, Department of Information & Communication Technology,
6) The Co-ordinator, MKCL.

(Dr. Shailendra Deolankar)
I/c Registrar

Desktop/Circular of Engineering/Priya

Copy to :-

1. **The Deputy Registrar, Academic Authorities Meetings and Services (AAMS),**
2. **The Deputy Registrar, College Affiliations & Development Department (CAD),**
3. **The Deputy Registrar, (Admissions, Enrolment, Eligibility and Migration Department (AEM),**
4. **The Deputy Registrar, Research Administration & Promotion Cell (RAPC),**
5. **The Deputy Registrar, Executive Authorities Section (EA),**
6. **The Deputy Registrar, PRO, Fort, (Publications Section),**
7. **The Deputy Registrar (Special Cell),**
8. **The Deputy Registrar, Fort/Vidyanagari Administration Department (FAD) (VAD), Record Section,**
10. **The Professor-cum- Director, Institute of Distance and Open Learning (IDOL Admin), Vidyanagari,**

They are requested to treat this as action taken report on the concerned resolution adopted by the Academic Council referred to in the above circular and that on separate Action Taken Report will be sent in this connection.

1. **P.A. to Hon'ble Vice-Chancellor,**
2. **P.A. to Pro-Vice-Chancellor,**
3. **P.A. to Registrar,**
4. **All Deans of all Faculties,**
5. **P.A. to Finance & Account Officer, (F. &. A.O.),**
6. **P.A. to Director, Board of Examination & Evaluation,**
7. **P.A. to Director, Innovation, Incubation and Linkages,**
8. **P.A. to Director, Board of Lifelong Learning and Extension (BLLE),**
9. **The Director, Dept. Of Information and Communication Technology (DICT) (CCF & UCC), Vidyanagari,**
10. **The Director of Board of Student Development,**
11. **The Director, Department of Students Welfare (DSD),**
12. **All Deputy Registrar, Examination House,**
13. **The Deputy Registrars, Finance & Accounts Section,**
14. **The Assistant Registrar, Administrative sub-campus Thane,**
15. **The Assistant Registrar, School of Engg. & Applied Sciences, Kalyan,**
16. **The Assistant Registrar, Ratnagiri Sub-centre, Ratnagiri,**
17. **The Assistant Registrar, Constituent Colleges Unit,**
18. **BUCTU,**
19. **The Receptionist,**
20. **The Telephone Operator,**
21. **The Secretary MUASA,**

for information.

AC – 11 July, 2022
Item No. – 6.30 (R)

University of Mumbai

Revised Syllabus for

B.E. (Electronics & Telecommunication Engineering)

(Sem. - VII to VIII)

(Choice Based Credit System)

(With effect from the academic year 2022-23)

University of Mumbai

Syllabus for Approval

O: ____________	Title of Course	B.E. (Electronics and Telecommunication Engineering)
O: ____________	Eligibility	After Passing Third Year Engineering as per the Ordinance 0.6243
R: ____________	Passing Marks	40%
No. of years/Semesters:		8 semesters
Level:		~~P.G.~~ / U.G./ ~~Diploma / Certificate~~
Pattern:		~~Yearly~~ / Semester
Status:		New / Revised
To be implemented from Academic Year :		With effect from Academic Year : 2022-23

Dr Faruk Kazi
Chairman
of Ad-hoc Board of Studies in Electronics and Telecommunication Engineering

Dr. Suresh K. Ukarande
Associate Dean,
Faculty of Science and Technology

Dr Anuradha Majumdar
Dean,
Faculty of Science and Technology

Preamble

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Science and Technology (in particular Engineering)of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth and approach of course to be taught, which will enhance learner's learning process. Choice based Credit and grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. Credit assignment for courses is based on 15 weeks teaching learning process, however content of courses is to be taught in 13 weeks and remaining 2 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

There was a concern that the earlier revised curriculum more focused on providing information and knowledge across various domains of the said program, which led to heavily loading of students in terms of direct contact hours. In this regard, faculty of science and technology resolved that to minimize the burden of contact hours, total credits of entire program will be of 171, wherein focus is not only on providing knowledge but also on building skills, attitude and self learning. Therefore in the present curriculum skill based laboratories and mini projects are made mandatory across all disciplines of engineering in second and third year of programs, which will definitely facilitate self learning of students. The overall credits and approach of curriculum proposed in the present revision is in line with AICTE model curriculum.

The present curriculum will be implemented for Second Year of Engineering from the academic year 2020-21. Subsequently this will be carried forward for Third Year and Final Year Engineering in the academic years 2021-22, 2022-23, respectively.

Dr. S. K. Ukarande
Associate Dean
Faculty of Science and Technology
University of Mumbai

Dr Anuradha Muzumdar
Dean
Faculty of Science and Technology
University of Mumbai

Incorporation and Implementation of Online Contents from NPTEL/ Swayam Platform

The curriculum revision is mainly focused on knowledge component, skill based activities and project based activities. Self learning opportunities are provided to learners. In the revision process this time in particular Revised syllabus of 'C ' scheme wherever possible additional resource links of platforms such as NPTEL, Swayam are appropriately provided. In an earlier revision of curriculum in the year 2012 and 2016 in Revised scheme 'A' and 'B' respectively, efforts were made to use online contents more appropriately as additional learning materials to enhance learning of students.

In the current revision based on the recommendation of AICTE model curriculum overall credits are reduced to 171, to provide opportunity of self learning to learner. Learners are now getting sufficient time for self learning either through online courses or additional projects for enhancing their knowledge and skill sets.

The Principals/ HoD's/ Faculties of all the institute are required to motivate and encourage learners to use additional online resources available on platforms such as NPTEL/ Swayam. Learners can be advised to take up online courses, on successful completion they are required to submit certification for the same. This will definitely help learners to facilitate their enhanced learning based on their interest.

Dr. S. K. Ukarande	Dr Anuradha Muzumdar
Associate Dean	Dean
Faculty of Science and Technology	Faculty of Science and Technology
University of Mumbai	University of Mumbai

Preface By BoS

Technological developments in the field of electronics and telecommunication engineering have revolutionized the way people see the world today. Hence, there is a need for continuously enriching the quality of education by a regular revision in the curriculum, which will help our students achieve better employability, start-ups, and other avenues of higher studies. The current revision in the Bachelor of Engineering program (REV- 2019 'C' Scheme) aims at providing a strong foundation with required analytical concepts in the field of electronics and telecommunication engineering.

Some of the salient features of this revised curriculum are as below and they fall in line with the features in AICTE Model Curriculum.

1. The curriculum is designed in such a way that it encourages innovation and research as the total number of credits has been reduced from around 200 credits in an earlier curriculum to 171 credits in the current revision.
2. In the second and third-year curriculum, skill-based laboratories and mini-projects are introduced.
3. It will result in the students developing a problem-solving approach and will be able to meet the challenges of the future.
4. The University of Mumbai and BoS – Electronics and Telecommunication Engineering will ensure the revision of the curriculum on regular basis in the future as well and this update will certainly help students to achieve better employability; start-ups and other avenues for higher studies.

The BoS would like to thank all the subject experts, industry representatives, alumni, and various other stakeholders for their sincere efforts and valuable time in the preparation of course contents, reviewing the contents, giving valuable suggestions, and critically analyzing the contents.

Board of Studies in Electronics and Telecommunication Engineering

Dr. Faruk Kazi: Chairman

Dr. V. N. Pawar: Member

Dr. Ravindra Duche: Member

Dr. Milind Shah: Member

Dr. R. K. Kulkarni: Member

Dr. Baban U. Rindhe: Member

Dr. Mrs. Nair: Member

Dr. Nalbarwar: Member

Dr. Sudhakar Mande: Member

Dr. S. D. Deshmukh: Member

Program Structure for Final Year
Electronics & Telecommunication Engineering

Semester VII & VIII
UNIVERSITY OF MUMBAI
(With Effect from 2022-2023)
Semester VII

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	**Pract.**	**Tut.**	**Theory**	**Pract.**	**Tut.**	**Total**
ECC701	Microwave Engineering	**3**	--	--	**3**	--	--	**3**
ECC702	Mobile Communication System	**3**	--	--	**3**	--	--	**3**
ECCDLO701X	Department Optional Course-3	**3**	--	--	**3**	--	--	**3**
ECCDLO702X	Department Optional Course-4	**3**	--	--	**3**	--	--	**3**
ILO701X	Institute Level Optional Course-1	**3**	--	--	**3**	--	--	**3**
ECL701	Microwave Engineering Laboratory	--	**2**	--	--	**1**	--	**1**
ECL702	Mobile Communication System Laboratory	--	**2**	--	--	**1**	--	**1**
ECP701	Major Project-I	--	**6#**	--	--	**3**	--	**3**
Total		**15**	**10**	--	**15**	**5**	--	**20**

 # indicates work load of Learner (Not Faculty), for Major Project

Project Guide Load = ½ hour per week per project group

Course Code	Course Name	Examination Scheme							
		Theory							
		Internal Assessment			End Sem. Exam	Exam Duration (in Hrs)	Term Work	Practical & Oral	Total
		Test 1	Test 2	Avg.					
ECC701	Microwave Engineering	**20**	**20**	**20**	**80**	**3**	--	--	**100**
ECC702	Mobile Communication System	**20**	**20**	**20**	**80**	**3**	--	--	**100**
ECCDLO 701X	Department Level Optional Course-3	**20**	**20**	**20**	**80**	**3**	--	--	**100**
ECCDLO 702X	Department Level Optional Course-4	**20**	**20**	**20**	**80**	**3**	--	--	**100**
ILO701X	Institute Level Optional Course-1	**20**	**20**	**20**	**80**	**3**	--	--	**100**
ECL701	Microwave Engineering Laboratory	--	--	--	--	--	**25**	**25**	**50**
ECL702	Mobile Communication System Laboratory	--	--	--	--	--	**25**	**25**	**50**
ECP701	Major Project-I	--	--	--	--	--	**25**	**25**	**50**
Total		--	--	**100**	**400**	--	**75**	**75**	**650**

Department Level Optional Course-3

Course Code	Course Name
ECCDLO 7011	Efficient Architectures for DSP Algorithms
ECCDLO 7012	Deep Learning
ECCDLO 7013	Cloud Computing and Security
ECCDLO 7014	Big Data Analytics
ECCDLO 7015	Software Defined Radio

Department Level Optional Course-4

Course Code	Course Name
ECCDLO 7021	Robotics
ECCDLO 7022	5G Technology
ECCDLO 7023	Internet Communication Engineering
ECCDLO 7024	Advanced Digital Signal Processing
ECCDLO 7025	Quantum Computing

Institute Level Optional Course-1

Course Code	Course Name
ILO 7011	Product Lifecycle Management
ILO 7012	Reliability Engineering
ILO 7013	Management Information System
ILO 7014	Design of Experiments
ILO 7015	Operation Research
ILO 7016	Cyber Security and Laws
ILO 7017	Disaster Management and Mitigation Measures
ILO 7018	Energy Audit and Management
ILO 7019	Development Engineering

Program Structure for Final Year
Electronics & Telecommunication Engineering

Semester VII & VIII
UNIVERSITY OF MUMBAI
(With Effect from 2022-2023)
Semester VIII

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ECC801	Optical Communication and Networks	3	--	--	3	--	--	3
ECCDLO801X	Department Level Optional Course-5	3	--	--	3	--	--	3
ECCDLO802X	Department Level Optional Course-6	3	--	--	3	--	--	3
ILO801X	Institute Level Optional Course-2	3	--	--	3	--	--	3
ECL801	Optical Communication and Networks Laboratory	--	2	--	--	1	--	1
ECP801	Major Project-II	--	12#	--	--	6	--	6
Total		**12**	**14**	**--**	**12**	**7**	**--**	**19**

 # indicates work load of Learner (Not Faculty), for Major Project

Project Guide Load = 01 hour per week per project group

Course Code	Course Name	Examination Scheme							
		Theory							
		Internal Assessment			End Sem. Exam	Exam Duration (in Hrs)	Term Work	Practical & Oral	Total
		Test 1	Test 2	Avg.					
ECC801	Optical Communication and Networks	20	20	20	80	3	--	--	100
ECCDLO801X	Department Level Optional Course-5	20	20	20	80	3	--	--	100
ECCDLO802X	Department Level Optional Course-6	20	20	20	80	3	--	--	100
ILO801X	Institute Level Optional Course-2	20	20	20	80	3	--	--	100
ECL801	Optical Communication and Networks Laboratory	--	--	--	--	--	25	25	50
ECP801	Major Project-II	--	--	--	--	--	50	100	150
Total		**--**	**--**	**80**	**320**	**--**	**75**	**125**	**600**

Department Level Optional Course-5

Course Code	Course Name
ECCDLO 8011	System On Chip Design
ECCDLO 8012	Natural Language Processing
ECCDLO 8013	Wireless Networks
ECCDLO 8014	Web Design
ECCDLO 8015	RF Design

Department Level Optional Course-6

Course Code	Course Name
ECCDLO 8021	Autonomous Vehicle
ECCDLO 8022	Satellite and Nano Satellite Communication
ECCDLO 8023	Network Management in Telecommunication
ECCDLO 8024	Microstrip Antenna
ECCDLO 8025	Augmented Reality and Virtual Reality

Institute Level Optional Course-1

Course Code	Course Name
ILO 8011	Project Management
ILO 8012	Finance Management
ILO 8013	Entrepreneurship Development and Management
ILO 8014	Human Resource Management
ILO 8015	Professional Ethics and CSR
ILO 8016	Research Methodology
ILO 8017	IPR and Patenting
ILO 8018	Digital Business Management
ILO 8019	Environmental Management

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC701	Microwave Engineering	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC701	Microwave Engineering	20	20	20	80	03	--	--	100

Course Pre-requisite: Knowledge of Electromagnetic Engineering

-

Course Objectives:

The course should enable the students to:

1. Perceive the concepts of waveguides and analyze the field components in different types of Waveguides.
2. Categorize different types of microwave components based on their applications.
3. Imbibe knowledge to use microwave oscillators & amplifiers in microwave communication and Compare their characteristics. IV.
4. Demonstrate the ability to measure different microwave parameters using microwave bench setup.

Course Outcomes:

1. Describe the types of waveguides, rectangular waveguides and field equations
2. Understand the coupling mechanisms in waveguides and analyze the waveguide multiport junctions
3. .Explore the microwave linear tubes and analyze with microwave cross field tubes
4. Understand the microwave solid state devices and avalanche transit time devices
5. Demonstrate the microwave bench set up and conducting measurements of different parameters

Module No.	Unit No.	Topics	Hrs.
1.0		**TRANSMISSION LINES**	**06**
	1.1	Transmission line equations, open and short circuit transmission lines, variation of impedance over length of line, Smith chart, use of Smith chart in impedance matching	
	1.2	Planar transmission lines: microstrip line, strip line and coplanar lines	
2.0		**WAVEGUIDES**	**07**
	2.1	Introduction, microwave spectrum and bands, applications of microwaves, Types of waveguides, rectangular waveguides, field equations in rectangular waveguide, field components of TM and TE waves for rectangular waveguide, modes of TM and TE waves in rectangular waveguide, impossibility of TEM waves, cut off frequency of rectangular waveguide; Wave impedance in rectangular waveguide: Wave impedance for a TM and TE wave in rectangular waveguide, Dominant mode and degenerate modes, mode characteristics of phase velocity, group velocity, wavelength and impedance relations; Illustrative problems;	
	2.2	Cavity resonators: Types of cavity resonators; Rectangular cavity resonator: Dominant modes and resonant frequencies, illustrative problems.	
3.0		**WAVEGUIDE COMPONENTS**	**06**
	3.1	Coupling mechanisms: Probe, loop, coupling to a cavity resonator, waveguide discontinuities, waveguide irises, tuning screws and posts, matched loads; Waveguide attenuators; Waveguide phase shifters; waveguide	
	3.2	multiport junctions: E plane Tee, H plane Tee, Magic Tee, applications of Magic Tee, hybrid ring; Ferrites: Faraday rotation principle, gyrator, isolator, circulator	
4.0		**MICROWAVE TUBES**	**10**
	4.1	Microwave linear beam tubes (O type): Limitations of conventional tubes at microwave frequencies; Klystron: Velocity modulation process, bunching process, output power and beam loading; Multicavity Klystron amplifiers: Beam current density, output current and output power of two cavity Klystron; Reflex Klystron: Velocity modulation, power output and efficiency.	
	4.2	Helix Traveling Wave tube: Slow wave structures, amplification process, conventional current; Microwave cross field tubes (M type): Introduction, cross-field effects; Magnetrons: Different types, 8-cavity cylindrical travelling wave Magnetron, Hull cut-off and Hartree conditions, modes of resonance and PI-mode operation.	
5.0		**MICROWAVE SEMICONDUCTOR DEVICES**	**06**
	4.1	Microwave solid-state devices: Microwave tunnel diode; Pin diodes, varactor diodes, crystal detectors. Transferred electron devices: Gunn-effect diodes, RWH theory, modes of operations; Avalanche transit time devices: IMPATT diode, TRAPATT diode, BARITT diode,	
6.0		**MICROWAVE MEASUREMNETS**	**04**

	6.1	Description of microwave bench: Different blocks and their features, precautions; Microwave power measurement: Bolometers; Measurement of attenuation; Frequency standing wave measurements: measurement of low and high VSWR; Cavity Q; Impedance measurements.	
		Total	**39**

Text Books:

1. Samuel Y. Liao, ―Microwave Devices and Circuits‖, Pearson, 3rd Edition, 2003.
2. Peter A. Rizzi, ―Microwave Engineering Passive Circuits‖ PHI, 3rd Edition, 1999
3. M.L. Sisodia, G.S.Raghuvanshi, ―Microwave Circuits and Passive Devices‖ Wiley Eastern Ltd., New Age International Publishers Ltd, 1stEdition, 1995.

Reference books

1. R.E. Collin ―Foundations for Microwave Engineering‖ IEEE Press, John Wiley

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC702	Mobile Communication System	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC702	Mobile Communication System	20	20	20	80	03	--	--	100

Course Pre-requisite:

ECC405 - Principles of Communication Engineering
ECC501 - Digital Communication
ECC602 - Computer Communication and Networks

Course Objectives:

1. To understand the cellular fundamentals and different types of radio propagation models.
2. To study evolution of 2G and 3G mobile technologies.
3. To illustrate the working principle of LTE.
4. To learn the concepts of emerging technologies for 4 G standards and beyond.

Course Outcomes:

After successful completion of the course student will be able to:

1. Explain the cellular fundamentals and estimate the coverage and capacity of cellular systems.
2. Classify different types of propagation models and analyse the link budget.
3. Compare and contrast GSM, GPRS, HSCSD, EDGE and IS-95 Technologies.
4. Apply the concepts of 3G technologies for UMTS and CDMA 2000.
5. Describe the features and working principle of 3GPP LTE.
6. Discuss the emerging technologies for upcoming mobile communication systems.

Module No.	Unit No.	Topics	Hrs.
1.0		**Fundamentals of Mobile Communication**	**07**
	1.1	Introduction to Wireless Communication: Mobile Radio Telephony, Examples of Wireless Communication Systems	01
	1.2	The Cellular Concept System Design Fundamentals: Frequency reuse, Channel assignment strategies, Interference and system capacity, Trunking and Grade of service, Improving Coverage and Capacity in Cellular System and related problems.	06
2.0		**Mobile Radio Propagation**	**08**
	2.1	Large scale fading: Free space propagation model, ground reflection (two-ray) model, practical Link budget design using path loss models. Self-learning: Basic propagation mechanisms, reflection, diffraction and scattering.	03
	2.2	Small scale fading: Small-scale multipath propagation, parameters of mobile multipath channels, types of small-scale fading, Rayleigh and Ricean distributions.	02
	2.3	Features of all conventional multiple access techniques: Frequency Division Multiple Access (FDMA), Time Division Multiple Access (TDMA), Space Spectrum Multiple Access (SSMA), Space Division Multiple Access (SDMA), Orthogonal Frequency Division Multiple Access (OFDMA), OFDM-PAPR	03
3.0		**2G Technologies**	**08**
	3.1	GSM: GSM Network Architecture, air interface specifications, GSM signaling protocol architecture, GSM channels, GSM services and features, GSM multifare structure, GSM speech coding, GSM Call procedures, Authentication and security in GSM, and handoff procedures in GSM.	04
	3.2	GSM evolution: GPRS, HSCSD and EDGE architecture, radio specifications	02
	3.3	IS-95: CDMA air interface, CDMA channels, power control in CDMA system, handoff, and RAKE receiver.	02
4.0		**3G Technologies**	**05**
	4.1	UMTS: Objectives, standardization and releases, network architecture, air interface specifications, channels, security procedure, W-CDMA air interface, attributes of W-CDMA system, W-CDMA channels.	03
	4.2	Cdma2000 cellular technologies: Forward and Reverse Channels, Handoff and Power Control.	02
5.0		**3GPP LTE**	**06**
	5.1	Introduction, system overview: Frequency bands and spectrum flexibility, network structure, protocol structure.	02
	5.2	Physical layer: Frames, slots, and symbols, modulation, coding, multiple-antenna techniques	02
	5.3	Logical and Physical Channels: Mapping of data onto (logical) sub-channels, Establishing a connection, Physical layer retransmissions and reliability, Power control, and handover.	02
6.0		**Advanced techniques for 4G deployment and beyond**	**05**
	6.1	Multi-antenna Techniques: Smart antennas, Multiple input Multiple output systems.	02
	6.2	Cognitive radio: Architecture, spectrum sensing. Software Defined Radio (SDR): Components and Applications.	02

	6.4	Introduction to 5G network and technologies used in 5G such as small cell concept, Massive MIMO, Beamforming, NOMA, and mm wave).	01
		Total	**39**

Textbooks:

1. T. L. Singal "wireless communications", Mc Graw Hill Education.
2. Theodore S. Rappaport "wireless communications - principles and practice", PEARSON, Second edition.
3. Andreas F. Molisch "wireless communications" WILEY INDIA PVT LTD, Second edition.

Reference Books:

1. Upena Dalal "Wireless and Mobile Communications‖", Oxford university Press
2. Vijay K.Garg "Wireless Communications and Networking" ,Morgan–Kaufmann series in Networking-Elsevier.
3. J. H. Reed, Software-Defined Radio, Prentice-Hall, 2002
4. W. C. Y. Lee, Mobile Communication, Wiley
5. David Tse, Pramod Viswanath "Fundamentals of Wireless Communication" published by Cambridge University Press

E - Resources:

NPTEL courses:

1. http://nptel.ac.in/courses/117104099/ - (Advanced 3G and 4G Wireless Mobile communications)
2. https://nptel.ac.in/courses/117/102/117102062/ - (Wireless Communications)
3. Virtual lab: http://vlab.co.in

Internal Assessment (20-Marks):

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Course Code	Course Name	Teaching Scheme (Contact Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 7011	Efficient Architectures for DSP Algorithms	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (in Hrs.)	Term Work	Practical And Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. Of Test 1 and Test 2					
ECCDLO 7011	Efficient Architectures for DSP Algorithms	20	20	20	80	03	--	--	100

Course Prerequisite:

ECC303 Digital System Design
ECC404 Signals & Systems
ECC502 Discrete Time Signal Processing
ECC503 Digital VLSI Design
ECM601 Mini Project 2B- FPGA based Project

Course Objectives:

1. To describe the characteristics of computationally intensive algorithms
2. To identify the bottlenecks of intensive computations.
3. To learn various techniques to map DSP algorithms on hardware to improve performance.

Course Outcome:

After successful completion of the course students will be able to

CO1: Explain various typical DSP algorithms and their applications
CO2: Describe various methodologies/techniques to map DSP algorithms on Hardware
CO3: Analyze various hardware architectures available to implementation DSP algorithms
CO4: Evaluate and select efficient hardware architecture for implementation of given DSP algorithm.
CO5: Design/propose hardware architecture for effective implementation of given DSP algorithm.

Module No.	Unit No.	Topics	Hrs
1		**Introduction to DSP Systems**	**06**
	1.1	Typical DSP Algorithms, Graphical representation of DSP Algorithms	
	1.2	Signal flow graph (SFG), data flow graph (DFG) and dependence graph (DG), high level transformation, critical path	
2		**Efficient Algorithm to Architecture Mapping**	**07**
	2.1	Design of N-bit incrementer, decrementer, complimenter ,	
	2.2	Techniques to enhance circuit performance, pipelining and parallel processing, circuit design for N bit natural numbers, optimized circuit design for different functions	
3		**Efficient Adder Architecture**	**07**
	3.1	Introduction to Adder design, Variable Block Adder circuit design, Delay optimized Carry Look Ahead Adder	
	3.2	Carry Select Sum Adder, Conditional Sum Adder, Ling's Adder	
	3.3	Prefix and Parallel prefix adders, Running Average Circuit	
4		**Efficient Multiplier Design**	**07**
	4.1	Array Multiplier ,Signed and Unsigned Multiplier ,Booths Multiplier , Bough-Wooley Multiplier	
	4.2	Architecture of Squaring Circuit, Reconfigurable Constant Multiplier Design	
5		**DSP Architecture Design**	**06**
	5.1	Floating point representation IEE754, floating point operations-2's compliment representation, adder, subtractor, multiplier	
	5.2	CORDIC Architecture, FFT Architecture, FIR filter	
6		**Efficient Design of Machine Learning Hardware**	**06**
	6.1	Artificial Intelligence and Machine Learning, Software and Co-design Optimizations, Pruning, Systolic array convolution	
	6.2	Hardware-Level Techniques, RTL design of sum of differences, Energy efficient hardware accelerator design methodology for Neural Networks	
		Total	**39**

Textbooks:

1. VLSI Digital Signal Processing Systems Design and Implementation – Khesab Parhi
2. COMPUTER ARITHMETIC Algorithms and Hardware Designs-Behrooz Parhami
3. **Machine Learning in VLSI-Ibrahim (Abe) M. Elfadel, Duane S. Boning, Xin Li Computer-Aided Design**

Reference Books:

1. Bill Franks, ―Taming The Big Data Tidal Wave: Finding Opportunities in Huge Data Streams with Advanced Analytics‖, Wiley
2. Chuck Lam, ―Hadoop in Action‖, Dreamtech Press

E-Resources:

1. https://nptel.ac.in/courses/108105118
2. https://nptel.ac.in/courses/108106149
3. https://nptel.ac.in/courses/108105157

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 7012	Deep Learning	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 7012	Deep Learning	20	20	20	80	03	--	--	100

Course Pre-requisite:

1. ECC 604-Artificial Neural Networks and Fuzzy logic

Course Objectives:

At the end of the course, the students will be expected to:

1. Learn how to use TensorFlow for building and testing Deep Learning models
2. Compare various CNN architectures
3. Know the importance of Regularisation and Optimization techniques in Deep Learning networks
4. Learn Deep Learning models for working with sequential data
5. Understand motivation and functioning of the most common types of Autoencoders and apply such mechanisms to various learning problems.

Course Outcomes:

After successful completion of the course student will be able to:

1. Understand the fundamentals of Deep Learning
2. Understand the concepts of TensorFlow, its main functions, operations and the execution pipeline
3. Improve deep learning models using Regularization and Optimization techniques
4. Compare the Convolution Neural Network architectures and use them as per the application
5. Design and implement Sequence Neural Network systems and solve real-world problems
6. Illustrate the working of Autoencoders and use them for real-life applications

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Deep learning**	**03**
	1.1	History of Deep Learning- A Probabilistic Theory of Deep Learning	
	1.2	Introduction to Deep Feedforward Networks, Gradient Based Learning, Hidden Units	
	1.3	Architecture Design, Backpropagation Algorithm	
2.0		**TensorFlow for Deep learning**	**06**
	2.1	Introduction to TensorFlow using Python: Computational Graph, Key Highlights, Creating a Graph	
	2.2	Regression example, Gradient Descent, TensorBoard, Modularity, Sharing Variables, Keras	
	2.3	Preprocessing and Data Augmentation of Images and Datasets using TensorFlow	
3.0		**Regularization and Optimization Techniques**	**06**
	3.1	**Regularization:** Need of Regularization, L2 Regularization, L1 Regularization, Early Stopping and Dropout	
	3.2	**Optimization:** Challenges in NN Optimization, Gradient Descent Approaches, Parameter Initialization Approach, Adaptive Approaches - AdaGrad, RMSProp and Adam	
	3.2	Introduction to Batch Normalization	
4.0		**Evolution of CNN in Deep Learning**	**08**
	4.1	Review of CNN Architecture, Introduction of various CNN Architectures: LeNet, AlexNet, VGG, GoogleNet, ResNet and UNet	
	4.2	Comparison of CNN Architectures, Evaluation Parameters	
	4.3	Applications of CNN in Image Classification and Object Detection	
5.0		**Sequence Modeling**	**08**
	5.1	Recurrent and Recursive Nets: Recurrent Neural Networks, Bidirectional RNN, Encoder Decoder Architectures	
	5.2	Introduction to Long Short-Term Memory (LSTM) and Temporal Dependencies	
	5.3	Gated Recurrent Units (GRUs)	
	5.4	Applications of RNN in Real World- Image Captioning and Time Series Forecasting and Prediction	
6.0		**Encoder Decoder Models**	**08**
	6.1	Autoencoder: Encoder-Decoder Model, Training & Learning Manifold Space	
	6.2	Regularized Autoencoders: Sparse, De-noising and Contractive	
	6.3	Deep Autoencoder: Architecture and Working	
	6.4	Variational Autoencoders: Limitations of Autoencoders, Loss Function, Re-parameterization Trick, Latent Space Visualization	

	6.5	Applications of Autoencoders and Variational Autoencoders-Dimensionality Reduction , Image De-noising and Compression	
		Total	**39**
Self-learning Topics*:** Deep learning applications in Object Localization, Video Classification, Content based Image Retrieval, Recommender System, End-to-End Speech Recognition and Machine Translation *** No questions to be asked in exams.			

Text Books:

1. Charu C. Aggarwal, Neural Networks and Deep Learning, Springer International Publishing, 2018.
2. Ian Goodfellow, Yoshua Bengio and Aaron Courville, Deep Learning, MIT Press, 2016.

Reference books

1. Christopher M. Bishop, Pattern Recognition and Machine Learning, Springer-Verlag, 2006.
2. Duda, Richard, Peter Hart, and David Stork, Pattern Classification, 2nd edition, Wiley-Interscience, 2000.
3. Michael Nielsen, Neural Networks and Deep Learning, Determination Press, 2015.
4. Reza Zadeh, Bharath Ramsundar, TensorFlow for Deep Learning, 1st edition, O'Reilly Media Inc, 2018.
5. Zaccone, Giancarlo, Deep Learning with TensorFlow, 2nd edition, Packt Publishing, 2018.

NPTEL / Swayam Courses:

1. NPTEL course on Deep learning by Prof. Sudarshan Iyengar, IIT Ropar. https://nptel.ac.in/courses/106/106/106106184/
2. NPTEL course on Deep learning by Prof. Prabir Kumar Biswas, IIT Kharagpur. https://nptel.ac.in/courses/106/105/106105215/
3. NPTEL Course on Practical Machine Learning with TensorFlow by Prof. Balaraman Ravindran, IIT Chennai. https://nptel.ac.in/courses/106/106/106106213/

Online Resources:

1. https://www.tensorflow.org/tutorials/images/data_augmentation
2. https://towardsai.net/p/machine-learning/improving-artificial-neural-network-with-regularization-and-optimization
3. https://towardsdatascience.com/regularization-techniques-for-neural-networks-e55f295f2866
4. https://www.kaggle.com/sid321axn/regularization-techniques-in-deep-learning
5. https://medium.com/@minions.k/optimization-techniques-popularly-used-in-deep-learning-3c219ec8e0cc
6. https://www.jeremyjordan.me/variational-autoencoders/

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLOC 7013	Cloud Computing and Security	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLOC 7013	Cloud Computing and Security	20	20	20	80	03	--	--	100

Course Pre-requisite:

Computer Communication Network
Digital Encryption System

Course Objectives:

1. Understand the fundamentals of cloud computing .
2. Appreciate the importance of virtualization in cloud computing
3. Understand various cloud computing services and platforms
4. Understand application design concepts in cloud
5. Understand the security aspects of cloud computing
6. Understand the advances in cloud computing

Course Outcome:

After successful completion of the course student will be able to :-

1. Explain the fundamentals of cloud computing.
2. Interpret the significance of virtualization in the context of cloud computing
3. Describe cloud computing services working on AWS, Azure and Google cloud platforms
4. Explain application design aspects of cloud computing
5. Interpret security aspects to cloud computing.
6. Explain advances in cloud computing in terms of multimedia cloud, fog, edge computing and real applications of cloud.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Cloud**	**04**
	1.1	Introduction to Cloud Computing, Cloud Characteristics, Cloud Computing Components, Comparing of Cloud Computing with Peer to Peer architecture, Client Server , Distributed, Grid, Cloud Deployment model (Cloud types- Public, Private, Community, Hybrid), Service Models-(IaaS,PaaS,SaaS,)	
2.0		**Virtualization**	**07**
	2.1	Introduction & benefit of Virtualization – Implementation Levels of Virtualization- VMM Design Requirements and Providers – Virtualization at OS level – Middleware support for Virtualization– Virtualization structure/tools and mechanisms: Hypervisor and Xen Architecture, Binary Translation with full Virtualization, Para Virtualization with Compiler Support - Virtualization of CPU, Memory and I/O Devices, Hardware support for Virtualization in intel x86 processor – CPU Virtualization – MemoryVirtualization and I/O Virtualization – Virtualization in Multicore processors	
3.0		**Cloud Computing Services**	**10**
	3.1	Compute Services - Amazon Elastic Compute Cloud, Google Compute Engine, Windows Azure Virtual Machines Storage Services - Amazon Simple Storage Service, Google Cloud Storage, Windows Azure Storage Database Services - Amazon Relational Data Store, Amazon DynamoDB, Google Cloud SQL, Google Cloud Datastore, Windows Azure SQL Database, Windows Azure Table Service Application Services - Application Runtimes & Frameworks, Queuing Services, Email Services, Notification Services, Media Services	
	3.2	Content Delivery Services - Amazon CloudFront, Windows Azure Content Delivery Network Analytics Services - Amazon Elastic MapReduce, Google MapReduce Service, Google BigQuery, Windows Azure HDInsight Deployment & Management Services - Amazon Elastic Beanstalk, Amazon CloudFormation Identity & Access Management Services - Amazon Identity & Access Management, Windows Azure Active Directory Open Source Private Cloud Software - CloudStack, Eucalyptus, OpenStack	
4.0		**Cloud Application Design**	**06**
	4.1	Design Considerations for Cloud Applications - Scalability, Reliability & Availability, Security, Maintenance & Upgradation, Performance	
	4.2	Cloud Application Design Methodologies - Service Oriented Architecture, Cloud Component Model, IaaS, PaaS and SaaS services for cloud applications, Model View Controller, RESTful Web Services, Data Storage Approaches - Relational (SQL) Approach, Non-Relational (No-SQL) Approach	
5.0		**Cloud Security**	**06**
	5.1	Security for Virtualization Platform – Host security for SaaS, PaaS and IaaS – Data Security – Data Security Concerns – Data Confidentiality and Encryption – Data Availability –Data Integrity – Cloud Storage Gateways – Cloud Firewall	
	5.2	AAA Administration for Clouds -AAA model – SSO for Clouds – Authentication management and Authorization management in clouds – Accounting for Clouds Resource utilization.	

6.0		**Cloud Computing Applications**	**06**
	6.1	Cloud Computing for Health care, Education, Transportation, Manufacturing Industry, Energy System, Mobile Computing	
	6.2	Multimedia Cloud - Introduction, Streaming Protocols - RTMP Streaming, HTTP Live Streaming, HTTP Dynamic Streaming	
	6.3	Case Studies - Live Video Streaming App , Video Transcoding App, Edge Computing, FOG Computing	
		Total	**39**

Text books :

1. Cloud Computing - A Hands-on Approach - Arshdeep Bahga and Vijay K. Madisetti
2. Mastering Cloud Computing: Foundations and Applications Programming Paperback – by Rajkumar Buyya , Christian Vecchiola , S.Thamarai Selvi , Publisher: Morgan Kaufmann
3. Amazon Web Services For Dummies (For Dummies Series) Paperback by Bernard Golden, Publisher: John Wiley & Sons
4. “The Cloud Computing Book: The Future of Computing Explained” , Douglas E. Comer
5. Cloud Computing for Dummies, Judith Hurwitz Daniel Kirsch

Reference books

1. Cloud Computing Black Book : Kailash Jayaswal, Jagannath Kallakurchi, Donald J. Houde, Dr. Deven Shah by Kogent Learning Solutions , Publisher : Dreamtech Press
2. Cloud Computing Concepts Technology and Architecture - Erl second hand book online from UsedBooksFactory
3. Practical Cloud Security by Chris Dotson, Publisher(s): O'Reilly Media, Inc. ISBN: 9781492037514
4. AWS Whitepapers & Guides https://aws.amazon.com/whitepapers/
5. Azure whitepapers https://azure.microsoft.com/en-in/resources/whitepapers/
6. Google Cloud whitepapers https://cloud.google.com/whitepapers

MOOC

1. NPTEL Swayam Course on Cloud computing By Prof. Soumya Kanti Ghosh https://nptel.ac.in/courses/106/105/106105167/
2. Cloud Computing and Distributed Systems By Prof. Rajiv Misra https://onlinecourses.nptel.ac.in/noc22_cs18/preview
3. Google Cloud Computing Foundation Course https://nptel.ac.in/courses/106/105/106105223

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End Semester Examination (80-Marks):

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1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
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3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 7014	Big Data Analytics	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 7014	Big Data Analytics	20	20	20	80	03	--	--	100

Course Prerequisite:

Basic knowledge of Database Management System

Course Objectives:

1. To Provide an Overview of an exciting growing field of Big Data Analytics.
2. To introduce the tools required to manage and analyze big data like Hadoop, NoSql, Map Reduce.
3. To teach the fundamental techniques in achieving big data analytics with scalability and streaming capability

Course Outcome:

After successful completion of the course student will be able to

1. Understand the key issues in big data management and its associated applications in intelligent business and scientific computing.
2. Acquire fundamental enabling techniques and scalable algorithms like Hadoop, MapReduce and NoSQL in big data analytics.
3. Interpret business models and scientific computing paradigms, and apply software tools for big data analytics.
4. Achieve adequate perspectives of big data analytics in various applications like recommender systems, social media applications etc
5. Develop applications for Big Data analysis using Hadoop and NoSQL etc.

Module No.	Unit No.	Topics	Hrs
1		**Introduction to Big Data Analytics**	03
	1.1	Introduction to Big Data, Big Data characteristics, Types of Big Data, Traditional vs. Big Data a business approach	
	1.2	Technologies Available for Big Data, Infrastructure for Big Data, Big Data Challenges, Case Study of Big Data Solutions.	
2		**Hadoop**	05
	2.1	Introduction to Hadoop. Core Hadoop Components, Hadoop Ecosystem-Apache HBase, Hive, HCatalog, Pig, Mahout, Oozie, Zookeeper, Sqoop, Physical Architecture, Hadoop limitations.	
3		**NoSQL**	06
	3.1	Introduction to NoSQL, NoSQL business drivers, NoSQL database case studies.	
	3.2	NoSQL data architecture patterns: Key-value stores, Graph stores, Column family (Bigtable) stores, Document stores, Variations of NoSQL architectural patterns	
	3.3	Using NoSQL to manage big data: What is a big data NoSQL solution? Understanding the types of big data problems; Analyzing big data with a shared-nothing architecture; Choosing distribution models: master-slave versus peer-to-peer; Four ways that NoSQL systems handle big data problems Managing MongoDB database with CRUD operations.	
4		**MapReduce**	06
	4.1	MapReduce and The New Software Stack: Distributed File Systems, Physical Organization of Compute Nodes, Large Scale File-System Organization.	
	4.2	MapReduce: The Map Tasks, Grouping by Key, The Reduce Tasks, Combiners, Details of MapReduce Execution, Coping With Node Failures	
	4.3	Algorithms Using MapReduce: MapReduce WordCount Program, Matrix-Vector Multiplication by MapReduce , Relational-Algebra Operations by MapReduce, Matrix Operations, Matrix Multiplication by MapReduce.	
5		**Techniques in Big Data Analytics**	13
	5.1	Finding Similar Item: Nearest Neighbor Search, Similarity of Documents, Distance Measures: Euclidean, Jaccard , Cosine , Edit and Hamming Distance with its Examples	
	5.2	Mining Data Streams: Data Stream Management Systems, Data Stream Model, Examples of Data Stream Applications: Sensor Networks, Network Traffic Analysis Filtering streams: The Blooms filter.	
	5.3	Link Analysis: PageRank Definition, Structure of the web, dead ends, Using Page rank in a	

		search engine, Efficient computation of Page Rank: Page Rank Implementation Using MapReduce	
	5.4	Frequent Itemset Mining: Market-Basket Model, Apriori Algorithm, Algorithm of Park-Chen-Yu	
6		**Big Data Analytics Applications**	**06**
	6.1	Recommendation Systems: Introduction, A Model for Recommendation Systems: Collaborative-Filtering System, Content based system and its Examples.	
	6.2	Mining Social-Network Graphs: Social Networks as Graphs, Types of Social-Network. Clustering of Social Graphs: Applying Standard Clustering Techniques, Counting triangles using MapReduce.	
		Total	**39**

Textbooks:

1. Radha Shankarmani and M Vijayalakshmi —Big Data Analytics‖, Wiley
2. Alex Holmes —Hadoop in Practice‖, Manning Press, Dreamtech Press.
3. Dan McCreary and Ann Kelly —Making Sense of NoSQL – A guide for managers and the rest of us, Manning Press.

Reference Books:

1. Bill Franks, —Taming The Big Data Tidal Wave: Finding Opportunities in Huge Data Streams with Advanced Analytics‖, Wiley
2. Chuck Lam, —Hadoop in Action‖, Dreamtech Press

E-Resources:

1. https://www.analyticsvidhya.com/blog/2014/05/hadoop-simplified
2. https://www.analyticsvidhya.com/blog/2014/05/introduction-mapreduce/
3. https://www.pdfdrive.com/big-data-analytics-a-hands-on-approach-e158549112.html
4. https://www.pdfdrive.com/data-science-and-big-data-analytics-e58447171.html

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLOC 7015	Software Defined Radio	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLOC 7015	Software Defined Radio	20	20	20	80	03	--	--	100

Prerequisites:
- Computer Communication and Networks
- Mobile Communication Systems

Course Objectives: The objective of this course is

1. To introduce fundamental knowledge of Software Defined Radio (SDR) and Cognitive Radio (CR) technology in next generation networks.
2. To introduce the hardware and software requirements and design aspects of CR
3. To introduce the architecture, spectrum sensing, spectrum awareness and allocation in CR networks.
4. To introduce the various standards available in CR technology and GNU platform for experimentation.

Course Outcomes: After learning the course the students will be able to demonstrate the ability

1. To Learn the hardware and software architecture and various design principles of SDR
2. To understand challenges of receiver design and select suitable hardware and software for SDR.
3. To understand the functions, components and challenges of CR technology for better spectrum exploitation.
4. To analyze various spectrum sensing techniques in CR environment.
5. To understand and apply the techniques of dynamic spectrum allocation and scheduling in CR based networks.
6. To understand various standards of CR Technology and its role in next generation networks and GNU platform.

Module No	Unit No.	Topic	No. of Hrs
1		**Software Defined Radio**	5
	1.1	Basic components of Software Defined Radios, Software defined radio hardware architectures	
	1.2	Distortion parameters - Sources and metrics of distortion in a transceiver, Nonlinear distortion and nonlinearity specifications, Power amplifiers: Nonlinear Distortion in Transmitted Signals	
2		**SDR Architecture and Components**	8
	2.1	Power amplifier Line-up for linearity & power requirement calculations, Linearization Techniques for nonlinear distortion in SDR, Pre distortion Techniques for nonlinear distortion in SDR	
	2.2	Digital Pre distortion Techniques for Linear/Nonlinear Distortion	
	2.3	SDR Software architecture, Software Tunable Analog Radio Components	
	2.4	Antenna Systems, Reconfigurable Digital Radio Technologies, Basic Digital Radio Components	
3		**Cognitive Radio**	6
	3.1	**Cognitive radio features and capabilities**: Cognitive radio architecture Functions of cognitive radio Dynamic spectrum access, Components of cognitive radio Interference temperature ,Spectrum sensing Spectrum analysis and spectrum decision	
	3.2	**Research challenges in Cognitive Radio:** Issues in spectrum sensing, Spectrum management issues Spectrum mobility issues , Network layer and transport layer issues, Cross-layer design for cognitive radio networks, Artificial intelligence approach for designing cognitive radio, Location-aware cognitive radio	
4		**Spectrum Sensing for Cognitive Radio**	6
	4.1	Challenges, Matched Filtering, Waveform-Based Sensing, Cyclostationarity - Based Sensing, Energy Detector-Based Sensing, Radio Identification, Cooperative Sensing, External Sensing, Statistical Approaches and Prediction.	
	4.2	Sensing Frequency, Hardware Requirements and Approaches, Multi-dimensional Spectrum Awareness	
5		**Dynamic spectrum access and management in Cognitive Radio**	8
	5.1	Spectrum access models : Exclusive-use model , Shared-use model Spectrum commons model	
	5.2	Dynamic spectrum access architecture: Infrastructure-based versus infra structure less cognitive radio network Centralized versus distributed dynamic spectrum access Inter- and intra-RAN dynamic spectrum allocation	
	5.3	Medium access control for dynamic spectrum access :	

		Optimal decision on spectrum sensing and spectrum access Multichannel and multiuser MAC Spectrum allocation and scheduling, Spectrum trading Performance analysis of cognitive MAC protocols	
6		**Advanced topics in Cognitive Radio**	**6**
	6.1	Cognitive radio architectures for NeXt Generation (XG) networks	
	6.2	Cognitive radio standardization : IEEE SCC 41, IEEE 802.22 for wireless regional area networks (WRANs)	
	6.3	GNU Radio for cognitive radio experimentation	
		Total	**39**

Recommended Books:

1. Huseyin Arslan, “Cognitive Radio, Software Defined Radio, and Adaptive Wireless Systems”, Springer, 2007

2. Ekram Hossain, Dusit Niyato, Zhu Han, “Dynamic Spectrum Access and Management in Cognitive Radio Networks”, Cambridge University Press, 2009

3. Bruce Fette, “Cognitive radio technology”, Elsevier, 2nd edition, 2009.

4 Alexandar M Wylingskey, Maziar Nikovee, Y Thomas Hou, “Cognitive Radio Communications and Networks Principles and Practice”, Elsevier, 2010

REFERENCES:

1. Kwang-Cheng Chen, Ramjee Prasad, “Cognitive radio networks”, John Wiley & Sons Ltd., 2009.

2. Francisco Rodrigo Porto Cavalcanti, Soren Andersson, “Optimizing Wireless Communication Systems” Springer, 2009.

3. Linda Doyle, “Essentials of Cognitive Radio”, Cambridge University Press, 2009.

E-Resources:

1. NPTEL: https://nptel.ac.in/courses/108/107/108107107/
2. GNU Radio: https://www.gnuradio.org/
   https://wiki.gnuradio.org/index.php/Tutorials
   http://www.gcndevelopment.com/gnuradio/downloads.html

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 7021	Robotics	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (in Hrs.)	Term Work	Practical And Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ECCDLO 7021	Robotics	20	20	20	80	03	--	--	100

Course Prerequisite: - Engineering Mathematics III and IV

Course Objectives:

1. To introduce the functional elements of Robotics
2. To impart knowledge on the direct and inverse kinematics
3. To introduce the manipulator differential motion and control
4. To educate on various path planning techniques
5. To introduce the dynamics and control of manipulators
6. To study about the localization, planning and navigation

Course Outcomes:

After successful completion of the course students will be able to

- Explain basic concept of robotics.
- Describe the differential motion, add statics in robotics
- Describe the various path planning techniques.
- Describe the dynamics and control in robotics industries.
- Write program to use a robot for a typical application
- Use Robots in different applications

Module No.	Unit No.	Topics	Hrs.
1.	**BASIC CONCEPTS**		3
	1.1	Brief History	
	1.2	Types of Robot–Technology-Robot classifications and specifications	
	1.3	Design and Control issues	
	1.4	Various manipulators	
	1.5	Sensors , work cell	
	1.6	Programming languages	
2.	**DIRECT AND INVERSE KINEMATICS**		8
	2.1	Mathematical representation of Robots - Position and orientation	
	2.2	Homogeneous transformation Various joints, Degrees of freedom	
	2.3	Representation using the Denavit Hattenberg parameters	
	2.4	Direct kinematics-Inverse kinematics	
	2.5	Solvability – Solution methods-Closed form solution	
	2.6	SCARA robots-	
3.	**PATH PLANNING**		8
	3.1	Joint space technique	
	3.2	Use of p-degree polynomial, Cubic polynomial, Cartesian space technique	
	3.3	Parametric descriptions	
	3.4	Straight line and circular paths	
	3.5	Position and orientation planning	
4.	**DYNAMICS AND CONTROL**		7
	4.1	Lagrangian mechanics	
	4.2	2DOF Manipulator	
	4.3	Lagrange Euler formulation	
	4.4	Dynamic model	
	4.5	Manipulator control problem-Linear control schemes-PID control scheme-Force control of robotic manipulator	
5.	**SERVICE ROBOTICS**		7
	5.1	Need for service robots	
	5.2	**LOCALIZATION:** Challenges of Localization- Map Representation-Probabilistic Map based Localization Monte carlo localization- Landmark based navigation-Globally unique localization- Positioning beacon systems- Route based localization	
	5.3	**PLANNING AND NAVIGATION:** Path planning overview, Cell decomposition path planning Potential field path planning-Obstacle avoidance	
6.	**APPLICATIONS**		6
	6.1	Ariel robots	
	6.2	Collision avoidance	
	6.3	Robots for agriculture, mining, exploration, underwater, civilian and military applications, nuclear applications, Space applications	
	6.4	Humanoids	
		Total	39

Text Books:

1. R.K.Mittal and I.J.Nagrath, Robotics and Control, Tata McGraw Hill, New Delhi,4th Reprint, 2005.
2. JohnJ.Craig ,Introduction to Robotics Mechanics and Control, Third edition, Pearson Education, 2009.

3. M.P.Groover, M.Weiss, R.N. Nageland N. G.Odrej, Industrial Robotics, McGraw-Hill Singapore, 1996.
4. Roland Siegwart, Illah Reza Nourbakhsh, Davide Scaramuzza, „Introduction to Autonomous Mobile Robots", Bradford Company Scituate, USA, 2004

Reference Books:

1. Ashitava Ghoshal, Robotics-Fundamental Concepts and Analysis', Oxford University Press, Sixth impression, 2010.
2. K. K.Appu Kuttan, Robotics, I K International, 2007.
3. Edwin Wise, Applied Robotics, Cengage Learning, 2003.
4. B.K.Ghosh, Control in Robotics and Automation: Sensor Based Integration, Allied Publishers,Chennai, 1998
5. Riadh Siaer, „The future of Humanoid Robots- Research and applications, Intech Publications, 2012.

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 7022	5G Technology	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 7022	5G Technology	20	20	20	80	03	--	--	100

Course pre-requisite:

Digital Communication
Mobile Communication Systems

Course Objectives:

1. Learn the basics of 5G and beyond wireless communication
2. Study 5G network architecture and Heterogeneous Network and Small cells
3. Provide understanding of the key technologies and enablers of 5G and beyond communication systems.
4. Learn 5G technology like massive MIMO, mmWave etc.

Course Outcome:

After successful completion of the course student will be able to:
1. Distinguish between the major cellular communication standards (1G/2G/3G/4G/5G systems) and architecture of wireless communications networks.
2. Apply the 5G techniques e.g., massive MIMO, mmWave etc. for the design of communication systems.
3. Analyse various modulation and multiplexing techniques e.g., OFDM, NOMA etc.
4. Describe applications of cognitive radio in 5G Wireless Communications.

Module No.	Unit No.	Topics	Hrs.
1		**Introduction**	**04**
	1.1	Introduction to 5G Technology, Features, Requirements, Applications, 5G Services, Introduction to 5Gi	
	1.2	Digital modulations (OFDM, 5G Technology Modulation Techniques) and performance metrics, 5G Internet, Internet of Things and Context-Awareness, Software Defined Networking, Network Function Virtualisation (NFV)	
2		**5G Architecture**	**08**
	2.1	5G Network Architecture, Cloud RAN(C-RAN), Definitions of Heterogeneous Networks, Radio Resource and Interference Management for Heterogeneous Networks, Traffic offloading scenarios for heterogeneous networks, mobility management and handover	
	2.2	Small cell deployments: different types, Deployment scenarios, performance and analysis, Energy efficient mechanism with BS sleep mode in green small cell networks, Game theory and learning techniques for self-organization in small cell networks, 3GPP RAN standards for small cell	
3		**Mm Wave**	**08**
	3.1	mmWave: Millimeter bands, radio-wave propagation Physical layer design and algorithms, mmWave MIMO challenges, channel modelling, channel estimation and Beam-forming. Types of transceivers, Merits and Demerits, Applications	
	3.2	Physical or Radio layer Technologies - Massive MIMO (Sub 6Ghz) -mm wave MIMO (above 6 GHz)	
4		**NOMA**	**05**
		Non orthogonal Multiple Access (NOMA), Different Types: power domain NOMA and code domain NOMA, Difference between Orthogonal multiple access and NOMA, Filter Bank multi carrier -Full duplex Radio Techniques, Precoding	
5		**Cognitive Radio for 5G Wireless Networks**	**08**
	5.1	Introduction, Overview of Cognitive Radio Technology in 5G Wireless, Spectrum Optimisation using Cognitive Radio, Cognitive Radio and Carrier Aggregation, Energy-Efficient Cognitive Radio Technology	
	5.2	Cognitive Radios to Mitigate Interference in Macro/femto Heterogeneous Networks, Cognitive Radio enabled Operations, Interference Coordination: Orthogonality in the Time/Frequency domain, Intra-tier Interference mitigation, Compressive sensing	
6		**Trends in 5G**	**06**
		5G NR, Carrier Aggregation in 5G, Open RAN, Use cases of 5G:eMBB, URLLC and mMTC, Advance applications: Robotic surgery, driverless car and Industrial IoT (IIoT), Tactile Internet, 5G-IoT applications, AR/VR in 5G	
		Total	**39**

Text books:

1. Principles of Modern Wireless communication systems by Aditya k Jagannathan
2. Robert W. Heath, Robert C. Daniel, James N. Theodore S. Rappaport, Murdock, "Millimeter Wave Wireless Communication", Prentice Hall, 2014.

Reference books

1. R. Vannithamby and S. Talwar, Towards 5G: Applications, Requirements and Candidate Technologies., John Willey & Sons, West Sussex, 2017.
2. Manish, M., Devendra, G., Pattanayak, P., Ha, N., 5G and Beyond Wireless Systems PHY Layer Perspective, Series in Wireless Technology Springer, 2021
3. Alagan Anpalagan, Mehdi Bennis, Rath Vannithamby, Design and deployment of small cell networks, Cambridge university press, 2015
4. Rose Qingyang Hu, Yi Qian**,** Heterogeneous Cellular Networks, John Wiley & Sons, Ltd., Publication, 2013
5. T. S. Rappaport, R. W. Heath Jr., R. C. Daniels, and J. M. Murdock, Millimeter Wave Wireless Communication., Pearson Education, 2015.
6. M. Vaezi, Z. Ding, and H. V. Poor, Multiple Access techniques for 5G Wireless Networks and Beyond., Springer Nature, Switzerland, 2019

Internal Assessment (20-Marks):

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 7023	Internet Communication Engineering	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test 1	Test 2	Avg.					
ECCDLO 7023	Internet Communication Engineering	20	20	20	80	03	--	--	100

Course Pre-requisite:

- Analog communication
- Digital Communication
- Computer Communication and Networks

Course Objectives:

1. To focus on Internet protocol, standards, services and administration.
2. To discuss the Internet security protocol and security services
3. To discuss multimedia communication standards and compression techniques
4. To add insights on software defined network & network automation
5. To introduce Internet of Things

Course Outcomes:

After successful completion of the course student will be able to:

1. Compare the protocols at each layer of TCP/IP protocol suite.
2. Explain the internet security aspects of protocols at various layers of TCP/IP protocol suite.
3. Apply the various compression algorithms for audio, image & video coding.
4. Categorize and design simple networked multimedia systems.
5. Compare integrated & differentiated services for quality of service.
6. Explain a software defined Network.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Internet**	**03**
	1.1	What is the Internet, Evolution of the Internet, service description, Network protocol?	
	1.2	Overview of TCP/IP, layer functions	
2.0		**Application Layer in the Internet**	**06**
	2.1	Application Layer- Host configuration, DHCP, Domain Name System (DNS), Multicast DNS	
	2.2	Remote Login, TELNET and SSH, HTTPS, electronic mail	
3.0		**Internet Security**	**05**
	3.1	Network layer security (AH, ESP, IPsec)	
	3.2	Transport layer security (SSL), Application layer security (secure E mail-PGP, S/MIME)	
	3.3	VPN Firewall, Intrusion Detection System.	
4.0		**Multimedia Communications**	**10**
	4.1	Information Representation- text, images, audio and video, Text and image compression, Audio and video compression, video	
	4.2	compression standards: H.261, H.263, P1.323, MPEG 1, MPEG 2, Other coding formats for text, speech, image and video	
	4.3	multimedia transport across IP networks and relevant protocols such as RSVP, RTP, RTCP, DVMRP, Signalling Protocols: Real-Time Streaming Protocol (RTSP).	
	4.4	VoIP, IPTV	
5.0		**Quality of Services (QoS)**	**07**
	5.1	Integrated services (intserv): Architecture and Service Model, Resource Reservation Protocol (RSVP), Packet Scheduling Disciplines in the Internet	
	5.2	Differentiated Services (diffserv): Framework and Concept, Assured and Expedited Services, Packet Classification, Routers Internals and Packet Dropping Techniques	
6.0		**Network Industry Trends & Automation**	**08**
	6.1	Introduction to software defined networking, OPENFLOW	
	6.2	Why network automation? Simplified Architectures, Deterministic outcomes, Business Agility, Types of network automation, Device Provisioning, Data collection, Migrations, Configuration Management, Compliance, Reporting, Troubleshooting, Evolving from the management plane from SNMP to device APIs--- Impact of open networking, Network Automation in the SDN era.	
	6.3	Introduction to Internet of Things (IoT): Definition and characteristics of IoT, Physical design of IoT: Things in IoT, IoT Protocols.	
		Total	**39**

Text Books:

1. B. Forouzan, ―*TCP/IP Protocol Suite*‖, 4th Edition, McGraw-Hill Publication
2. K. R. Rao, Zaron S. Bojkovic, Dragorad A. Milocanovic, Multimedia Communication Systems, Prentice Hall India, 2002. ISBN: 81-203-2145-6.
3. Network Programmability & Automation---Jason Edelman, Scott S. Lowe & Matt Oswalt, OREILLY.

References:

1. Steve Heath, Multimedia and Communication Technology, Second Edition, Focal Press, 2003.
2. ISBN: 81-8147-145-8. Ted Wallingford, ―*Switching to VoIP*‖, Oreilly Publication
3. Fred Halsall, ―Multimedia Communications‖, Pearson education, 2001
4. K. R. Rao, Zoran S. Bojkovic, Dragorad A. Milovanovic, ―Multimedia Communication Systems‖, Pearson education, 2004
5. Raif steinmetz, Klara Nahrstedt, ―Multimedia: Computing, Communications and Applications‖, Pearson education, 2002
6. Tay Vaughan, ―Multimedia: Making it Work‖, 6th edition, Tata McGraw Hill, 2004
7. Pallapa Venkataram, ―Multimedia information systems‖, Pearson education (InPress),2005.
8. Multimedia Communication Techniques and Standards
9. Arshdeep Bagha, Vijay Madisetti "Internet of Things", universities Press.

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 7024	Advanced Digital Signal Processing	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 7024	Advanced Digital Signal Processing	20	20	20	80	03	--	--	100

Course Pre-requisite:

ECC502 Discrete-Time Signal Processing

Course Objectives:

1. To develop a thorough understanding of power spectrum estimation and different models for the same.
2. To apply optimum linear filters, linear prediction, and adaptive filtering techniques for signal processing applications.
3. To process multi-rate data.
4. To develop multi-resolution analysis using wavelets.

Course Outcomes:

After successful completion of the course student will be able to:
1. Illustrate parametric and non-parametric techniques of power spectrum estimation.
2. Explain optimum linear filters and their different forms.
3. Perform linear estimation and prediction of discrete time signals.
4. Implement various types of adaptive filters for the given applications.
5. Design interpolator, decimator and sampling rate convertors for multi-rate signal processing.
6. Apply concepts of wavelets and filter banks for signal processing applications.

Module No.	Unit No.	Topics	Hrs.
1.0		**Power Spectrum Estimation**	**09**
	1.1	Principle of Power Spectrum Estimation	
	1.2	Non Parametric Method of Power Spectrum Estimation: Modified Periodogram, Bartlett's Method, Welch's Method, Blackman-Tukey Method	
	1.3	Parametric Methods for Power Spectrum Estimation: Relationships between the Autocorrelation and the Model Parameters, AR, MA & ARMA Models	
	1.4	Introduction to Least-Squares Method for the AR Model Parameters and Yule-Walker Method for the AR Model Parameters	
2.0		**Optimum Linear Filters**	**03**
	2.1	Wiener Filters	
	2.2	FIR Wiener Filter (Wiener-Hopf filter)	
	2.3	IIR Wiener filter (Non-Causal and Causal IIR Wiener Filter)	
	2.4	Orthogonality Principle in Linear Mean-Square Estimation	
3.0		**Linear Prediction**	**05**
	3.1	Forward and Backward Linear Prediction	
	3.2	Solution of Normal Equation (Levinson-Durbin and Schur Algorithm)	
	3.3	AR Lattice and ARMA Lattice Ladder Filters	
	3.4	MMSE Estimation	
	3.5	Introduction to Kalman Filter, Matched Filter	
4.0		**Adaptive Filters**	**07**
	4.1	Adaptive Algorithms: LMS Algorithm, NLMS Algorithm, RLS Algorithm, Lattice Ladder Algorithm	
	4.2	Applications of Adaptive Filters: System Identification, Adaptive Channel Equalization, Echo Cancellation, Adaptive Noise Cancellation	
		Self-Study: Suppression of Narrowband Interference in Wideband Signals, Adaptive Array	
5.0		**Multi-rate Signal Processing**	**08**
	5.1	Introduction to Multi-rate Signal Processing	
	5.2	Interpolation and Decimation, Sampling Rate Conversion by Non-integer Factor	
	5.3	Multistage Interpolation and Decimation	
	5.4	Polyphase Decomposition	
	5.5	Filter Banks: Quadrature Mirror Filter Banks	
		Self-Study: Subband Coding	
6.0		**Introduction to Wavelets**	**07**
	6.1	Limitations of Fourier Transform and Short Time Fourier Transform, Introduction to Time-Frequency Tiling	
	6.2	Multi-resolution analysis using Discrete Time Wavelet Transform: Haar MRA, Analysis of two band dyadic filter banks, Frequency response of the Haar Filter Bank	
	6.3	Introduction to Daubechies Wavelets	
	6.4	Application of Wavelet theory to Signal Denoising (Soft and Hard Thresholding)	
		Self-Study: Signal Compression, Image Compression	
		Total	**39**

Note: No questions will be asked in the end semester exam from self-study topics. However, students are encouraged to explore these topics for better understanding of the subject.

Textbooks:

1. John G. Proakis, Dimitris K. Manolakis, “Digital Signal Processing Principles, Algorithms, and Applications”, Prentice-Hall, 4th Edition, 2012.
2. Simon Haykin, “Adaptive Filter Theory”, Pearson Education, Fourth Edition, 2002
3. Martin Vetterli, Jelena Kovacevic, “Wavelets and Subband Coding”, Prentice-Hall, 1995.
4. Burrus, C. Sidney, Ramesh A. Gopinath, and Haitao Guo, "Introduction to wavelets and wavelet transforms", Prentice Hall Inc. 1997"

Reference Books:

1. Emmanuel C. Ifeachor, Barrie W. Jervis, “Digital Signal Processing: A Practical Approach”, Pearson Education, 2008.
2. E. Chandrasekhar, V. P. Dimri, V. M. Gadre (Eds.), “Wavelets and Fractals in Earth System Sciences”, CRC Press, 2013.
3. Tarun Kumar Rawat, “Digital Signal Processing”, Oxford University Press, 2014.
4. K. Deergha Rao, M.N.S. Swamy, “Digital Signal Processing: Theory and Practice”, Springer, 2018.
5. K. P. Soman, K.I. Ramchandran and N. G. Reshmi, “Insight into Wavelets: From Theory to Practice", Third Edition PHI, 2010.
6. P. P. Vaidyanathan, “Multirate Systems and Filter Banks”, Prentice-Hall, 1993.
7. Sanjit K. Mitra, “Digital Signal Processing: A Computer-Based Approach”, McGraw Hill, 2011.

NPTEL / Swayam Course:

1. “Estimation of Signals and Systems” by Prof. S. Mukhopadhyay, IIT Kharagpur. https://nptel.ac.in/courses/108/105/108105059/
2. “Adv. Digital Signal Processing - Multirate and wavelets” by Prof. V. M. Gadre, IIT Bombay. https://nptel.ac.in/courses/117/101/117101001/

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-1). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 7025	Quantum Computing	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 7025	Quantum Computing	20	20	20	80	03	--	--	100

Course pre-requisite:

ECC303- Digital System Design
ECC301-Engineering Mathematics-III
ECCDLO5014- Data Structures and Algorithm
ECL404-Skill Lab: Python Programming

Course Objectives:

1. To understand basics of quantum computing
2. To understand mathematics required for quantum computing.
3. To understand building blocks of quantum computing.
4. To understand quantum algorithms.
5. To understand quantum hardware principles.
6. To understand tools for quantum computing.

Course Outcome:

After successful completion of the course student will be able to :-

1. Explain basic concepts of quantum computing
2. Explain mathematical fundamentals required for quantum computing.
3. Explain building blocks of quantum computing through architecture and programming models.
4. Explain quantum algorithms.
5. Explain quantum hardware building principles.
6. Explain usage of tools for quantum computing.

Module No.	Unit No.	Topics	Hrs.
1.0		Introduction to Quantum Computing	**07**
	1.1	Motivation for studying Quantum Computing	
	1.2	Origin of Quantum Computing	
	1.3	Quantum Computer vs. Classical Computer	
	1.4	Introduction to Quantum mechanics	
	1.5	Overview of major concepts in Quantum Computing Qubits and multi-qubits states Bloch Sphere representation Quantum Superposition Quantum Entanglement	
	1.6	Major players in the industry (IBM, Microsoft, Rigetti, D-Wave etc.)	
2.0		Mathematical Foundations for Quantum Computing	**05**
	2.1	Matrix Algebra: basis vectors and orthogonality, inner product and Hilbert spaces, matrices and tensors, unitary operators and projectors, Dirac notation, Eigen values and Eigen vectors.	
3.0		Building Blocks for Quantum Program	**08**
	3.1	Architecture of a Quantum Computing platform	
	3.2	Details of q-bit system of information representation: Block Sphere Multi-qubits States Quantum superposition of qubits (valid and invalid superposition) Quantum Entanglement Useful states from quantum algorithmic perceptive e.g. Bell State Operation on qubits: Measuring and transforming using gates. Quantum Logic gates and Circuit No Cloning Theorem and Teleportation	
	3.3	Programming model for a Quantum Computing Program Steps performed on classical computer Steps performed on Quantum Computer Moving data between bits and qubits.	
4.0		Quantum Algorithms and Error correction	**06**
	4.1	Quantum Algorithms Shor's Algorithm Grover's Algorithm Deutsch's Algorithm Deutsch -Jozsa Algorithm	
	4.2	Quantum error correction using repetition codes 3 qubit codes Shor's 9 qubit error correction Code	
5.0		Quantum Hardware	**10**
	5.1	Ion Trap Qubits The DiVincenzo Criteria Lagrangian and Hamiltonian Dynamics in a Nutshell: Dynamics of a Translating Rotor	
	5.2	Quantum Mechanics of a Free Rotor: A Poor Person's Atomic Model: Rotor Dynamics and the Hadamard Gate, Two-Qubit Gates The Cirac-Zoller Mechanism: Quantum Theory of Simple Harmonic Motion, A Phonon-Qubit Pair Hamiltonian, Light-Induced Rotor-Phonon Interactions,	

		Trapped Ion Qubits, Mølmer-Sørenson Coupling ..	
	5.3	Cavity Quantum Electrodynamics (cQED): Eigenstates of the Jaynes-Cummings Hamiltonian Circuit QED (cirQED): Quantum LC Circuits, Artificial Atoms, Superconducting Qubits	
	5.4	Quantum computing with spins: Quantum inverter realized with two exchange coupled spins in quantum dots, A 2-qubit spintronic universal quantum gate.	
6.0		OSS Toolkits for implementing Quantum program	**03**
	6.1	IBM quantum experience	
	6.2	Microsoft Q Rigetti PyQuil (QPU/QVM)	
		Total	**39**

Text books:

1. Michael A. Nielsen, "Quantum Computation and Quantum Information", Cambridge University Press.
2. David McMahon, "Quantum Computing Explained", Wiley ,2008
3. Qiskit textbook https://qiskit.org/textbook-beta/
4. Vladimir Silva, Practical Quantum Computing for Developers,2018
5. Bernard Zygelman, A First Introduction to Quantum Computing and Information,2018
6. Supriyo Bandopadhyay and Marc Cahy, "Introduction to Spintronics", CRC Press, 2008.

Reference books

1. The Second Quantum Revolution: From Entanglement to Quantum Computing and Other Super-Technologies, Lars Jaeger
2. La Guardia**,** Giuliano Gladioli "Quantum Error correction codes"Springer,2021

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 7011	Product Life Cycle Management	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 7011	Product Life Cycle Management	20	20	20	80	03	--	--	100

Objectives:

1. To familiarize the students with the need, benefits and components of PLM
2. To acquaint students with Product Data Management & PLM strategies
3. To give insights into new product development program and guidelines for designing and developing a product
4. To familiarize the students with Virtual Product Development

Outcomes: Learner will be able to…

1. Gain knowledge about phases of PLM, PLM strategies and methodology for PLM feasibility study and PDM implementation.
2. Illustrate various approaches and techniques for designing and developing products.
3. Apply product engineering guidelines / thumb rules in designing products for moulding, machining, sheet metal working etc.
4. Acquire knowledge in applying virtual product development tools for components, machining and manufacturing plant

Module	Detailed Contents	Hrs
01	**Introduction to Product Lifecycle Management (PLM):** Product Lifecycle Management (PLM), Need for PLM, Product Lifecycle Phases, Opportunities of Globalization, Pre-PLM Environment, PLM Paradigm, Importance & Benefits of PLM, Widespread Impact of PLM, Focus and Application, A PLM Project, Starting the PLM Initiative, PLM Applications **PLM Strategies:** Industrial strategies, Strategy elements, its identification, selection and implementation, Developing PLM Vision and PLM Strategy , Change management for PLM	10
02	**Product Design:** Product Design and Development Process, Engineering Design, Organization and Decomposition in Product Design, Typologies of Design Process Models, Reference Model, Product Design in the Context of the Product Development Process, Relation with the Development Process Planning Phase, Relation with the Post design Planning Phase, Methodological Evolution in Product Design, Concurrent Engineering, Characteristic Features of Concurrent Engineering, Concurrent Engineering	09

	and Life Cycle Approach, New Product Development (NPD) and Strategies, Product Configuration and Variant Management, The Design for X System, Objective Properties and Design for X Tools, Choice of Design for X Tools and Their Use in the Design Process	
03	**Product Data Management (PDM):** Product and Product Data, PDM systems and importance, Components of PDM, Reason for implementing a PDM system, financial justification of PDM, barriers to PDM implementation	05
04	**Virtual Product Development Tools:** For components, machines, and manufacturing plants, 3D CAD systems and realistic rendering techniques, Digital mock-up, Model building, Model analysis, Modeling and simulations in Product Design, Examples/Case studies	05
05	**Integration of Environmental Aspects in Product Design:** Sustainable Development, Design for Environment, Need for Life Cycle Environmental Strategies, Useful Life Extension Strategies, End-of-Life Strategies, Introduction of Environmental Strategies into the Design Process, Life Cycle Environmental Strategies and Considerations for Product Design	05
06	**Life Cycle Assessment and Life Cycle Cost Analysis:** Properties, and Framework of Life Cycle Assessment, Phases of LCA in ISO Standards, Fields of Application and Limitations of Life Cycle Assessment, Cost Analysis and the Life Cycle Approach, General Framework for LCCA, Evolution of Models for Product Life Cycle Cost Analysis	05
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. John Stark, "Product Lifecycle Management: Paradigm for 21st Century Product Realisation", Springer-Verlag, 2004. ISBN: 1852338105
2. Fabio Giudice, Guido La Rosa, AntoninoRisitano, "Product Design for the environment-A life cycle approach", Taylor & Francis 2006, ISBN: 0849327229
3. SaaksvuoriAntti, ImmonenAnselmie, "Product Life Cycle Management", Springer, Dreamtech, ISBN: 3540257314
4. Michael Grieve, "Product Lifecycle Management: Driving the next generation of lean thinking", Tata McGraw Hill, 2006, ISBN: 0070636265

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 7012	Reliability Engineering	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme								
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 7012	Reliability Engineering	20	20	20	80	03	--	--	100

Objectives:

1. To familiarize the students with various aspects of probability theory
2. To acquaint the students with reliability and its concepts
3. To introduce the students to methods of estimating the system reliability of simple and complex systems
4. To understand the various aspects of Maintainability, Availability and FMEA procedure

Outcomes: Learner will be able to…

1. Understand and apply the concept of Probability to engineering problems
2. Apply various reliability concepts to calculate different reliability parameters
3. Estimate the system reliability of simple and complex systems
4. Carry out a Failure Mode Effect and Criticality Analysis

Module	Detailed Contents	Hrs
01	**Probability theory:** Probability: Standard definitions and concepts; Conditional Probability, Baye's Theorem. **Probability Distributions:** Central tendency and Dispersion; Binomial, Normal, Poisson, Weibull, Exponential, relations between them and their significance. **Measures of Dispersion:** Mean, Median, Mode, Range, Mean Deviation, Standard Deviation, Variance, Skewness and Kurtosis.	08
02	**Reliability Concepts:** Reliability definitions, Importance of Reliability, Quality Assurance and Reliability, Bath Tub Curve. **Failure Data Analysis:** Hazard rate, failure density, Failure Rate, Mean Time To Failure (MTTF), MTBF, Reliability Functions. **Reliability Hazard Models:** Constant Failure Rate, Linearly increasing, Time Dependent Failure Rate, Weibull Model. Distribution functions and reliability analysis.	08
03	**System Reliability:** System Configurations: Series, parallel, mixed configuration, k out of n structure, Complex systems.	05
04	**Reliability Improvement:** Redundancy Techniques: Element redundancy, Unit redundancy, Standby redundancies. Markov analysis. System Reliability Analysis – Enumeration method, Cut-set method, Success Path method, Decomposition method.	08
05	**Maintainability and Availability:** System downtime, Design for Maintainability: Maintenance requirements, Design methods: Fault Isolation and self-diagnostics, Parts	05

	standardization and Interchangeability, Modularization and Accessibility, Repair Vs Replacement. Availability – qualitative aspects.	
06	**Failure Mode, Effects and Criticality Analysis:** Failure mode effects analysis, severity/criticality analysis, FMECA examples. Fault tree construction, basic symbols, development of functional reliability block diagram, Fau1t tree analysis and Event tree Analysis	05
Total		39

<u>Assessment</u>:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. L.S. Srinath, "Reliability Engineering", Affiliated East-Wast Press (P) Ltd., 1985.
2. Charles E. Ebeling, "Reliability and Maintainability Engineering", Tata McGraw Hill.
3. B.S. Dhillion, C. Singh, "Engineering Reliability", John Wiley & Sons, 1980.
4. P.D.T. Conor, "Practical Reliability Engg.", John Wiley & Sons, 1985.
5. K.C. Kapur, L.R. Lamberson, "Reliability in Engineering Design", John Wiley & Sons.
6. Murray R. Spiegel, "Probability and Statistics", Tata McGraw-Hill Publishing Co. Ltd.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 7013	Management Information System	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 7013	Management Information System	20	20	20	80	03	--	--	100

Objectives:

1. The course is blend of Management and Technical field.
2. Discuss the roles played by information technology in today's business and define various technology architectures on which information systems are built
3. Define and analyze typical functional information systems and identify how they meet the needs of the firm to deliver efficiency and competitive advantage
4. Identify the basic steps in systems development

Outcomes: Learner will be able to…

1. Explain how information systems Transform Business
2. Identify the impact information systems have on an organization
3. Describe IT infrastructure and its components and its current trends
4. Understand the principal tools and technologies for accessing information from databases to improve business performance and decision making
5. Identify the types of systems used for enterprise-wide knowledge management and how they provide value for businesses

Module	Detailed Contents	Hrs
01	Introduction To Information Systems (IS): Computer Based Information Systems, Impact of IT on organizations, Imporance of IS to Society. Organizational Strategy, Competitive Advantages and IS.	4
02	Data and Knowledge Management: Database Approach, Big Data, Data warehouse and Data Marts, Knowledge Management. Business intelligence (BI): Managers and Decision Making, BI for Data analysis and Presenting Results	7
03	Ethical issues and Privacy: Information Security. Threat to IS, and Security Controls	7
04	Social Computing (SC): Web 2.0 and 3.0, SC in business-shopping, Marketing, Operational and Analytic CRM, E-business and E-commerce – B2B B2C. Mobile commerce.	7
05	Computer Networks Wired and Wireless technology, Pervasive computing, Cloud computing model.	6

06	Information System within Organization: Transaction Processing Systems, Functional Area Information System, ERP and ERP support of Business Process. Acquiring Information Systems and Applications: Various System development life cycle models.	8
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Kelly Rainer, Brad Prince, Management Information Systems, Wiley
2. K.C. Laudon and J.P. Laudon, Management Information Systems: Managing the Digital Firm, 10^{th} Ed., Prentice Hall, 2007.
3. D. Boddy, A. Boonstra, Managing Information Systems: Strategy and Organization, Prentice Hall, 2008

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 7014	Design of Experiments	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 7014	Design of Experiments	20	20	20	80	03	--	--	100

Objectives:

1. To understand the issues and principles of Design of Experiments (DOE)
2. To list the guidelines for designing experiments
3. To become familiar with methodologies that can be used in conjunction with experimental designs for robustness and optimization

Outcomes: Learner will be able to…

1. Plan data collection, to turn data into information and to make decisions that lead to appropriate action
2. Apply the methods taught to real life situations
3. Plan, analyze, and interpret the results of experiments

Module	Detailed Contents	Hrs
01	**Introduction** 1.1 Strategy of Experimentation 1.2 Typical Applications of Experimental Design 1.3 Guidelines for Designing Experiments 1.4 Response Surface Methodology	06
02	**Fitting Regression Models** 2.1 Linear Regression Models 2.2 Estimation of the Parameters in Linear Regression Models 2.3 Hypothesis Testing in Multiple Regression 2.4 Confidence Intervals in Multiple Regression 2.5 Prediction of new response observation 2.6 Regression model diagnostics 2.7 Testing for lack of fit	08
03	**Two-Level Factorial Designs** 3.1 The 2^2 Design 3.2 The 2^3 Design 3.3 The General 2^k Design 3.4 A Single Replicate of the 2^k Design 3.5 The Addition of Center Points to the 2^k Design,	07

	3.6 Blocking in the 2^k Factorial Design 3.7 Split-Plot Designs	
04	**Two-Level Fractional Factorial Designs** 4.1 The One-Half Fraction of the 2^k Design 4.2 The One-Quarter Fraction of the 2^k Design 4.3 The General 2^{k-p} Fractional Factorial Design 4.4 Resolution III Designs 4.5 Resolution IV and V Designs 4.6 Fractional Factorial Split-Plot Designs	07
05	**Response Surface Methods and Designs** 5.1 Introduction to Response Surface Methodology 5.2 The Method of Steepest Ascent 5.3 Analysis of a Second-Order Response Surface 5.4 Experimental Designs for Fitting Response Surfaces	07
06	**Taguchi Approach** 6.1 Crossed Array Designs and Signal-to-Noise Ratios 6.2 Analysis Methods 6.3 Robust design examples	04
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Raymond H. Mayers, Douglas C. Montgomery, Christine M. Anderson-Cook, Response Surface Methodology: Process and Product Optimization using Designed Experiment, 3rd edition, John Wiley & Sons, New York, 2001
2. D.C. Montgomery, Design and Analysis of Experiments, 5th edition, John Wiley & Sons, New York, 2001
3. George E P Box, J Stuart Hunter, William G Hunter, Statics for Experimenters: Design, Innovation and Discovery, 2nd Ed. Wiley
4. W J Dimond, Peactical Experiment Designs for Engineers and Scintists, John Wiley and Sons Inc. ISBN: 0-471-39054-2
5. Design and Analysis of Experiments (Springer text in Statistics), Springer by A.M. Dean, and D. T.Voss

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 7015	Operations Research	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 7015	Operations Research	20	20	20	80	03	--	--	100

Objectives:

1. Formulate a real-world problem as a mathematical programming model.
2. Understand the mathematical tools that are needed to solve optimization problems.
3. Use mathematical software to solve the proposed models.

Outcomes: Learner will be able to…

1. Understand the theoretical workings of the simplex method, the relationship between a linear program and its dual, including strong duality and complementary slackness.
2. Perform sensitivity analysis to determine the direction and magnitude of change of a model's optimal solution as the data change.
3. Solve specialized linear programming problems like the transportation and assignment problems, solve network models like the shortest path, minimum spanning tree, and maximum flow problems.
4. Understand the applications of integer programming and a queuing model and compute important performance measures

Module	Detailed Contents	Hrs
01	**Introduction to Operations Research**: Introduction, , Structure of the Mathematical Model, Limitations of Operations Research **Linear Programming**: Introduction, Linear Programming Problem, Requirements of LPP, Mathematical Formulation of LPP, Graphical method, Simplex Method Penalty Cost Method or Big M-method, Two Phase Method, Revised simplex method, **Duality**, Primal – Dual construction, Symmetric and Asymmetric Dual, Weak Duality Theorem, Complimentary Slackness Theorem, Main Duality Theorem, Dual Simplex Method, Sensitivity Analysis **Transportation Problem**: Formulation, solution, unbalanced Transportation problem. Finding basic feasible solutions – Northwest corner rule, least cost method and Vogel's approximation method. Optimality test: the stepping stone method and MODI method. **Assignment Problem**: Introduction, Mathematical Formulation of the Problem, Hungarian Method Algorithm, Processing of n Jobs Through Two Machines and m	14

	Machines, Graphical Method of Two Jobs m Machines Problem Routing Problem, Travelling Salesman Problem **Integer Programming Problem**: Introduction, Types of Integer Programming Problems, Gomory's cutting plane Algorithm, Branch and Bound Technique. Introduction to Decomposition algorithms.	
02	**Queuing models**: queuing systems and structures, single server and multi-server models, Poisson input, exponential service, constant rate service, finite and infinite population	05
03	**Simulation**: Introduction, Methodology of Simulation, Basic Concepts, Simulation Procedure, Application of Simulation Monte-Carlo Method: Introduction, Monte-Carlo Simulation, Applications of Simulation, Advantages of Simulation, Limitations of Simulation	05
04	**Dynamic programming**. Characteristics of dynamic programming. Dynamic programming approach for Priority Management employment smoothening, capital budgeting, Stage Coach/Shortest Path, cargo loading and Reliability problems.	05
05	**Game Theory**. Competitive games, rectangular game, saddle point, minimax (maximin) method of optimal strategies, value of the game. Solution of games with saddle points, dominance principle. Rectangular games without saddle point – mixed strategy for 2 X 2 games.	05
06	**Inventory Models**: Classical EOQ Models, EOQ Model with Price Breaks, EOQ with Shortage, Probabilistic EOQ Model,	05
	Total	39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Taha, H.A. "Operations Research - An Introduction", Prentice Hall, (7th Edition), 2002.
2. Ravindran, A, Phillips, D. T and Solberg, J. J. "Operations Research: Principles and Practice", John Willey and Sons, 2nd Edition, 2009.
3. Hiller, F. S. and Liebermann, G. J. "Introduction to Operations Research", Tata McGraw Hill, 2002.
4. Operations Research, S. D. Sharma, KedarNath Ram Nath-Meerut.
5. Operations Research, KantiSwarup, P. K. Gupta and Man Mohan, Sultan Chand & Sons.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 7016	Cyber Security and Laws	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 7016	Cyber Security and Laws	20	20	20	80	03	--	--	100

Objectives:

1. To understand and identify different types cybercrime and cyber law
2. To recognized Indian IT Act 2008 and its latest amendments
3. To learn various types of security standards compliances

Outcomes: Learner will be able to…

1. Understand the concept of cybercrime and its effect on outside world
2. Interpret and apply IT law in various legal issues
3. Distinguish different aspects of cyber law
4. Apply Information Security Standards compliance during software design and development

Module	Detailed Contents	Hrs
01	**Introduction to Cybercrime:** Cybercrime definition and origins of the world, Cybercrime and information security, Classifications of cybercrime, Cybercrime and the Indian ITA 2000, A global Perspective on cybercrimes.	4
02	**Cyber offenses & Cybercrime:** How criminal plan the attacks, Social Engg, Cyber stalking, Cyber café and Cybercrimes, Botnets, Attack vector, Cloud computing, Proliferation of Mobile and Wireless Devices, Trends in Mobility, Credit Card Frauds in Mobile and Wireless Computing Era, Security Challenges Posed by Mobile Devices, Registry Settings for Mobile Devices, Authentication Service Security, Attacks on Mobile/Cell Phones, Mobile Devices: Security Implications for Organizations, Organizational Measures for Handling Mobile, Devices-Related Security Issues, Organizational Security Policies and Measures in Mobile Computing Era, Laptops	9
03	**Tools and Methods Used in Cyberline** Phishing, Password Cracking, Keyloggers and Spywares, Virus and Worms, Steganography, DoS and DDoS Attacks, SQL Injection, Buffer Over Flow, Attacks on Wireless Networks, Phishing, Identity Theft (ID Theft)	6
04	**The Concept of Cyberspace** E-Commerce , The Contract Aspects in Cyber Law ,The Security Aspect of Cyber Law ,The Intellectual Property Aspect in Cyber Law , The Evidence Aspect in Cyber Law , The Criminal Aspect in Cyber Law, Global Trends in Cyber Law , Legal Framework for Electronic Data Interchange Law Relating to Electronic Banking , The Need for an Indian Cyber Law	8
05	**Indian IT Act.**	6

	Cyber Crime and Criminal Justice : Penalties, Adjudication and Appeals Under the IT Act, 2000, IT Act. 2008 and its Amendments	
06	**Information Security Standard compliances** SOX, GLBA, HIPAA, ISO, FISMA, NERC, PCI.	6
	Total	39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination.

In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Nina Godbole, Sunit Belapure, *Cyber Security*, Wiley India, New Delhi
2. The Indian Cyber Law by Suresh T. Vishwanathan; Bharat Law House New Delhi
3. The Information technology Act, 2000; Bare Act- Professional Book Publishers, New Delhi.
4. Cyber Law & Cyber Crimes By Advocate Prashant Mali; Snow White Publications, Mumbai
5. Nina Godbole, *Information Systems Security*, Wiley India, New Delhi
6. Kennetch J. Knapp, *Cyber Security & Global Information Assurance* Information Science Publishing.
7. William Stallings, *Cryptography and Network Security*, Pearson Publication
8. Websites for more information is available on : The Information Technology ACT, 2008- TIFR : https://www.tifrh.res.in
9. Website for more information , A Compliance Primer for IT professional : https://www.sans.org/reading-room/whitepapers/compliance/compliance-primer-professionals-33538

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 7017	Disaster Management and Mitigation Measures	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 7017	Disaster Management and Mitigation Measures	20	20	20	80	03	--	--	100

Objectives:

1. To understand physics and various types of disaster occurring around the world
2. To identify extent and damaging capacity of a disaster
3. To study and understand the means of losses and methods to overcome /minimize it.
4. To understand role of individual and various organization during and after disaster
5. To understand application of GIS in the field of disaster management
6. To understand the emergency government response structures before, during and after disaster

Outcomes: Learner will be able to…

1. Get to know natural as well as manmade disaster and their extent and possible effects on the economy.
2. Plan of national importance structures based upon the previous history.
3. Get acquainted with government policies, acts and various organizational structure associated with an emergency.
4. Get to know the simple do's and don'ts in such extreme events and act accordingly.

Module	Detailed Contents	Hrs
01	**Introduction** 1.1 Definition of Disaster, hazard, global and Indian scenario, general perspective, importance of study in human life, Direct and indirect effects of disasters, long term effects of disasters. Introduction to global warming and climate change.	03
02	**Natural Disaster and Manmade disasters:** 2.1 Natural Disaster: Meaning and nature of natural disaster, Flood, Flash flood, drought, cloud burst, Earthquake, Landslides, Avalanches, Volcanic eruptions, Mudflow, Cyclone, Storm, Storm Surge, climate change, global warming, sea level rise, ozone depletion **2.2** Manmade Disasters: Chemical, Industrial, Nuclear and Fire Hazards. Role of growing population and subsequent industrialization, urbanization and changing lifestyle of human beings in frequent occurrences of manmade disasters.	09
03	**Disaster Management, Policy and Administration**	06

	3.1 Disaster management: meaning, concept, importance, objective of disaster management policy, disaster risks in India, Paradigm shift in disaster management. 3.2 Policy and administration: Importance and principles of disaster management policies, command and co-ordination of in disaster management, rescue operations-how to start with and how to proceed in due course of time, study of flowchart showing the entire process.	
04	**Institutional Framework for Disaster Management in India:** 4.1 Importance of public awareness, Preparation and execution of emergency management programme. Scope and responsibilities of National Institute of Disaster Management (NIDM) and National disaster management authority (NDMA) in India. Methods and measures to avoid disasters, Management of casualties, set up of emergency facilities, importance of effective communication amongst different agencies in such situations. 4.2 Use of Internet and softwares for effective disaster management. Applications of GIS, Remote sensing and GPS in this regard.	06
05	**Financing Relief Measures:** 5.1 Ways to raise finance for relief expenditure, role of government agencies and NGO's in this process, Legal aspects related to finance raising as well as overall management of disasters. Various NGO's and the works they have carried out in the past on the occurrence of various disasters, Ways to approach these teams. 5.2 International relief aid agencies and their role in extreme events.	09
06	**Preventive and Mitigation Measures:** 6.1 Pre-disaster, during disaster and post-disaster measures in some events in general 6.2 Structural mapping: Risk mapping, assessment and analysis, sea walls and embankments, Bio shield, shelters, early warning and communication 6.3 Non Structural Mitigation: Community based disaster preparedness, risk transfer and risk financing, capacity development and training, awareness and education, contingency plans. 6.4 Do's and don'ts in case of disasters and effective implementation of relief aids.	06
	Total	39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. 'Disaster Management' by Harsh K.Gupta, Universities Press Publications.
2. 'Disaster Management: An Appraisal of Institutional Mechanisms in India' by O.S.Dagur, published by Centre for land warfare studies, New Delhi, 2011.

3. ‘Introduction to International Disaster Management’ by Damon Copolla, Butterworth Heinemann Elseveir Publications.
4. ‘Disaster Management Handbook’ by Jack Pinkowski, CRC Press Taylor and Francis group.
5. ‘Disaster management & rehabilitation’ by Rajdeep Dasgupta, Mittal Publications, New Delhi.
6. ‘Natural Hazards and Disaster Management, Vulnerability and Mitigation – R B Singh, Rawat Publications
7. Concepts and Techniques of GIS –C.P.Lo Albert, K.W. Yonng – Prentice Hall (India) Publications.

(Learners are expected to refer reports published at national and International level and updated information available on authentic web sites)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 7018	Energy Audit and Management	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 7018	Energy Audit and Management	20	20	20	80	03	--	--	100

Objectives:

1. To understand the importance energy security for sustainable development and the fundamentals of energy conservation.
2. To introduce performance evaluation criteria of various electrical and thermal installations to facilitate the energy management
3. To relate the data collected during performance evaluation of systems for identification of energy saving opportunities.

Outcomes: Learner will be able to…

1. To identify and describe present state of energy security and its importance.
2. To identify and describe the basic principles and methodologies adopted in energy audit of an utility.
3. To describe the energy performance evaluation of some common electrical installations and identify the energy saving opportunities.
4. To describe the energy performance evaluation of some common thermal installations and identify the energy saving opportunities
5. To analyze the data collected during performance evaluation and recommend energy saving measures

Module	Detailed Contents	Hrs
01	**Energy Scenario:** Present Energy Scenario, Energy Pricing, Energy Sector Reforms, Energy Security, Energy Conservation and its Importance, Energy Conservation Act-2001 and its Features. Basics of Energy and its various forms, Material and Energy balance	04
02	**Energy Audit Principles:** Definition, Energy audit- need, Types of energy audit, Energy management (audit) approach-understanding energy costs, Bench marking, Energy performance, Matching energy use to requirement, Maximizing system efficiencies, Optimizing the input energy requirements, Fuel and energy substitution. Elements of monitoring& targeting; Energy audit Instruments; Data and information-analysis. Financial analysis techniques: Simple payback period, NPV, Return on investment (ROI), Internal rate of return (IRR)	08

03	**Energy Management and Energy Conservation in Electrical System:** Electricity billing, Electrical load management and maximum demand Control; Power factor improvement, Energy efficient equipments and appliances, star ratings. **Energy efficiency measures in lighting system, Lighting control:** Occupancy sensors, daylight integration, and use of intelligent controllers. Energy conservation opportunities in: water pumps, industrial drives, induction motors, motor retrofitting, soft starters, variable speed drives.	10
04	**Energy Management and Energy Conservation in Thermal Systems:** Review of different thermal loads; Energy conservation opportunities in: Steam distribution system, Assessment of steam distribution losses, Steam leakages, Steam trapping, Condensate and flash steam recovery system. General fuel economy measures in Boilers and furnaces, Waste heat recovery, use of insulation- types and application. HVAC system: Coefficient of performance, Capacity, factors affecting Refrigeration and Air Conditioning system performance and savings opportunities.	10
05	**Energy Performance Assessment:** On site Performance evaluation techniques, Case studies based on: Motors and variable speed drive, pumps, HVAC system calculations; Lighting System: Installed Load Efficacy Ratio (ILER) method, Financial Analysis.	04
06	**Energy conservation in Buildings:** Energy Conservation Building Codes (ECBC): Green Building, LEED rating, Application of Non-Conventional and Renewable Energy Sources	03
	Total	39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Handbook of Electrical Installation Practice, Geofry Stokes, Blackwell Science
2. Designing with light: Lighting Handbook, By Anil Valia, Lighting System
3. Energy Management Handbook, By W.C. Turner, John Wiley and Sons
4. Handbook on Energy Audits and Management, edited by A. K. Tyagi, Tata Energy Research Institute (TERI).
5. Energy Management Principles, C.B.Smith, Pergamon Press
6. Energy Conservation Guidebook, Dale R. Patrick, S. Fardo, Ray E. Richardson, Fairmont Press
7. Handbook of Energy Audits, Albert Thumann, W. J. Younger, T. Niehus, CRC Press
8. www.energymanagertraining.com
9. www.bee-india.nic.in

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 7019	Development Engineering	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ILO 7019	Development Engineering	20	20	20	80	--	--	--	100

Course objectives:

- To understand the characteristics of rural Society and the Scope, Nature and Constraints of rural Development.
- To study Implications of 73rd CAA on Planning, Development and Governance of Rural Areas
- An exploration of human values, which go into making a _good‘ human being, a _good‘ professional, a _good‘ society and a _good life‘ in the context of work life and the personal life of modern Indian professionals
- To understand the Nature and Type of Human Values relevant to Planning Institutions

Course outcomes:

After successful completion of the course student will be able to

- Apply knowledge for Rural Development.
- Apply knowledge for Management Issues.
- Apply knowledge for Initiatives and Strategies
- Develop acumen for higher education and research.
- Master the art of working in group of different nature.
- Develop confidence to take up rural project activities independently

Module No.	Unit No.	Topics	Hrs.
1.0			**08**
	1.1	Introduction to Rural Development Meaning, nature and scope of development; Nature of rural society in India; Hierarchy of settlements; Social, economic and ecological constraints for rural development Roots of Rural Development in India Rural reconstruction and Sarvodaya programme before independence; Impact of voluntary effort and Sarvodaya Movement on rural development; Constitutional direction, directive principles; Panchayati Raj - beginning of planning and community development; National extension services.	
2.0			**04**
	2.1	Post-Independence rural Development Balwant Rai Mehta Committee - three tier system of rural local Government; Need and scope for people's participation and Panchayati Raj; Ashok Mehta Committee - linkage between Panchayati Raj, participation and rural development	
3.0			**06**
	3.1	Rural Development Initiatives in Five Year Plans Five Year Plans and Rural Development; Planning process at National, State, Regional and District levels; Planning, development, implementing and monitoring organizations and agencies; Urban and rural interface - integrated approach and local plans; Development initiatives and their convergence; Special component plan and sub-plan for the weaker section; Micro-eco zones; Data base for local planning; Need for decentralized planning; Sustainable rural development.	
4.0			**04**
	4.1	Post 73rd Amendment Scenario 73rd Constitution Amendment Act, including - XI schedule, devolution of powers, functions and finance; Panchayati Raj institutions - organizational linkages; Recent changes in rural local planning; Gram Sabha - revitalized Panchayati Raj; Institutionalization; resource mapping, resource mobilization including social mobilization; Information Technology and rural planning; Need for further amendments.	
5.0			**10**

	5.1	Values and Science and Technology Material development and its values; the challenge of science and technology; Values in planning profession, research and education. Types of Values Psychological values — integrated personality; mental health; Societal values — the modern search for a good society; justice, democracy, rule of law, values in the Indian constitution; Aesthetic values — perception and enjoyment of beauty; Moral and ethical values; nature of moral judgment; Spiritual values; different concepts; secular spirituality; Relative and absolute values; Human values— humanism and human values; human rights; human values as freedom, creativity, love and wisdom.	
6.0			**04**
	6.1	Ethics Canons of ethics; ethics of virtue; ethics of duty; ethics of responsibility; Work ethics; Professional ethics; Ethics in planning profession, research and education	
		Total	**36**

References :

1. ITPI, Village Planning and Rural Development, ITPI, New Delhi
2. Thooyavan, K.R. Human Settlements: A 2005 MA Publication, Chennai
3. GoI, Constitution (73rd GoI, New Delhi Amendment) Act, GoI, New Delhi
4. Planning Commission, Five Year Plans, Planning Commission
5. Planning Commission, Manual of Integrated District Planning, 2006, Planning Commission New Delhi
6. Planning Guide to Beginners
7. Weaver, R.C., The Urban Complex, Doubleday.
8. Farmer, W.P. et al, Ethics in Planning, American Planning Association, Washington.
9. How, E., Normative Ethics in Planning, Journal of Planning Literature, Vol.5, No.2, pp. 123-150.
10. Watson, V. , Conflicting Rationalities: -- Implications for Planning Theory and Ethics, Planning Theory andPractice, Vol. 4, No.4, pp.395 – 407

Internal Assessment:

Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL701	Microwave Engineering Laboratory	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECL701	Microwave Engineering Laboratory	--	--	--	--	--	25	25	50

Course Objectives:

1. To become familiar working with rectangular waveguides and doing microwave bench set up
2. To determine the characteristics of various microwave components
3. To be able to measure wave parameters like impedance, frequency, wavelength using microwave bench and VSWR/power meter
4. To study characteristics and behavior of various microwave semiconductor devices.

Course outcomes: On completion of this lab course the students will be able to:

1. Able to handle microwave equipments
2. Able to understand microwave measurements and test the characteristics of microwave components
3. Able to understand Wave guide and transmission line measurements
4. Demonstrate working of microwave semiconductor devices
5. Demonstrate the microwave bench set up and conducting measurements of different parameters

	Suggested List of Experiments
1	Measurement of microwave frequency using direct and indirect method
2	Measurement of guide wavelength
3	Measurement of VSWR of unknown load
4	Measurement of impedance of unknown load.
5	Study of field patterns of various modes inside a rectangular waveguide cavity using Virtual lab
6	Study of field patterns of various modes inside a rectangular waveguide using Virtual lab
7	Find the change in characteristics impedance and reflection coefficients of the transmission line by changing the dielectric properties of materials Embedded between two conductors. using Virtual lab
8	Determination of VI characteristics of Gunn diode using microwave test bench.
9	Measurement of attenuation
10	Measurement of microwave power
11	Characterization of E plane TEE, H plane TEE and Magic TEE
12	Measurement of reflection coefficient using transmission line bench

Term Work:

At least 8 experiments covering the entire syllabus must be given "**Batch Wise**". The experiments can be conducted with the help of appropriate hardware setup/simulation tool (preferably open source)/breadboard and components. Teacher should refer the suggested list of experiments and can design additional experiments to acquire practical design skills. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting and innovative.

Term work assessment must be based on the overall performance of the student with every experiment and assignments graded from time to time. The grades will be converted to marks as per **"Credit and Grading System"** manual and should be added and averaged. Based on the above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus. Students are encouraged to share their experiments codes on online repository. Practical exam slip should cover all the 8 experiments for examination.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL702	Mobile Communication System Laboratory	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam.			
		Test 1	Test 2	Avg.				
ECL702	Mobile Communication System Laboratory	--	--	--	--	25	25	50

Course objectives:

1. To understand the inter-dependencies of design parameters of cellular system.
2. To examine orthogonality condition for CDMA systems.
3. To Classify different types of propagation models and analyze the link budget
4. To understand the working principles of OFDM, MIMO, and Cognitive radio.

Course outcomes:

After the successful completion of the course student will be able to

1. Demonstrate the effect of cellular system design parameters on system capacity and quality of service.
2. Compare and contrast trunking radio systems.
3. Examine the effect of small-scale fading parameters on the performance of radio channel characteristics.
4. Analyze link budget for various propagation path-loss models.
5. Summarize the attributes of OFDM, MIMO, and Cognitive radio.
6. Evaluate the performance of different MIMO systems.

Suggested list of experiments: (Course teacher can design their own experiments based on the prescribed syllabus)

Suggested Experiment List

- To observe the effect of velocity and direction of arrival of a vehicle on Doppler frequency.
- To observe the effect of Cluster size (N) on C/I ratio and comment on the voice quality.
- To observe the effect of incidence angle on reflection coefficient.
- To observe the effect of different propagation models on coverage distance.
- To analyze the effect of delay on blocking probability of a call for Erlang B and Erlang C systems.

- To observe the effect of C/I ratio in a sectorised cell site and perform worst case analysis for different values of N and degree of sectorisation
    - A) Worst case C/I in a 3-sector cellular system for N = 7
    - B) Worst case C/I in a 3-sector cellular system for N = 4
    - C) Worst case C/I in a 6-sector cellular system for N = 7
    - D) Worst case C/I in a 6-sector cellular system for N = 4
- To generate Pseudo noise code used in a CDMA system.
- To generate Walsh Codes using Hadamard Matrix.
- To plot Knife edge diffraction gain as a function of Fresnel diffraction parameter.
- To analyze the effect of multipath diversity (RAKE receiver) on Bit Error Rate in CDMA system.
- To plot channel capacity versus SNR for different MIMO systems.
- Simulation of OFDMA system.
- GSM Network simulation.
- CDMA Network simulation.
- Simulation of spectrum sensing using energy detection method in cognitive radio.
- Demonstration of OFDM / MIMO /Cognitive radio.

Term Work, Practical and Oral:

At least 8 experiments covering the entire syllabus must be given "**Batch Wise"**. The experiments can be conducted with the help of simulation tool (preferably open source) and breadboard and components. Teacher should refer the suggested list of experiments and can design additional experiments to acquire practical design skills. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting and innovative.

Term work assessment must be based on the overall performance of the student with every experiment and assignments graded from time to time. The grades will be converted to marks as per **"Credit and Grading System"** manual and should be added and averaged. Based on the above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus. Students are encouraged to share their experiments codes on online repository. Practical exam slip should cover all the 8 experiments for examination.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECP701	Major Project-1	--	06	--	--	3	--	3

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECP701	Major Project-1	--	--	--	--	25	25	--	50

Objective: The Project work enables the students to develop the required skills and knowledge gained during the programme by applying them for the analysis of a specific problem or issue, via a substantial piece of work which is carried out over an extended period. It also enables the students to demonstrate the proficiency in the design of a research project, application of appropriate research methods, collection and analysis of data and presentation of results.

Guidelines:

1. **Project Topic:**
   - To proceed with the project work it is very important to select a right topic. Project can be undertaken on any domain of electronics and telecommunication programme. Research and development projects on problems of practical and theoretical interest should be encouraged.
   - Project work must be carried out by the group of at least two students and maximum four and must be original.
   - Students can certainly take ideas from anywhere, but be sure that they should evolve them in the unique way to suit their project requirements.
   - The project work can be undertaken in a research institute or organization/company/any business establishment.
   - Student must consult internal guide along with external guide (if any) in selection of topic.
   - Head of department and senior staff in the department will take decision regarding selection of projects.
   - Student has to submit weekly progress report to the internal guide and whereas internal guide has to keep track on the progress of the project and also has to maintain attendance report. This progress report can be used for awarding the term work marks.

- In case of industry projects, visit by internal guide will be preferred.

2. **Project Report Format:**

At the end of semester a project report should preferably contain at least following details:-

- Abstract
- Introduction
- Literature Survey
    a) Survey Existing system
    b) Limitation of the Existing system or research gap
    c) Problem Statement and Objective
    d) Scope
- Proposed System
    a) Analysis/Framework/ Algorithm
    b) Details of Hardware & Software
    c) Design details
    d) Methodology (your approach to solve the problem)
- Implementation Plan for next semester
- Conclusion
- References

3. **Term Work:**

Distribution of marks for term work shall be as follows:

a) Weekly Attendance on Project Day
b) Contribution in the Project work
c) Project Report (Spiral Bound)
d) Term End Presentation (Internal)

The final certification and acceptance of TW ensures the satisfactory performance on the above aspects.

4. **Oral & Practical :**

Oral &Practical examination of Project-I should be conducted by Internal and External examiners approved by University of Mumbai. Students have to give presentation and demonstration on the Project- I.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC801	Optical Communication and Networks	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECC801	Optical Communication and Networks	20	20	20	80	03	--	--	100

Course pre-requisite:

FEC102 - Engineering Physics-I
FEC202 - Engineering Physics-II
ECC302 - Electronic Devices & Circuits
ECC405 - Principles of Communication Engineering
ECC501 - Digital Communication
ECC601 - Electromagnetics and Antenna

Course Objectives:

- Introduction to optical fiber network it's need, elements and fundamentals.
- To learn Parameters that limits the repeaterless transmission , its mitigation and Managing techniques.
- To learn high speed optical sources, detectors and Amplifiers.
- Study the multiplexing schemes SDH ,PDH and WDM and its applications for current and NGNs
- To have an insight into optical packet switched, bust switched and advanced networks
- Learn high speed network management techniques and challenges in its counterpart Free Space Optics

Course Outcome:

- Understand optical networks at large by identifying the types of fibers, cables and deployment.
- Design point to point optical fiber communication links using appropriate optical fibers, light sources, couplers, detectors, and multiplexers.
- Design a short haul or long-haul optical network with repeater by incorporating suitable amplifiers.
- Compare SDH, PDH and WDM techniques and implement.
- Explore concepts of designing and operating principles of modern optical communication systems and networks.
- Apply the knowledge acquired to design the next generation fiber and FSO networks for indoor and outdoor applications

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction and Basics of Optical fiber communication**	**07**
	1.1	Historical Development, Electromagnetic Spectrum, Optical Bands and Windows, Need for optical fiber communication, Fiber optic cable types and color codes, Block diagram, advantages and disadvantages of optical fiber cables, loss and bandwidth, applications and deployment.	
	1.2	Basics of Optical Fiber: Review of Ray theory, Wave theory, Light propagation in optical fiber Classification of optical fibers, Propagation modes, MFD in SMF	
	1.3	Fiber material, Fabrication techniques for high quality fiber: MCVD, fiber joints, fiber connectors, splices. Brief introduction to Photonic Crystal Fiber and its types.	
2.0		**Transmission Characteristic of Optical Fiber**	**07**
	2.1	Dispersion in Optical fiber, types of dispersion, Dispersion compensation techniques and dispersion measurements, Time domain and Frequency Domain measurements. Dispersion management, Need for dispersion management and Post compensation techniques.	
	2.2	Transmission losses in the optical fiber, Attenuation, Absorption losses, radiation losses and linear scattering losses, Comparison of optical fibers, Measurement of attenuation: Insertion loss, Return loss, OTDR.	
3.0		**Optical Communication Systems**	**07**
	3.1	Working principle and characteristics of sources Edge emitting LED,, Edge emitting LASER, VCEL, Spectrum, Noise, and Optical amplifiers .	
	3.2	Working principle and characteristics of detectors (PIN, APD),coherent and non-coherent detection, Intensity modulated direct detection, optical receivers, receiver performance: Bit error rate, Q function and Eye diagram	
	3.3	Point to point links system considerations, link power budget, and rise time budget	
4.0		**Optical Network System Components and Optical Networks**	**08**
	4.1	Couplers, isolators , circulators, multiplexers, Optical routers and filters - fiber gratings, Fabry ,switches and wavelength converters ,Add drop multiplexers	
	4.2	SONET and SDH standards, architecture of optical transport networks (OTNs), protection schemes in SONET/SDH, PDH	
	4.3	Operational principle of WDM, WDM network elements and Architectures. Types of WDM Networks, WDM Access Network, WDM Metro Networks, WDM Long Haul Networks Data center networks and Elastic Networks	
5.0		**Packet Switching and Access Networks**	**04**
	5.1	OTDM, multiplexing and de-multiplexing, synchronization and broadcast OTDM networks.	
	5.2	Network architecture overview, optical access networks. FTTH Network	
	5.3	Optical Burst switching Networks	
6.0		**Network Design and Management**	**06**
	6.1	Transmission system model, power penalty, transmitter, receiver, optical amplifiers, crosstalk.	
	6.2	Network management functions, configuration management, performance management, fault management, optical safety, and service interface	
	6.3	Introduction to free space optics and its challenges	
		Total	**39**

Text books:

1. John M. Senior, —*Optical Fiber Communication*‖, Prentice Hall of India Publication, Chicago, 3rd Edition, 2013
2. Gerd Keiser, —*Optical Fiber Communication*‖, Mc-Graw Hill Publication , Singapore, 4th Edition, 2012
3. T.L.Singhal –*Optical Fiber Communication Principles and Applications,* Cambridge Press, Edition 2016
4. Kumar Sivarajan and Rajiv Ramaswamy, Morgan Kauffman, *Optical Networks: A Practical Perspective,* Elsevier Publication Elsevier India Pvt. Ltd, 3rd Edition, 2010.
5. Ivan B. Djordjevin,__*Advanced Optical and Wireless Communication Systems,*Springer,Edition 2018.
6. Debasish Datta,*Optical Networks*,Oxword Cambridge University Press,2021
7. Kaushal, H.Jain, V.K. Kar,S, *Free Space Optical Communication* ,Springer,2017

Reference books

1. G Agarwal, —*Fiber optic communication Systems*‖, John Wiley and Sons, 3rd Edition, New York 2014
2. Rajiv Ramaswami and Kumar N. Sivarajan, —*Optical Networks: A Practical Perespective*‖, Elsevier Publication Elsevier India Pvt.ltd, 3rd Edition, 2010
3. P.E.Green, —*Optical Networks*‖, Prentice Hall,1994
4. Biswanath Mukherjee, —*Optical Communication Networks*‖, McGraw-Hill, 1997.
5. Le Nguyen Binh, —*Optical Fiber Communication System: Theory and Practice with MATLAB and Simulink*‖, CRC Press, 2010
6. 2. Harry G. Parros, Communication Oriented Networks, Wiley
7. G. Agrwal, *Fiber Optic Communication Systems*, John Wiley and Sons, 3rd Edition, New York, 2014.

Further reading:

https://www.iitg.ac.in/psm/qip2015/material/Subir_Bandyopadhyay_Lecture1.pdf
https://www.rp-photonics.com/fiber_fabrication.html
www.osa.org

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8011	System on Chip Design	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (in Hrs.)	Term Work	Practical And Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. Of Test 1 and Test 2					
ECCDLO 8011	System on Chip Design	20	20	20	80	03	--	--	100

Course Prerequisite:

ECC303 Digital System Design
ECC503 Digital VLSI Design
ECM601 Mini Project 2B- FPGA based Project

Course Objectives:

1. To introduce modern system design using SoC
2. To understand the concept of hardware software co-design
3. To learn software and hardware design integration

Course Outcome:

After successful completion of the course student will be able to
CO1: Explain basics of SoC design
CO2: Design and verify SoC system
CO3: Explain physical design flow
CO4: Analyze routing issues in SoC Design
CO5: Interpret complex SoC design issues
CO6: Explain non-technical issues related to SoC

Module No.	Unit No.	Topics	Hrs.
1		**Introduction to SoC Design**	**08**
	1.1	The fundamental trends of SoC design, SoC design flow, The Semiconductor Economics, Challenges in SoC design	03
	1.2	Hardware system structure, Software structure, Accelerating Processors for traditional software task, System Design with multiple processor design	05
2		**System Level Design**	**05**
	2.1	Complex SoC system architecture, Processor centric SoC organization, Communication Design – Hardware and Software interconnects	03
	2.2	Balancing computation and Communication, SoC Design flow, Non-processor building block in SoC design	02
3		**RTL Synthesis**	**08**
	3.1	Review of Verilog - RTL Coding and RTL Synthesis RTL coding guidelines, Synthesizable coding style, FSM Coding style, Memory Modelling.	08
4		**SoC Verification**	**08**
	4.1	Verification technology options, Verification methodology. System level verification, block-level verification. Timing verification.	08
5		**Physical Design**	**06**
	5.1	Partitioning, Floor Planning, Placement, Routing, Goals of routing, Global routing, Physical verification and design sign-off.	06
6		**Reconfigurable SoC Arithmetic: Case Study**	**04**
	6.1	16 bit Carry Skip Adder on FPGA using LUT, 16 bit Carry Select Adder on FPGA using LUT, Divide-and-conquer 4 × 4 multiplier design using LUT	04
		Total	**39**

Textbooks:

1. Engineering the Complex SOC: Fast, Flexible Design with Configurable Processors-Chris Rowen, Pesrson, 2004.
2. System on a chip verification: Methodology and Verification-Second edition, Prakash Rashinkar, Peter Paterson, Leena Singh, Kluwer Academic Publishers
3. Digital Design with RTL Design, VHDL and VERILOG- Frank Vahid, John Wiley and Sons Publisher, 2010

Reference Books:

1. System-on-a-Chip: Design and Test- Rochit Rajsuman-Artech house-2002
2. VLSI Physical design Automation: Theory and Practice, Sadiq Sait, Habib Youssef, World Scientific Publishing, 1999
3. Surviving the SoC revolution- Henry Chang, Larry Cooke, Grant Martin, Kluwer Academic Publishers-2002

E-Resources:

1. https://nptel.ac.in/content/storage2/courses/117101058/downloads/Lec-2.pdf

2. https://www.btechguru.com/engineering-videos--electrical-engineering--circuit-theory--system-on-chip-(soc)-video-lecture--1790--4--21.html

3. http://www.asic.co.in/DesignGuidlinesRTLcoding.htm#:~:text=1.1%20STYLE%20AND%20NAMING%20GUIDELINES&text=WHY%3A%20Readability%20is%20required%20to,well%20as%20transferability%20between%20designers.&text=WHY%3A%20To%20avoid%20conflict%2C%20module,%E2%80%9Ccontrol%E2%80%9D%20are%20too%20generic

Internal Assessment (20-Marks):

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8012	Natural Language Processing	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 8012	Natural Language Processing	20	20	20	80	03	--	--	100

Course Pre-requisite:

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Course Objectives:

1. To understand natural language processing and to learn how to apply basic algorithms in this field.
2. To get acquainted with the basic concepts and algorithmic description of the main language levels: morphology, syntax, semantics, and pragmatics.
3. To design and implement applications based on natural language processing

Course Outcomes:

After successful completion of the course student will be able to:

1. Have a broad understanding of the field of natural language processing.
2. Understand the mathematical and linguistic preliminaries necessary for various processes in NLP
3. Be able to Design, implement and test algorithms for NLP problems
4. Perform Word-Level, Syntax-Level and Semantic-Level Analysis
5. Develop basic understanding of Pragmatics in NLP
6. Be able to apply NLP techniques to design real world NLP applications

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Natural Language Processing**	**06**
	1.1	The need of NLP. Generic NLP system, Levels of NLP	02
	1.2	Stages in building a Natural Language Processing System. Challenges and ambiguities in NLP Design	04
2.0		**Mathematical and Linguistic Preliminaries**	**06**
	2.1	Probability Theory, Conditional Probability and Independence, Bayes Rule, Random Variables, Probability Distributions, Statistics, Counting, Frequency, Mean and Variance	04
	2.2	English Grammar, Parts of Speech, Phrase Structures	02
3.0		**Word Level Analysis**	**06**
	3.1	Tokenization, Segmentation, Lemmatization, Edit Distance, Collocations, Porter Stemmer, N-gram Language Model	04
	3.2	Morphological Analysis, Derivational and Reflectional Morphology	02
4.0		**Syntax-Analysis**	**08**
	4.1	Tag set for English, Penn Tree bank, Introduction to Parts of Speech Tagging (POST)	02
	4.2	Markov Processes, Hidden Markov Models (HMM)	02
	4.2	Parts of Speech Tagging using Hidden Markov Models, Viterbi Algorithm	04
5.0		**Semantic Analysis**	**08**
	4.1	Lexical Semantics, ambiguous words, word senses, Relations between senses: synonym, antonym, reversives, hyponym, hypernym, meronym, structured polysemy, metonymy, zeugma	04
	4.2	Introduction to WordNet, gloss, synset, sense relations in WordNet. Cosine distance between documents. Word sense disambiguation.	04
6.0		**Pragmatics and applications of NLP**	**05**
	6.1	Reference resolution: Discourse model, Reference Phenomenon, Syntactic and Semantic Constraints on co reference	03
	6.2	Applications of NLP: Categorization, Summarization, Sentiment Analysis, Named Entity Recognition, Machine Translation, Information Retrieval, Question Answer System	02
		Total	**39**

Text Books:

1. Daniel Jurafsky, James H. Martin, Speech and Language Processing‖ Second Edition, Prentice Hall.
2. Christopher D. Manning and Hinrich Schutze, Foundations of Statistical Natural Language Processing, MIT Press.

Reference books

1. Steven Bird, Ewan Klein, Natural Language Processing with Python, O‘Reilly
2. Alexander Clark (Editor), Chris Fox (Editor), Shalom Lappin (Editor), The Handbook of Computational Linguistics and Natural Language Processing

NPTEL / Swayam Course:

1. Course: Natural Language Processing By Prof. Pawan Goyal, IIT Kharagpur https://onlinecourses.nptel.ac.in/noc21_cs102/preview
2. Course: Applied Natural Language Processing By Prof. Ramaseshan R, CMI https://onlinecourses.nptel.ac.in/noc20_cs87/preview

Internal Assessment (20-Marks):

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDL OC8013	Wireless Networks	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDL OC8013	Wireless Networks	20	20	20	80	03	--	--	100

Course Pre-requisite:

ECC602 - Computer Communication and Networks
ECC702 - Mobile Communication System

Course Objectives:

1. To analyze the fundamental architecture, design issues and standards of wireless networks.
2. To compare Personal Area Network (PAN) technologies such as ZigBee, Bluetooth, UWB, NFC and 6LoWPAN.
3. To classify different LAN topologies and technologies and ad hoc networks.
4. To classify network protocols, ad hoc vehicle networks and Wireless MANs.
5. To understand planning and design of GSM and CDMA system in Wireless WANs.
6. To apply Wireless sensor networks concepts to develop an IoT applications.

Course Outcomes:

After successful completion of the course student will be able to:

1. Explain fundamental architecture, design issues and standards of wireless networks.
2. Compare different types of Personal Area Network (PAN) technologies such as ZigBee, Bluetooth, UWB, NFC and 6LoWPAN.
3. Analyze different LAN topologies and technologies and ad hoc networks.
4. Compare various types of network protocols, ad hoc vehicle networks and Wireless MANs.
5. Evaluate the planning and design of performance of GSM and CDMA system in Wireless WANs.
6. Understand the basic network architecture of Wireless sensor networks concepts to develop an IoT applications.

Module No.	Unit No.	Topics	Hrs.
1.0		**Overview of wireless networks**	**04**
	1.1	**Wireless Networks**: Architecture, Classifications, Switching technology, Communication Problems, Reference Models. Networking issues and Networking Standard.	02
	1.2	**Wireless Body Area Networks:** Properties, Network Architecture, Network components and Applications	02
2.0		**Wireless Personal Area Networks**	**10**
	2.1	**WPAN:** Bluetooth (802.15.1): Radio Specifications, Protocol Stack, Link Types, Security, Topologies, Applications.	02
	2.2	**ZigBee (802.15.4):** Radio Specifications, Components, Topologies, Protocol Stack, Applications.	02
	2.3	**RFID:** Radio Specifications, Architecture, Types and applications.	02
	2.4	**Near Field Communication & UWB (802.15.3 a):** Introduction and working.	02
	2.5	**6LoWPAN:** Features, Architecture, protocol stack and applications	02
3.0		**Wireless Local Area Network & Wireless Adhoc Networks**	**06**
	3.1	**Wireless Local Area Network:** Equipment, Topologies, Technologies, Applications, Main features of IEEE802.11a,b, i and n, Protocol Architecture of WLAN	03
	3.2	**Wireless Adhoc Networks**: Features, advantages & Applications **Mobile Adhoc Networks:** Network Architecture, MAC Protocol.	03
4.0		**Wireless Metropolitan & Vehicular Adhoc Networks**	**05**
	4.1	**WMAN(IEEE802.16):**Introduction, WMAN Network Architecture, Network Protocols, Broadband Wireless Networks, Applications	03
	4.2	**Vehicular Adhoc Networks (VANETs):** Characteristics, Protocols & Applications	02
5.0		**Wireless Wide Area Networks**	**06**
	5.1	Planning and design of Wireless Networks, Radio design for a cellular Network	04
	5.2	Receiver sensitivity, Link budget for GSM and CDMA Systems, HSDPA	02
6.0		**Advanced Technologies of Wireless Networks**	**08**
	6.1	**Wireless Sensor Networks:** Network Architecture, Design Considerations, Network Protocol Stack, and Applications	04

	6.2	**Wireless Mesh Network:** Network architecture, Protocols, technologies & Applications	02
	6.3	**Internet of Things:** IoT Conceptual Frame work, Architecture, Technology & Examples. M2M Communication	02
		Total	**39**

Text Books & References:

1. Vijay K. Garg, "Wireless Communication and Networking", Morgan -Kaufmann Series in Networking—Elsevier

2. Kazem Sohraby, Daniel Minoli, and Taieb Znati, "Wireless Sensor Networks: Technology, Protocols, and Applications", Wiley Student Edition

3. Dr SunilkumarS. Manvi, Mahabaleshwar S. Kakkasageri, "Wireless and Mobile Networks Concepts and Protocol"Wiley India Pvt Ltd

4. Raj Kamal, "Internet of Things Architecture & Design Principles" Mcgraw Hill

Internal Assessment (20-Marks):

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8014	Web Design	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 8014	Web Design	20	20	20	80	03	--	--	100

Course pre-requisite:

ETS: 305 – Skill development lab Java
ETC: 405 - Skill development lab Python

Course Objectives:

1. To design and create web pages using HTML5 and CSS3.
2. To implement client-side scripting to static web pages.
3. To create dynamic web pages using server-side scripting.
4. To use MVC framework for web application development.
5. To use web services in web application development

Course Outcome:

After successful completion of the course students will be able to:

1. Design web pages using HTML5 and CSS3.
2. Apply the concepts of client-side validation and scripts to static web pages using JavaScript and its framework.
3. Build responsive web pages using front-end framework Bootstrap.
4. Develop a web application using appropriate web development framework.
5. Understand working of web services

Module No.	Unit No.	Topics	Hrs.
1.0		**INTRODUCTION TO WWW**	**03**
	1.1	Understanding web system architecture, understanding 3 - tier web architecture. Clients, Servers and Communication, The Internet, Basic Internet protocols, World wide web, HTTP Request Message, HTTP Response Message, Web Clients, Web Servers	
2.0		**CLIENTSIDE PROGRAMMING PART I**	**05**
	2.1	**HTML:** Basic structure of an HTML5 document, Creating an HTML5 document, Markup Tags, Heading-Paragraphs, line Breaks HTML5 Tags - Introduction to elements of HTML, Working with Text, Lists, Tables and Frames, Hyperlinks, Images and Multimedia, Forms and other HTML5 controls	
	2.2	**CSS:** Concept of CSS, Creating Style Sheet, CSS Properties, CSS Styling 4 (Background, Text Format, Controlling Fonts), Working with block elements and objects, Lists and Tables, CSS Id and Class, Box Model(Introduction, Border properties, Padding Properties, Margin properties), CSS Advanced (Grouping, Dimension, Display, Positioning, Floating, Align, Pseudo class, Navigation Bar, Image Sprites, Attribute sector)	
3.0		**CLIENTSIDE PROGRAMMING PART II**	**03**
	3.1	**Bootstrap** Introduction to Bootstrap, downloading and installing Bootstrap. The Grid System: Introducing the Grid, Offsetting and Nesting, Responsive Features, Utility Classes, and Supported Devices. CSS Foundations: Typography in Bootstrap, Styling Tables, Styling Forms, Styling Buttons, Images, icons, and Thumbnails. Navigation Systems: Tabs, Pills, and Lists, Breadcrumbs and Pagination, Navigation Bar, Making the Navigation Bar Responsive.	
4.0		**WEB DEVELOPMENT WITH JAVA**	**12**
	4.1	**Server-side programming Java Servlets**: Java Servlet Architecture, Servlet Life Cycle, Form GET and POST actions, Session Handling, Understanding Cookies,	
	4.2	**Client-side programming: JavaScript** Introduction to JavaScript, Lexical Structure, Types, Values, Variables, Expressions and Operators, Statements, Objects, Arrays, Functions, Pattern matching with regular expressions, JavaScript in Web Browsers, The Window object, Scripting Documents, Handling Events.	
	4.3	**Client-side programming: JavaScript framework jQuery** jQuery: jQuery Basics, jQuery Getters and Setters, Altering Document Structure, Handling events with jQuery, Animated Effects, Utility functions, jQuery Selectors and Selection Methods,	
5.0		**SERVERSIDE PROGRAMMING**	**12**
	5.1	MVC architecture - Introduction and applications, Server side-scripting – Laravel Framework Managing Your Project Controllers, Layout, Views, and Other Assets.	
	5.2	Introduction to PHP, PHP Tags, Adding Dynamic content, accessing form variables, identifiers, user-declared variables, Data types, Constants, Operators, Control structures, Conditionals, Iteration constructs, using arrays, string manipulation and regular expressions, reusing code and writing functions.	
	5.3	Designing and creating your web database, Accessing MySQL database from the Web with PHP, Session Control in PHP	
	5.4	Introduction to AJAX: AJAX design basics, AJAX vs Traditional Approach, Rich User Interface using Ajax	

6.0		WEBSERVICES	03
	6.1	Introduction to Web Services: The definition of web services, basic operational model of web services (SOAP and REST), tools and technologies enabling web services, benefits and challenges of using web services.	
	6.2	Web Services Architecture: Web services Architecture and its characteristics, core building blocks of web services, standards and technologies available for implementing web services, web services communication, basic steps of implementing web services	
		Total	**39**

Text Books :

1. "Web Technology Black Book", Dreamtech Press, First Edition, 978-7722- 97
2. WEB TECHNOLOGIES A Computer Science Perspective Jeffrey C. Jackson Duquesne University
3 Developing Java Web Services, R. Nagappan, R. Skoczylas, R.P. Sriganesh, Wiley India, rp 2008
4. Learning Bootstrap Aravind Shenoy Ulrich Sossou PACKT PUBLISHING

Reference Books

1. Ralph Moseley, M.T. Savliya , "Developing Web Applications", Willy India, Second Edition,
2. Robin Nixon, "Learning PHP, MySQL, JavaScript, CSS & HTML5" Third Edition, O'REILLY,2014. (http://www.ebooksbucket.com/uploads/itprogramming/javascript/Learning_PHP_MySQL_Javascript_CSS_HTML5__Robin_Nixon_3e.pdf)
3. Professional Rich Internet Applications: AJAX and Beyond, Dana Moore, Raymond Budd, Edward Benson, Wiley publications. https://ebooks-it.org/0470082801-ebook.htm
4. Jennifer Kyrnin, "SAMS Teach Yourself Bootstrap in 24 hours", 1st edition, Pearson Education.
5. Martin Bean, "Laravel 5 Essentials", PACKT Publishing Ltd
6. Harvey & Paul Deitel & Associates, Harvey Deitel and Abbey Deitel, "Internet and World Wide Web - How To Program", Fifth Edition, Pearson Education, 2011.
7. Achyut S Godbole and Atul Kahate, "Web Technologies", Second Edition, Tata McGraw Hill, 2012.
8. Thomas A Powell, Fritz Schneider, "JavaScript: The Complete Reference", Third Edition, Tata McGraw Hill, 2013.
9. David Flanagan, "JavaScript: The Definitive Guide, Sixth Edition", O'Reilly Media, 2011
10. Steven Holzner, "The Complete Reference – PHP", Tata McGraw Hill, 2008
11. Mike Mcgrath, "PHP & MySQL in easy Steps", Tata McGraw Hill, 2012.
12. J. Millman and A. Grabel, "Head First HTML and CSS", 2nd edition, O" Reilly..
13. Ben Frain, "Responsive Web design with HTML5 and CSS3", PACKT Publishing Ltd.
14. L. Welling and L. Thomson, "PHP and MySQL Web Development", 4th edition, Adison Wesley Professional.

Digital Material:

1. www.w3schools.com

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8015	RF Design	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 8015	RF Design	20	20	20	80	03	--	--	100

Course Pre-requisite:

1. Electromagnetic and Antenna
2. Principles of Communication Engineering
3. Microwave Engineering

Course Objectives: The course should enable the students to:

1. To learn RF circuit fundamentals for designing various circuit building blocks in a typical RF transceiver
2. To learn importance of EMI/EMC

Course Outcomes:

1. Design impedance matching networks and passive RF filters
2. Design and appraise the RF amplifiers
3. Design and appraise the RF oscillators
4. Differentiate the RF mixers
5. Analyze EMI and EMC in RF circuits
6. Analyze stability of RF trans-receiver

Module No.	Unit No.	Topics	Hrs.
1.0		**RF Filter Design**	**08**
	1.1	Introduction to Periodic Structure	
	1.2	Filter design using Image parameter method (Theory and Numerical)	
	1.3	**Filter design using Insertion loss method-** Maximally flat low pass prototype, Equal ripple low pass prototype, Filter transformation and filter implementation. (Theory and Numerical)	
2.0		**Microwave Amplifier Design**	**10**
	2.1	Two-port power gain derivation, signal flow graph(SFG) and stability criterion (Theory and Numerical)	
	2.2	**Single stage amplifier design**: Design for maximum gain, design for specified gain, low noise amplifier design (Theory and Numerical)	
	2.3	**Power amplifier design**: Characteristics of power amplifier and classes of amplifiers, design of class A power amplifier. (Theory and Numerical)	
3.0		**Microwave Oscillator**	**06**
	3.1	One-port microwave oscillator design. (Theory and Numerical)	
	3.2	One-port microwave oscillator design. (Theory and Numerical)	
	3.3	Analysis of phase noise in oscillators	
4.0		**Microwave Mixer**	**05**
	4.1	**Mixers:** Characteristics, Various types of Mixers: Single ended diode mixers, FET mixers, Balanced mixers, Image reject mixers and other types of mixers	
5.0		**Electromagnetic Interference in RF circuits**	**04**
	5.1	**Introduction**: Natural and Nuclear Sources of EMI, EMI From Apparatus and Circuits. Quantification of Communication System EMI	
	5.2	**Elements of Interference** Including Antennas, Transmitters, Receivers and Propagation. Electronic Equipment And System EMI Concepts. Examples Of EMI Coupling Modes	
	5.3	**Mode of coupling**: Common-Mode coupling , Differential mode coupling, and other coupling mechanisms (Power supply and victim amplifiers)	
6.0		**Electromagnetic Compatibility**	**06**
	6.1	**For Achieving EMC**: Grounding, Bonding, Shielding Effectiveness, EMI Diagnostics And Fixes: Techniques Used In EMI Diagnostics Fixes, troubleshooting.	
	6.2	**Instruments, Tools, used to measure Electromagnetic Field (Radiated and Conducted Emission)**: voltage and current probe, LISN, CDN, Clamp, Field probes, Spectrum analyzer, Oscilloscope, EMI Receiver	
	6.3	**Electromagnetic Noise specification**: Surge, EFT (Electrical Fast transients), PFMF, Radiated and conducted susceptibility, Voltage and dips interruption, Ring wave, Damped oscillatory wave	
	6.4	**EMC Specifications, Standards And Measurements**: A Discussion of the Genesis of EMC documentation including a historical Summary, The Rationale, and A Review of MIL-Std., FCC And CISPR Requirements	
		Total	**39**

Text Books:

1. David Pozar, “Microwave Engineering”, Wiley Publication (Fourth Edition)
2. Ludwig R. and Bogdanov G., “RF Circuit Design”, Prentice Hall

3. Jack Smith, "Modern Communication circuits", Tata McGraw Hill
4. W. Prasad Kodali, "Engineering Electromagnetic Compatibility: Principles, Measurements, Technologies, and Computer Models", Wiley-IEEE Press (Second Edition)
5. David. A. Weston, "Electromagnetic Compatibility principles and applications", Marcel Dekker (Second Edition)
6. MARK I. MONTROSE EDWARD M. NAKAUCHI, "Testing for EMC compliance: Approaches and Techniques"

Reference books

1. Guillermo Gonzalez, "Microwave Transistor Amplifiers Analysis and Design "Prentice Hall. (Second Edition)
2. M. L. Sisodia, G. S. Raghuvanshi, " Microwave Circuits and Passive Devices", New Edge International Publisher(First Edition)
3. Clayton R. Paul, "Electromagnetic Compatibility", John Wiley & Sons. (Second Edition)

Useful Links:

1. www.nptelvideos.in
2. https://nptel.ac.in/courses/108/106/108106138/
3. https://freevideolectures.com/course/4367/nptel-microwave-theory-techniques

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-1). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. 4.**Total 04 questions** need to be attempted.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8021	Autonomous Vehicles	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Exam Duration (in Hrs.)	Term Work	Practical And Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ECCDLO 8021	Autonomous Vehicles	20	20	20	80	03	--	--	100

Course Pre-requisite:
ECCDLO5015 : Sensor Technology
ECC603 : IPMV
ECC604 : ANNFL
ECCDLO7012 : Deep Learning

Course Objectives:
1. Introduction to Autonomous vehicles/SDC (Self Driving Cars), advantages and challenges in SDC’s.
2. Gain Knowledge about the Sensors in SDC’s.
3. Understand the in- vehicle communication aspects in SDC’s
4. Understand perception and localization in SDC.
5. Get to grips with planning and control in SDC.
6. To know the various applications of SDC’s.

Course Outcome:
After successful completion of the course student will be able to
1. Understand fundamentals of SDC (Self Driving Cars).
2. Compare different types of Sensors in SDC’s.
3. Illustrate different protocols of In - vehicle communication for SDC’s.
4. Identify perception and localization in SDC’s.
5. Analyze planning and control in SDC.
6. Evaluate different applications and algorithms in SDC’s.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction**	**04**
	1.1	Introduction to Autonomous Vehicles /Self Driving cars (SDC), Benefits of SDCs, Challenges in Current Deployment.	
	1.2	Levels of Autonomy	
2.0		**Sensors in Autonomous Vehicles**	**06**
	2.1	Camera (3D and stereo), LiDAR, Sensor Fusion	
	2.2	Passive Perception with Sonar and Millimeter Wave Radar	
	2.3	Vehicle-to-Everything Infrastructure	
3.0		**In-Vehicle Communication Systems**	**08**
	3.1	CAN: Introduction and architecture	
	3.2	CANopen: Introduction and architecture	
	3.3	FlexRay protocol: Introduction and architecture	
	3.4	Introduction to Operating System for SDC's.	
4.0		**Perception and Localization in SDC**	**09**
	4.1	Introduction to Computer vision in SDC. Artificial eyes VS human eyes. Four pillars of autonomous driving: Perception, Localization, Planning and Control.	
	4.2	Perception: Object Detection and Line Lane detection Object/ obstacle Detection: Comparison of 2D and 3D object detection. Overview of ML algorithms for obstacle detection-Histogram of Oriented Gradients (HOG), Support Vector Machine (SVM). Object detection using deep learning algorithm: Architecture of YOLO	
	4.3	Line Lane Detection: Introduction to Semantic Segmentation, architecture, overview of different semantic segmentation architecture.	
	4.4	Localization: Introduction to GNSS, GNSS error analysis, Visual Odometry, SLAM Self-Learning: Implementation of YOLO for object Detection, Implementation of semantic segmentation for images.	
5.0		**Planning and Control in SDC**	**06**
	5.1	Planning and Control: Architecture of planning and control, Traffic Prediction and routing.	
	5.2	Behavioral decision, Motion Planning and feedback control.	
6.0		**Applications of SDC**	**06**
	6.1	DragonFly Model: Sensor Configuration and Software Architecture	
	6.2	Enabling Commercial Autonomous Space Robotic Explorers: Sensor configuration and its working.	
	6.3	Algorithm for YOLO object detection: Detecting objects in images and Detecting objects in videos	
		Total	**39**

Textbooks:

1. Sumit Ranjan, Dr. S. Senthamilarasu - Applied Deep Learning and Computer Vision for Self-Driving Cars , Packt Publishing Ltd. 2020.
2. Shaoshan Liu, Liyun Li , Jie Tang, Shuang Wu, Jean-Luc Gaudiot - Creating Autonomous Vehicle System , Second Edition , Morgan & Claypool Publishers , 2018.
3. William Ribbens - Understanding- Automotive-Electronics , Butterworth-Heinemann Publisher , Fifth Edition, 1998.

Reference Books:

1. Markus Maurer ,J. Christian Gerdes, Barbara Lenz , Hermann Winner - Autonomous Driving Technical, Legal, Social Aspects , Springer Open, 2015.
2. Shaoshan Liu - Engineering Autonomous Vehicles and Robots, Wiley, 2020.

E-Resources:

1. Open Innovation in EVs: A case study of Tesla Motors
   https://www.diva-portal.org/smash/get/diva2:635929/FULLTEXT01.pdf

2. Autonomous vehicles Research report by MRCagney
   https://drive.google.com/drive/folders/1nxROagqwDKUpVMDLFPPgG7_DKakuyItf

3. Reinventing Safety: A Joint Approach to Automated Driving Systems Mercedes-Benz and Bosch
   https://www.daimler.com/documents/innovation/other/vssa-mercedes-benz-and-bosch.pdf

Online Courses

1. https://digitaldefynd.com/best-self-driving-cars-courses/
2. https://www.classcentral.com/course/intro-self-driving-cars-13140

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8022	Satellite and Nano Satellite Communication	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test1 and Test 2					
ECCDLO 8022	Satellite and Nano Satellite Communication	20	20	20	80	--	--	--	100

Prerequisites:

- Analog Communication
- Digital Communication

Course objectives:

- To understand the basics of satellite communications and different satellite orbits
- Provide an in-depth understanding of satellite communication system operation, launching techniques, and earth station technology
- To Analyze and evaluate satellite link design
- To review structure design, payload and space segment related to Nano satellite.

Course outcomes:

After successful completion of the course student will be able to

- Understand the basic concepts of satellite communication system and orbital parameters.
- Explain various satellite sub-systems, earth station technologies and launching mechanisms.
- Analyze and evaluate link budget and various performance parameters of satellite signal for proper communication.
- Understand Nano satellite's structure design, payloads, Thermal control system and space segment

Module No.	Unit No.	Topics	Hrs.
1.0		**Overview of Satellite Systems, Orbits and Launching**	**08**
	1.1	An overview of space and classification of satellite, orbital elements: apogee and perigee heights, semi-major axis, eccentricity, and mean anomaly, argument of perigee, inclination and right ascension of the ascending node, polar orbiting satellites, Kepler's first, second and third law, Orbital perturbations, effects of a non-spherical earth, atmospheric drag.	
	1.2	Sub-satellite point, predicting satellite position, antenna look angels, polar mount antenna, limits of visibility, near geostationary orbits, earth eclipse of satellite, sun transit outage.	
	1.3	Selection of launching site, launch window, zero and non-zero degree latitude launching, sea launch, Launchers: Polar Satellite Launch Vehicle (PSLV), Geostationary Satellite Launch Vehicle (GSLV), Reusable launch vehicles (RLV),Sounding rockets, Fuels used for launch Vehicles.	
2.0		**Space Segment**	**04**
	2.1	Satellite configuration, transponder sub-system, antenna sub- system, AOC sub-system, TT&C sub-system, power sub-system,thermal sub-system.	
	2.2	Reliability and quality assurance.	
3.0		**Earth Station**	**03**
	3.1	Design consideration.	
	3.2	General configuration: block diagram, receive only type earth, transmit-receive type earth station, antenna system, feed system, tracking system, LNA and HPA.	
4.0		**Satellite Losses and Links**	**10**
	4.1	Atmospheric losses, ionospheric losses, transmission losses, feeder losses, antenna misalignment losses, rain attenuation, other impairments, antenna polarization, polarization of satellite signals, cross polarization discrimination, ionospheric depolarization, rain depolarization and ice depolarization, Isotropic radiated power and link budget.	
	4.2	System noise, antenna noise, amplifier noise temperature, amplifiers in cascade, noise factor, noise temperature of absorptive networks, overall system noise temperature and carrier to noise ratio.	
	4.3	Saturation flux density, input back off, earth station HPA, output back off and satellite TWTA output.	
	4.4	Effects of rain, uplink rain-fade margin, downlink rain-fade margin, combined uplink and downlink C/N ratio and intermodulation noise.	
5.		**Overview of nano satellite**	**06**
	5.1	Introduction: Important transformation, Review of nano satellite, Global economics related to it, Evolution of nano satellite, ISRO's small satellite program and future scenario.	
	5.2	Payloads for nano satellite: Types of payloads: Earth observation payload, communication payload and scientific payload. Design considerations for payloads.	
	5.3	Nanosatellite structures: Function of satellite structure, Types of structure designs: Skin frame structure, Truss structure, Monocoque cylinders and Skin stringer structure, Overview for building of structure and materials for structures.	

6.		**Space segment for nano satellite**	**08**
	6.1	Thermal control system (TCS) implementation in nano satellite and it's testing for verification of TCS. Power system design for nano satellite.	
	6.2	Function and design consideration of Deployment mechanisms, Critical elements in deployment mechanisms, Overview of types of deployment mechanisms.	
	6.3	On board Computer and digital electronics (OBC): Block diagram of typical OBC, Overview of OBC Software and hardware, Telemetry and telecommand, Attitude control electronics	
	6.4	Quality, Quality assurance, product assurance and reliability analysis for Nano satellite	
		Total	**39**

Text Books & References:

1. Dennis Roddy, –Satellite Communications‖, 4th Ed., Mc. Graw-Hill International Ed. 2009.
2. M. Richharia, –Satellite Communication Systems Design Principles‖, Macmillan Press Ltd. Second Edition 2003.
3. R. N. Mutangi, — Satellite Communication‖, Oxford university press, 2016.
4. Gerard Maral and Michel Bousquet, –Satellite Communication Systems‖, 4th Edition Wiley Publication
5. Gerard Maral, —VSAT Networks‖, John Willy & Sons
6. Space technology Veterans, Quintessence of Nano satellite technology (small is big), Planet aerospace India,2020.
7. Timothy Pratt, Charles Bostian, and Jeremy Allmuti, –Satellite Communications‖, John Willy & Sons (Asia) Pvt. Ltd. 2004
8. Wilbur L. Pritchard, Henri G. Suyderehoud, and Robert A. Nelson, –Satellite Communication systems Engineering‖, Pearson Publication

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8023	Network Management in Telecommunication	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 8023	Network Management in Telecommunication	20	20	20	80	03	--	--	100

Course pre-requisite:

ECC602- Computer Communication Networks

Course Objectives:

1. To understand the concepts of network management in Telecommunication (NMT), architectures and protocols.
2. To familiarize the student with the design, analysis, operation and management of modern data communications networks.
3. To provide the student with a working knowledge of the types of communication network management systems and their strengths and limitations in solving various information network management problems.

Course Outcome:

After successful completion of the course, the student will be able to:-

1. Explain the need for interoperable network management and analyze the trends and development of the Telecommunications Network Management.
2. Demonstrate broad knowledge of fundamental principles and technical standards underlying NMT.
3. Describe the concepts and architecture behind standards-based network management associated with SNMP and CMIP.
4. Apply basics of telecommunication, networking and information technologies and architect and implement networked informative systems.
5. Continuously improve their knowledge of technology and communication skills.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction of Network Management**	6
	1.1	Introducing Network Design Concepts: Case histories on network, system and service management, Network design based on economy and SLA-based services. Challenges of IT managers	
	1.2	Network Management: Goals, organization and functions	
	1.3	Network management architecture, organization network and management perspectives	
2.0		**OSI Network Management**	6
	2.1	Network Management standards	
	2.2	OSI Network Management model	
	2.3	Network Management layers	
	2.4	ISO Network Management functions	
	2.5	Communication model and functional model	
	2.6	Abstract Syntax Notation One (ASN.1): Terminology, symbols, and conventions. TLV encoding structure	
3.0		**Internet Management**	10
	3.1	**SNMP model**: SNMP Organizational model, System overview, Information model, Management of Information Base	
	3.2	**SNMP v1**: SNMP Communication model- SNMP architecture, Administrative model, SNMP Protocol specifications, SNMP operations, SNMP Functional model	
	3.3	**SNMPv2**: Major changes in SNMPv2, SNMPv2 architecture, SNMPv2 Management Information Base, SNMPv2 protocol, Compatibility with SNMPv1	
	3.4	**SNMPv3**:Key features, SNMPv3 architecture, SNMPv3 applications, Security, security model, message format, SNMPv3 User- based Security Model, Access control (VACM)	
	3.5	**RMON:** What is RMON? RMON 1, RMON 2	
4.0		**Telecommunication Management Networks(TMN)**	4
	4.1	Definition of TMN , TMN framework, TMN functional model	
	4.2	TMN Conceptual model, OSI functionality in TMN	
	4.3	TMN management services architecture and TMN implementation	
5.0		**Network Management Tools and Applications**	9
	5.1	System Utilities for Network Management: Basic tools, SNMP tools and Protocol analyzer	
	5.2	Network Statistics and Measurements: Traffic load, Protocol statistics, Data and Error statistics	
	5.3	NMS Design: Functional requirements, NMS Client design and NMS Server architecture, Distributed Management approaches	
	5.4	Network Management Systems: Commercial and Open-source NMSs	
	5.5	Network Management Applications: Fault, Configuration, Accounting, Performance and Security (FCAPS)	
	5.6	Event Correlation Techniques: Rule-based reasoning, Model-based reasoning, Case-based reasoning, Codebook, State Transition Graph model and Finite State Machine model	

	5.7	Report Management, Policy-based Management and Service Level Management	
6.0		**Broadband Network Management**	**4**
	6.1	Broadband networks and services, ATM Technology – VP, VC, ATM Packet, Integrated service, ATM LAN emulation, Virtual LAN	
	6.2	ATM Network Management – ATM network reference model, Integrated Local Management Interface, role of SNMP and ILMI in ATM	
	6.3	ATM Management Information Base, M1, M2, M3, M4 interfaces	
		Total	**39**

Text books

1. Mani Subramaniam, *Network Management Principles and Practice*, New Delhi: Pearson, 2010.
2. Alexander Clemm, *Network Management Fundamentals*, Cisco Press, December 2006, ISBN-13: 978-158720137.
3. Benoit Claise and Ralf Wolter, *Network Management: Accounting and Performance Strategies*, CISCO Press, 2007.
4. J. Richard Burke, *Network Management: Concepts and Practice, A Hands-On Approach*,Pearson Education India, 2008, ISBN-13: 978-8131718490.
5. Salh Aiidarons, Thomas Plevoyak, *Telecommunications Network Technologies and Implementations*, Eastern Economy Edition, New Delhi:IEEE Press, 1998.
6. Henry Haojin Wang, *Telecommunication Network Management*, McGraw Hill, 1999.

Online Learning Resources:-

1. https://www.youtube.com/watch?v=liBB_Q7Go5k
2. https://www.youtube.com/watch?v=xdUjwlyyi9U
3. https://www.youtube.com/watch?v=aQGeSDauRso
4. https://nptel.ac.in/courses/117/101/117101050/
5. https://nptel.ac.in/courses/106/105/106105183/

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8024	Microstrip Antenna	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 8024	Microstrip Antenna	20	20	20	80	03	--	--	100

Course Prerequisite:

1. **Electromagnetics and Antenna**
2. **Microwave Engineering**

Course Objectives:

In the course, Students will be introduced to :

1. Fundamental parameters and characteristics of Microstrip Antennas(MSA)
2. Design and analysis of Rectangular and Circular MSA.
3. Different compact and broadband techniques of MSA.
4. Circularly Polarized MSA's and various applications in wireless communication systems.

Course Outcomes:

After successful completion of the course student will be able to:

1. Apply the fundamental parameters of MSA.
2. Analyze Rectangular and Circular MSA.
3. Identify various compact and broadband methods of MSA.
4. Examine the methods of circular polarization.
5. Compare various applications of MSA.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Microstrip Antennas**	**05**
	1.1	Types of MSA's, Characteristics of MSA's, Advantages and Disadvantages, Applications of MSA's.	01
	1.2	Reflection coefficient, VSWR, Return loss, Impedance mismatch, VSWR Bandwidth, Gain, Directivity, Antenna efficiency, E-Plane and H-Plane radiation pattern, Co and Cross polarization, Specific absorption rate (SAR), Axial ratio.	02
	1.3	Feeding Techniques, brief introduction to Methods of Analysis, Surface waves, Various substrates with dielectric constant for MSA.	02
2.0		**Design of Rectangular Microstrip Antenna (RMSA)**	**08**
	2.1	Design considerations of RMSA: Resonant frequency, Voltage and Current variation, Radiation Pattern, Calculation of effective dielectric constant, actual and effective length, width, feed point location.	04
	2.2	Parametric Study of RMSAs : Effect of - feed point location, width of RMSA(W), height of substrate(h), dielectric constant(Ɛr), probe diameter, finite ground plane, loss tangent.	03
	2.3	Analysis of Higher order modes of RMSA	01
3.0		**Design of Circular Microstrip Antenna (CMSA)**	**08**
	3.1	Design considerations of CMSA: Resonant frequency, Input Impedance and Voltage Distribution, Radiation Pattern, Calculation of effective dielectric constant, actual and effective radius, feed point location.	06
	3.2	Parametric Study of CMSAs : Effect of loss tangent	01
	3.3	Analysis of Higher order modes of CMSA	01
4.0		**Compact and Broadband Techniques**	**08**
	4.1	Compact Techniques for RMSA and CMSA:Introduction, Compact Shorted RMSA, Partially Shorted RMSA, Effect of Dimensions of RMSA with a Single Shorting Post, Effect of the Position of the Single Shorting Post.	04
	4.2	Broadband Techniques for RMSA and CMSA: Planar Multiresonator configurations (Radiating and Non-radiating Gap coupled concept), Electromagnetically coupled MSA's, Stacked Multiresonator Rectangular Patches on Thick Substrates, U slot technique.	04
5.0		**Circularly Polarized MSAs**	**05**
	5.1	Methods to achieve Circular Polarization in MSA : single feed and dual feed.	03
	5.2	Design procedure for single feed circularly polarized MSA (RMSA & CMSA).	02
6.0		**Applications of MSA**	**05**
	6.1	Introduction: Wearable and Fractal Antennas for wireless communication systems, MIMO Patch Antenna, Reconfigurable Antenna, Implanted Antennas in the medical field.	05
		Total	**39**

Text Books:

1. Girish Kumar, K. P. Ray, Broadband Microstrip Antennas, Artech House, 2003.
2. Constantine A. Balanis, Antenna Theory: Analysis and Design, John Wiley Publication 4th Edition.
3. Sabban, Albert. Wearable Communication Systems and Antennas for Commercial, Sport and Medical Applications. IOP Publishing 2018. https://dx.doi.org/10.1088/2053-2563/aade55
4. Abed, M. J. Abu-AlShaer, and A. M. Jawad, "Fractal Antennas for Wireless Communications", in Modern Printed-Circuit Antennas. London, United Kingdom: IntechOpen, 2020 [Online]. Available: https://www.intechopen.com/chapters/71491 doi: 10.5772/intechopen.90332
5. Sharawi Mohammad S., Printed MIMO antenna engineering, Artech House Publishers, 2014.
6. J. T. Bernhard, Reconfigurable Antennas. San Rafael, CA, USA: Morgan and Claypool Publishers, 2007.

7. Rahmat-Samii, Yahya, Kim, Jaehoon, Implanted Antennas in Medical Wireless Communications. United States: Morgan & Claypool Publishers, 2006. https://doi.org/10.2200/S00024ED1V01Y200605ANT001

Reference books

1. Ramesh Garg, Prakash Bhartia, Inder J. Bahl, A. Ittipiboon, Microstrip Antenna Design Handbook, Artech House, 2001.
2. Kin-Lu Wong, Compact and Broadband Microstrip Antennas, John Wiley & Sons Inc, United States, 2002.
3. Kai Fong Lee, Kwai Man Luk, Hau Wah Lai, Microstrip Patch Antennas, World Scientific; 2nd edition.

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End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to the number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of a total **of 06 questions**, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on the entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8025	Augmented and Virtual Reality	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ECCDLO 8025	Augmented and Virtual Reality	20	20	20	80	03	--	--	100

Prerequisite: Programming Language, Computer Graphics

Course Objectives: The course aims:

1. To learn the underlying concepts of Augmented and Virtual Reality and primitives of computer graphics.
2. To understand the use of hardware devices in AR-VR system.
3. To understand the tracking system in AR.
4. To apply concept of calibration and registration of different components in AR system
5. To design AR-VR applications.
6. To understand the use of AR-VR in interdisciplinary immersive applications

Course Outcomes: On successful completion of this course, learner /student will be able to:

1. Identify and compare different Virtual and Augmented Reality Technologies and apply modelling techniques.
2. Identify and use AR-VR hardware components.
3. Apply concepts of Computer Vision for tracking in AR Systems.
4. Apply calibration techniques and registration for components in AR.
5. Design AR-VR application
6. Apply insights of AR-VR in different applications.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Augmented and Virtual Reality**	**05**
		Definition and Scope, A Brief History of Augmented and Virtual Reality, AR-VR Architecture, Challenges with AR-VR, AR-VR systems and functionality, Types of Augmented Reality Application (Location Based AR Apps Marker-Based AR Applications). Understanding Virtual Space and Geometry: coordinate systems, Geometric Modelling, 2D transformations, 3D rotation and 6 degree of freedom, View Transformation, projective transformation, Related fields: MR, XR and ubiquitous computing and their comparison.	
2.0		**Visual Physiology, perception and Interaction**	**05**
		Mechanics of Sight: the visual pathway, spatial vision and depth cues. Display fundamentals, optical architecture. Augmenting displays. Multimodal Displays; Visual Perception; Spatial Display Model; Visual Displays. Mechanics of hearing, audio displays. Augmented and Virtual reality Hardware	
3.0		**Tracking and Computer Vision for AR**	**10**
		Characteristics of Tracking Technology; Stationary Tracking Systems; Mobile Sensors; Optical Tracking; Sensor Fusion; Marker Tracking, infrared tracking, Natural feature tracking by detection.	
4.0		**Calibrations and Registration**	**06**
		Camera projection and setup for AR. Camera calibration techniques. Registration	
5.0		**AR-VR Application Development**	**07**
		AR-VR Application Requirements, Software engineering requirements, AR-VR Design Principles, Data Flow, Scene Graphs; Developer Support: Parameter Configuration, Tools used in AR-VR development.	
6.0		**Applications of AR-VR and Human Factors, Legal and Social Considerations**	**06**
		Applications of AR-VR in: Edutainment, Medical, Military, Production and Manufacturing, Navigation, Astronomical Observation, E-commerce; What are Human Factors, Physical Side Effects, Visual Side Effects, Legal Considerations, Moral and Ethical Considerations.	
		Total	**39**

Textbooks:

1. John Vince, “Virtual Reality Systems”, Pearson publication
2. Tony Parisi, “Learning Virtual Reality”, O’REILLY’
3. Dieter Schmalsteig and Tobias Hollerer, “Augmented Reality- Principles and Practice”, Pearson Education, Inc. 2016 Edition.
4. Chetankumar G Shetty, “Augmented Reality- Theory, Design and Development”, Mc Graw Hill, 2020 Edition.
5. Alan B. Craig, “Understanding Augmented Reality – Concepts and Applications”, Morgan Kaufmann, Elsevier, 2013 Edition.

References:

1. Borko Furht, “Handbook of Augmented Reality”, Springer.
2. Erin Pangilinan, Steve Lukas, and Vasanth Mohan, “Creating Augmented and Virtual Realities- Theory and Practice for Next-Generation Spatial Computing”, O’Reilly Media, Inc., 2019 Edition.
3. Jens Grubert, Dr. Raphael Grasset, “Augmented Reality for Android Application Development”, PACKT Publishing.

Online References:

1. www.nptel.ac.in
2. www.coursera.org

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-I). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. **Total 04 questions** need to be attempted.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 8011	Project Management	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 8011	Project Management	20	20	20	80	03	--	--	100

Objectives:

1. To familiarize the students with the use of a structured methodology/approach for each and every unique project undertaken, including utilizing project management concepts, tools and techniques.
2. To appraise the students with the project management life cycle and make them knowledgeable about the various phases from project initiation through closure.

Outcomes: Learner will be able to…

1. Apply selection criteria and select an appropriate project from different options.
2. Write work break down structure for a project and develop a schedule based on it.
3. Identify opportunities and threats to the project and decide an approach to deal with them strategically.
4. Use Earned value technique and determine & predict status of the project.
5. Capture lessons learned during project phases and document them for future reference

Module	Detailed Contents	Hrs
01	**Project Management Foundation:** Definition of a project, Project Vs Operations, Necessity of project management, Triple constraints, Project life cycles (typical & atypical) Project phases and stage gate process. Role of project manager. Negotiations and resolving conflicts. Project management in various organization structures. PM knowledge areas as per Project Management Institute (PMI).	5
02	**Initiating Projects:** How to get a project started, Selecting project strategically, Project selection models (Numeric /Scoring Models and Non-numeric models), Project portfolio process, Project sponsor and creating charter; Project proposal. Effective project team, Stages of team development & growth (forming, storming, norming & performing), team dynamics.	6
03	**Project Planning and Scheduling:** Work Breakdown structure (WBS) and linear responsibility chart, Interface Co-ordination and concurrent engineering, Project cost estimation and budgeting, Top down and bottoms up budgeting, Networking and Scheduling techniques. PERT, CPM, GANTT chart. Introduction to Project Management Information System (PMIS).	8
04	**Planning Projects:**	6

	Crashing project time, Resource loading and leveling, Goldratt's critical chain, Project Stakeholders and Communication plan. Risk Management in projects: Risk management planning, Risk identification and risk register. Qualitative and quantitative risk assessment, Probability and impact matrix. Risk response strategies for positive and negative risks	
05	**5.1 Executing Projects:** Planning monitoring and controlling cycle. Information needs and reporting, engaging with all stakeholders of the projects. Team management, communication and project meetings. **5.2 Monitoring and Controlling Projects:** Earned Value Management techniques for measuring value of work completed; Using milestones for measurement; change requests and scope creep. Project audit. **5.3 Project Contracting** Project procurement management, contracting and outsourcing,	8
06	**6.1 Project Leadership and Ethics:** Introduction to project leadership, ethics in projects. Multicultural and virtual projects. **6.2 Closing the Project:** Customer acceptance; Reasons of project termination, Various types of project terminations (Extinction, Addition, Integration, Starvation), Process of project termination, completing a final report; doing a lessons learned analysis; acknowledging successes and failures; Project management templates and other resources; Managing without authority; Areas of further study.	6
	Total	39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Jack Meredith & Samuel Mantel, Project Management: A managerial approach, Wiley India, 7th Ed.
2. A Guide to the Project Management Body of Knowledge (PMBOK® Guide), 5th Ed, Project Management Institute PA, USA
3. Gido Clements, Project Management, Cengage Learning.
4. Gopalan, Project Management, , Wiley India
1. Dennis Lock, Project Management, Gower Publishing England, 9 th Ed.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 8012	Finance Management	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 8012	Finance Management	20	20	20	80	03	--	--	100

Objectives:

1. Overview of Indian financial system, instruments and market
2. Basic concepts of value of money, returns and risks, corporate finance, working capital and its management
3. Knowledge about sources of finance, capital structure, dividend policy

Outcomes: Learner will be able to…

1. Understand Indian finance system and corporate finance
2. Take investment, finance as well as dividend decisions

Module	Detailed Contents	Hrs
01	**Overview of Indian Financial System:** Characteristics, Components and Functions of Financial System. **Financial Instruments:** Meaning, Characteristics and Classification of Basic Financial Instruments — Equity Shares, Preference Shares, Bonds-Debentures, Certificates of Deposit, and Treasury Bills. **Financial Markets:** Meaning, Characteristics and Classification of Financial Markets — Capital Market, Money Market and Foreign Currency Market **Financial Institutions:** Meaning, Characteristics and Classification of Financial Institutions — Commercial Banks, Investment-Merchant Banks and Stock Exchanges	06
02	**Concepts of Returns and Risks:** Measurement of Historical Returns and Expected Returns of a Single Security and a Two-security Portfolio; Measurement of Historical Risk and Expected Risk of a Single Security and a Two-security Portfolio. **Time Value of Money:** Future Value of a Lump Sum, Ordinary Annuity, and Annuity Due; Present Value of a Lump Sum, Ordinary Annuity, and Annuity Due; Continuous Compounding and Continuous Discounting.	06
03	**Overview of Corporate Finance:** Objectives of Corporate Finance; Functions of Corporate Finance—Investment Decision, Financing Decision, and Dividend Decision. **Financial Ratio Analysis:** Overview of Financial Statements—Balance Sheet, Profit and Loss Account, and Cash Flow Statement; Purpose of Financial Ratio Analysis; Liquidity	09

	Ratios; Efficiency or Activity Ratios; Profitability Ratios; Capital Structure Ratios; Stock Market Ratios; Limitations of Ratio Analysis.	
04	**Capital Budgeting:** Meaning and Importance of Capital Budgeting; Inputs for Capital Budgeting Decisions; Investment Appraisal Criterion—Accounting Rate of Return, Payback Period, Discounted Payback Period, Net Present Value(NPV), Profitability Index, Internal Rate of Return (IRR), and Modified Internal Rate of Return (MIRR) **Working Capital Management:** Concepts of Meaning Working Capital; Importance of Working Capital Management; Factors Affecting an Entity's Working Capital Needs; Estimation of Working Capital Requirements; Management of Inventories; Management of Receivables; and Management of Cash and Marketable Securities.	10
05	**Sources of Finance:** Long Term Sources—Equity, Debt, and Hybrids; Mezzanine Finance; Sources of Short Term Finance—Trade Credit, Bank Finance, Commercial Paper; Project Finance. **Capital Structure:** Factors Affecting an Entity's Capital Structure; Overview of Capital Structure Theories and Approaches— Net Income Approach, Net Operating Income Approach; Traditional Approach, and Modigliani-Miller Approach. Relation between Capital Structure and Corporate Value; Concept of Optimal Capital Structure	05
06	**Dividend Policy:** Meaning and Importance of Dividend Policy; Factors Affecting an Entity's Dividend Decision; Overview of Dividend Policy Theories and Approaches—Gordon's Approach, Walter's Approach, and Modigliani-Miller Approach	03
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Fundamentals of Financial Management, 13th Edition (2015) by Eugene F. Brigham and Joel F. Houston; Publisher: Cengage Publications, New Delhi.
2. Analysis for Financial Management, 10th Edition (2013) by Robert C. Higgins; Publishers: McGraw Hill Education, New Delhi.
3. Indian Financial System, 9th Edition (2015) by M. Y. Khan; Publisher: McGraw Hill Education, New Delhi.
4. Financial Management, 11th Edition (2015) by I. M. Pandey; Publisher: S. Chand (G/L) & Company Limited, New Delhi.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 8013	Entrepreneurship Development and Management	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 8013	Entrepreneurship Development and Management	20	20	20	80	03	--	--	100

Objectives:

1. To acquaint with entrepreneurship and management of business
2. Understand Indian environment for entrepreneurship
3. Idea of EDP, MSME

Outcomes: Learner will be able to…

1. Understand the concept of business plan and ownerships
2. Interpret key regulations and legal aspects of entrepreneurship in India
3. Understand government policies for entrepreneurs

Module	Detailed Contents	Hrs
01	**Overview Of Entrepreneurship:** Definitions, Roles and Functions/Values of Entrepreneurship, History of Entrepreneurship Development, Role of Entrepreneurship in the National Economy, Functions of an Entrepreneur, Entrepreneurship and Forms of Business Ownership Role of Money and Capital Markets in Entrepreneurial Development: Contribution of Government Agencies in Sourcing information for Entrepreneurship	04
02	**Business Plans And Importance Of Capital To Entrepreneurship:** Preliminary and Marketing Plans, Management and Personnel, Start-up Costs and Financing as well as Projected Financial Statements, Legal Section, Insurance, Suppliers and Risks, Assumptions and Conclusion, Capital and its Importance to the Entrepreneur **Entrepreneurship And Business Development:** Starting a New Business, Buying an Existing Business, New Product Development, Business Growth and the Entrepreneur Law and its Relevance to Business Operations	09
03	Women's Entrepreneurship Development, Social entrepreneurship-role and need, EDP cell, role of sustainability and sustainable development for SMEs, case studies, exercises	05
04	**Indian Environment for Entrepreneurship:** key regulations and legal aspects , MSMED Act 2006 and its implications, schemes and policies of the Ministry of MSME, role and responsibilities of various government organisations, departments, banks etc., Role of State governments in terms of infrastructure developments and support etc., Public private partnerships, National Skill development Mission, Credit Guarantee Fund, PMEGP, discussions, group exercises etc	08

05	**Effective Management of Business:** Issues and problems faced by micro and small enterprises and effective management of M and S enterprises (risk management, credit availability, technology innovation, supply chain management, linkage with large industries), exercises, e-Marketing	08
06	**Achieving Success In The Small Business:** Stages of the small business life cycle, four types of firm-level growth strategies, Options – harvesting or closing small business Critical Success factors of small business	05
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Poornima Charantimath, Entrepreneurship development- Small Business Enterprise, Pearson
2. Education Robert D Hisrich, Michael P Peters, Dean A Shapherd, Entrepreneurship, latest edition, The McGrawHill Company
3. Dr TN Chhabra, Entrepreneurship Development, Sun India Publications, New Delhi
4. Dr CN Prasad, Small and Medium Enterprises in Global Perspective, New century Publications, New Delhi
5. Vasant Desai, Entrepreneurial development and management, Himalaya Publishing House
6. Maddhurima Lall, Shikah Sahai, Entrepreneurship, Excel Books
7. Rashmi Bansal, STAY hungry STAY foolish, CIIE, IIM Ahmedabad
8. Law and Practice relating to Micro, Small and Medium enterprises, Taxmann Publication Ltd.
9. Kurakto, Entrepreneurship- Principles and Practices, Thomson Publication
10. Laghu Udyog Samachar
11. www.msme.gov.in
12. www.dcmesme.gov.in
13. www.msmetraining.gov.in

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 8014	Human Resource Management	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 8014	Human Resource Management	20	20	20	80	03	--	--	100

Objectives:

1. To introduce the students with basic concepts, techniques and practices of the human resource management.
2. To provide opportunity of learning Human resource management (HRM) processes, related with the functions, and challenges in the emerging perspective of today's organizations.
3. To familiarize the students about the latest developments, trends & different aspects of HRM.
4. To acquaint the student with the importance of inter-personal & inter-group behavioral skills in an organizational setting required for future stable engineers, leaders and managers.

Outcomes: Learner will be able to…

1. Understand the concepts, aspects, techniques and practices of the human resource management.
2. Understand the Human resource management (HRM) processes, functions, changes and challenges in today's emerging organizational perspective.
3. Gain knowledge about the latest developments and trends in HRM.
4. Apply the knowledge of behavioral skills learnt and integrate it with in inter personal and intergroup environment emerging as future stable engineers and managers.

Module	Detailed Contents	Hrs
01	**Introduction to HR** • Human Resource Management- Concept, Scope and Importance, Interdisciplinary Approach Relationship with other Sciences, Competencies of HR Manager, HRM functions. • Human resource development (HRD): changing role of HRM – Human resource Planning, Technological change, Restructuring and rightsizing, Empowerment, TQM, Managing ethical issues.	5
02	**Organizational Behavior (OB)** • Introduction to OB Origin, Nature and Scope of Organizational Behavior, Relevance to Organizational Effectiveness and Contemporary issues • Personality: Meaning and Determinants of Personality, Personality development, Personality Types, Assessment of Personality Traits for Increasing Self Awareness	7

	• Perception: Attitude and Value, Effect of perception on Individual Decision-making, Attitude and Behavior. • Motivation: Theories of Motivation and their Applications for Behavioral Change (Maslow, Herzberg, McGregor); • Group Behavior and Group Dynamics: Work groups formal and informal groups and stages of group development. Team Effectiveness: High performing teams, Team Roles, cross functional and self-directed team. • Case study	
03	**Organizational Structure &Design** • Structure, size, technology, Environment of organization; Organizational Roles & conflicts: Concept of roles; role dynamics; role conflicts and stress. • Leadership: Concepts and skills of leadership, Leadership and managerial roles, Leadership styles and contemporary issues in leadership. • Power and Politics: Sources and uses of power; Politics at workplace, Tactics and strategies.	6
04	**Human resource Planning** • Recruitment and Selection process, Job-enrichment, Empowerment - Job-Satisfaction, employee morale. • Performance Appraisal Systems: Traditional & modern methods, Performance Counseling, Career Planning. • Training & Development: Identification of Training Needs, Training Methods	5
05	**Emerging Trends in HR** • Organizational development; Business Process Re-engineering (BPR), BPR as a tool for organizational development , managing processes & transformation in HR. Organizational Change, Culture, Environment • Cross Cultural Leadership and Decision Making**:** Cross Cultural Communication and diversity at work**,** Causes of diversity, managing diversity with special reference to handicapped, women and ageing people, intra company cultural difference in employee motivation.	6
06	**HR & MIS** Need, purpose, objective and role of information system in HR, Applications in HRD in various industries (e.g. manufacturing R&D, Public Transport, Hospitals, Hotels and service industries **Strategic HRM** Role of Strategic HRM in the modern business world, Concept of Strategy, Strategic Management Process, Approaches to Strategic Decision Making; Strategic Intent – Corporate Mission, Vision, Objectives and Goals **Labor Laws & Industrial Relations** Evolution of IR, IR issues in organizations, Overview of Labor Laws in India; Industrial Disputes Act, Trade Unions Act, Shops and Establishments Act	10
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Stephen Robbins, Organizational Behavior, 16th Ed, 2013
2. V S P Rao, Human Resource Management, 3rd Ed, 2010, Excel publishing
3. Aswathapa, Human resource management: Text & cases, 6th edition, 2011
4. C. B. Mamoria and S V Gankar, Dynamics of Industrial Relations in India, 15th Ed, 2015, Himalaya Publishing, 15th edition, 2015
5. P. Subba Rao, Essentials of Human Resource management and Industrial relations, 5th Ed, 2013, Himalaya Publishing
6. Laurie Mullins, Management & Organizational Behavior, Latest Ed, 2016, Pearson Publications

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 8015	Professional Ethics and CSR	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 8015	Professional Ethics and CSR	20	20	20	80	03	--	--	100

Objectives:

1. To understand professional ethics in business
2. To recognized corporate social responsibility

Outcomes: Learner will be able to…

1. Understand rights and duties of business
2. Distinguish different aspects of corporate social responsibility
3. Demonstrate professional ethics
4. Understand legal aspects of corporate social responsibility

Module	Detailed Contents	Hrs
01	**Professional Ethics and Business:** The Nature of Business Ethics; Ethical Issues in Business; Moral Responsibility and Blame; Utilitarianism: Weighing Social Costs and Benefits; Rights and Duties of Business	04
02	**Professional Ethics in the Marketplace:** Perfect Competition; Monopoly Competition; Oligopolistic Competition; Oligopolies and Public Policy **Professional Ethics and the Environment:** Dimensions of Pollution and Resource Depletion; Ethics of Pollution Control; Ethics of Conserving Depletable Resources	08
03	**Professional Ethics of Consumer Protection:** Markets and Consumer Protection; Contract View of Business Firm's Duties to Consumers; Due Care Theory; Advertising Ethics; Consumer Privacy **Professional Ethics of Job Discrimination:** Nature of Job Discrimination; Extent of Discrimination; Reservation of Jobs.	06
04	**Introduction to Corporate Social Responsibility:** Potential Business Benefits—Triple bottom line, Human resources, Risk management, Supplier relations; Criticisms and concerns—Nature of business; Motives; Misdirection. Trajectory of Corporate Social Responsibility in India	05
05	**Corporate Social Responsibility:** Articulation of Gandhian Trusteeship Corporate Social Responsibility and Small and Medium Enterprises (SMEs) in India, Corporate Social Responsibility and Public-Private Partnership (PPP) in India	08

06	**Corporate Social Responsibility in Globalizing India:** Corporate Social Responsibility Voluntary Guidelines, 2009 issued by the Ministry of Corporate Affairs, Government of India, Legal Aspects of Corporate Social Responsibility—Companies Act, 2013.	08
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Business Ethics: Texts and Cases from the Indian Perspective (2013) by Ananda Das Gupta; Publisher: Springer.
2. Corporate Social Responsibility: Readings and Cases in a Global Context (2007) by Andrew Crane, Dirk Matten, Laura Spence; Publisher: Routledge.
3. Business Ethics: Concepts and Cases, 7th Edition (2011) by Manuel G. Velasquez; Publisher: Pearson, New Delhi.
4. Corporate Social Responsibility in India (2015) by Bidyut Chakrabarty, Routledge, New Delhi.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 8016	Research Methodology	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 8016	Research Methodology	20	20	20	80	03	--	--	100

Objectives:

1. To understand Research and Research Process
2. To acquaint students with identifying problems for research and develop research strategies
3. To familiarize students with the techniques of data collection, analysis of data and interpretation

Outcomes: Learner will be able to…

1. Prepare a preliminary research design for projects in their subject matter areas
2. Accurately collect, analyze and report data
3. Present complex data or situations clearly
4. Review and analyze research findings

Module	Detailed Contents	Hrs
01	**Introduction and Basic Research Concepts** **1.1** Research – Definition; Concept of Construct, Postulate, Proposition, Thesis, Hypothesis, Law, Principle.Research methods vs Methodology **1.2** Need of Research in Business and Social Sciences **1.3** Objectives of Research **1.4 Issues** and Problems in Research **1.5** Characteristics of Research: Systematic, Valid, Verifiable, Empirical and Critical	**09**
02	**Types of Research** **2.1**. Basic Research **2.2**. Applied Research **2.3.** Descriptive Research **2.4.** Analytical Research **2.5**. Empirical Research **2.6** Qualitative and Quantitative Approaches	**07**
03	**Research Design and Sample Design** **3.1** Research Design – Meaning, Types and Significance **3.2** Sample Design – Meaning and Significance Essentials of a good sampling Stages in Sample Design Sampling methods/techniques Sampling Errors	**07**
04	**Research Methodology** **4.1** Meaning of Research Methodology **4.2**. Stages in Scientific Research Process: **a.** Identification and Selection of Research Problem	**08**

	b. Formulation of Research Problem **c.** Review of Literature **d.** Formulation of Hypothesis **e.** Formulation of research Design **f**. Sample Design **g.** Data Collection **h.** Data Analysis **i.** Hypothesis testing and Interpretation of Data **j.** Preparation of Research Report	
05	**Formulating Research Problem** **5.1** Considerations: Relevance, Interest, Data Availability, Choice of data, Analysis of data, Generalization and Interpretation of analysis	**04**
06	**Outcome of Research** **6.1** Preparation of the report on conclusion reached **6.2** Validity Testing & Ethical Issues **6.3** Suggestions and Recommendation	**04**
	Total	39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or at least 6 assignment on complete syllabus or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Dawson, Catherine, 2002, Practical Research Methods, New Delhi, UBS Publishers Distributors.
2. Kothari, C.R.,1985, Research Methodology-Methods and Techniques, New Delhi, Wiley Eastern Limited.
3. Kumar, Ranjit, 2005, Research Methodology-A Step-by-Step Guide for Beginners, (2nd ed), Singapore, Pearson Education

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 8017	IPR and Patenting	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 8017	IPR and Patenting	20	20	20	80	03	--	--	100

Objectives:

1. To understand intellectual property rights protection system
2. To promote the knowledge of Intellectual Property Laws of India as well as International treaty procedures
3. To get acquaintance with Patent search and patent filing procedure and applications

Outcomes: Learner will be able to…

1. understand Intellectual Property assets
2. assist individuals and organizations in capacity building
3. work for development, promotion, protection, compliance, and enforcement of Intellectual Property and Patenting

Module	Detailed Contents	Hr
01	**Introduction to Intellectual Property Rights (IPR)**: Meaning of IPR, Different category of IPR instruments - Patents, Trademarks, Copyrights, Industrial Designs, Plant variety protection, Geographical indications, Transfer of technology etc. **Importance of IPR in Modern Global Economic Environment:** Theories of IPR, Philosophical aspects of IPR laws, Need for IPR, IPR as an instrument of development	05
02	**Enforcement of Intellectual Property Rights:** Introduction, Magnitude of problem, Factors that create and sustain counterfeiting/piracy, International agreements, International organizations (e.g. WIPO, WTO) active in IPR enforcement **Indian Scenario of IPR:** Introduction, History of IPR in India, Overview of IP laws in India, Indian IPR, Administrative Machinery, Major international treaties signed by India, Procedure for submitting patent and Enforcement of IPR at national level etc.	07
03	**Emerging Issues in IPR:** Challenges for IP in digital economy, e-commerce, human genome, biodiversity and traditional knowledge etc.	05
04	**Basics of Patents:** Definition of Patents, Conditions of patentability, Patentable and non-patentable inventions, Types of patent applications (e.g. Patent of addition etc), Process Patent and Product Patent, Precautions while patenting, Patent specification Patent claims, Disclosures and non-disclosures, Patent rights and infringement, Method of getting a patent	07

05	**Patent Rules:** Indian patent act, European scenario, US scenario, Australia scenario, Japan scenario, Chinese scenario, Multilateral treaties where India is a member (TRIPS agreement, Paris convention etc.)	08
06	**Procedure for Filing a Patent (National and International):** Legislation and Salient Features, Patent Search, Drafting and Filing Patent Applications, Processing of patent, Patent Litigation, Patent Publication etc, Time frame and cost, Patent Licensing, Patent Infringement **Patent databases:** Important websites, Searching international databases	07
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or at least 6 assignment on complete syllabus or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCE BOOKS:

1. Rajkumar S. Adukia, 2007, A Handbook on Laws Relating to Intellectual Property Rights in India, The Institute of Chartered Accountants of India
2. Keayla B K, Patent system and related issues at a glance, Published by National Working Group on Patent Laws
3. T Sengupta, 2011, Intellectual Property Law in India, Kluwer Law International
4. Tzen Wong and Graham Dutfield, 2010, Intellectual Property and Human Development: Current Trends and Future Scenario, Cambridge University Press
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14. Vivien Irish, 2005, Intellectual Property Rights for Engineers, IET
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Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 8018	Digital Business Management	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 8018	Digital Business Management	20	20	20	80	03	--	--	100

Objectives:
1. To familiarize with digital business concept
2. To acquaint with E-commerce
3. To give insights into E-business and its strategies

Outcomes: The learner will be able to …..
1. Identify drivers of digital business
2. Illustrate various approaches and techniques for E-business and management
3. Prepare E-business plan

Module	Detailed content	Hours
1	**Introduction to Digital Business-** Introduction, Background and current status, E-market places, structures, mechanisms, economics and impacts Difference between physical economy and digital economy, **Drivers of digital business-** Big Data & Analytics, Mobile, Cloud Computing, Social media, BYOD, and Internet of Things(digitally intelligent machines/services) Opportunities and Challenges in Digital Business,	09
2	**Overview of E-Commerce** **E-Commerce-** Meaning, Retailing in e-commerce-products and services, consumer behavior, market research and advertisement B2B-E-commerce-selling and buying in private e-markets, public B2B exchanges and support services, e-supply chains, Collaborative Commerce, Intra business EC and Corporate portals Other E-C models and applications, innovative EC System-From E-government and learning to C2C, mobile commerce and pervasive computing EC Strategy and Implementation-EC strategy and global EC, Economics and Justification of EC, Using Affiliate marketing to promote your e-commerce business, Launching a successful online business and EC project, Legal, Ethics and Societal impacts of EC	06
3	**Digital Business Support services**: ERP as e –business backbone, knowledge Tope Apps, Information and referral system **Application Development:** Building Digital business Applications and Infrastructure	06

4	**Managing E-Business**-Managing Knowledge, Management skills for e-business, Managing Risks in e –business Security Threats to e-business -Security Overview, Electronic Commerce Threats, Encryption, Cryptography, Public Key and Private Key Cryptography, Digital Signatures, Digital Certificates, Security Protocols over Public Networks: HTTP, SSL, Firewall as Security Control, Public Key Infrastructure (PKI) for Security, Prominent Cryptographic Applications	06
5	**E-Business Strategy**-E-business Strategic formulation- Analysis of Company's Internal and external environment, Selection of strategy, E-business strategy into Action, challenges and E-Transition (Process of Digital Transformation)	04
6	**Materializing e-business: From Idea to Realization**-Business plan preparation **Case Studies and presentations**	08
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or at least 6 assignment on complete syllabus or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

References:

1. A textbook on E-commerce, Er Arunrajan Mishra, Dr W K Sarwade,Neha Publishers & Distributors, 2011
2. E-commerce from vision to fulfilment, Elias M. Awad, PHI-Restricted, 2002
3. Digital Business and E-Commerce Management, 6th Ed, Dave Chaffey, Pearson, August 2014
4. Introduction to E-business-Management and Strategy, Colin Combe, ELSVIER, 2006
5. Digital Business Concepts and Strategy, Eloise Coupey, 2nd Edition, Pearson
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7. Digital Business Discourse Erika Darics, April 2015, Palgrave Macmillan
8. E-Governance-Challenges and Opportunities in : Proceedings in 2nd International Conference theory and practice of Electronic Governance
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Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ILO 8019	Environmental Management	03	--	--	03	--	--	03

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (Hrs.)	Term Work	Practical and Oral	Total
		Internal Assessment			End Sem. Exam.				
		Test1	Test2	Avg.					
ILO 8019	Environmental Management	20	20	20	80	03	--	--	100

Objectives:

1. Understand and identify environmental issues relevant to India and global concerns
2. Learn concepts of ecology
3. Familiarise environment related legislations

Outcomes: Learner will be able to…

1. Understand the concept of environmental management
2. Understand ecosystem and interdependence, food chain etc.
3. Understand and interpret environment related legislations

Module	Detailed Contents	Hrs
01	Introduction and Definition of Environment: Significance of Environment Management for contemporary managers, Career opportunities. Environmental issues relevant to India, Sustainable Development, The Energy scenario.	10
02	Global Environmental concerns : Global Warming, Acid Rain, Ozone Depletion, Hazardous Wastes, Endangered life-species, Loss of Biodiversity, Industrial/Man-made disasters, Atomic/Biomedical hazards, etc.	06
03	Concepts of Ecology: Ecosystems and interdependence between living organisms, habitats, limiting factors, carrying capacity, food chain, etc.	05
04	Scope of Environment Management, Role & functions of Government as a planning and regulating agency. Environment Quality Management and Corporate Environmental Responsibility	10
05	Total Quality Environmental Management, ISO-14000, EMS certification.	05
06	General overview of major legislations like Environment Protection Act, Air (P & CP) Act, Water (P & CP) Act, Wildlife Protection Act, Forest Act, Factories Act, etc.	03
Total		39

Assessment:

Internal:
Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:
Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

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4. Only Four question need to be solved.

REFERENCES:

1. Environmental Management: Principles and Practice, C J Barrow, Routledge Publishers London, 1999
2. A Handbook of Environmental Management Edited by Jon C. Lovett and David G. Ockwell, Edward Elgar Publishing
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6. Introduction to Environmental Management, Mary K Theodore and Louise Theodore, CRC Press
7. Environment and Ecology, Majid Hussain, 3rd Ed. Access Publishing.2015

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECL801	Optical Communication and Networks Laboratory	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical and Oral	Total
		Internal assessment			End Sem. Exam.			
		Test 1	Test 2	Avg.				
ECL801	Optical Communication and Networks Laboratory	--	--	--	--	25	25	50

Course Objectives:

1. To make students understand and familiarize with different types of optical fibers.
2. To enable the students to measure the fundamental parameters such as numerical aperture, losses dispersion for single mode and multimode fibers
3. Expose the students to realize the dynamic change in the network performance when various sources, Amplifiers, detectors, components and fibers are incorporated.
4. In depth exposition to the point-to-point link, metro network,WDM network and DWDM Network
5. To understand the basic concepts and challenges in free space optical systems

Course Outcome:

On completion of this lab course the students will be able to:

1. Acquire proficiency in identifying the different types of fibers and understanding their properties.
2. To measure the losses, dispersion and compensation techniques in all optical network.
3. Learn to design all optical network with amplifiers and modern lasers for error free transmission.
4. To design or implement point to point optical fiber network, WDM or DWDM Network.
5. To design free space optical system with atmospheric impairments and propose mitigation technique for minimum BER.

Suggested Experiment List

- Calculation of Numerical aperture for SMF and MMF
- Calculation of dispersion for given fiber and its measurement
- Calculation of link Loss for given link
- Performance analysis of Single mode fiber
- Performance analysis of multimode fiber
- Performance Analysis of Optical Link with Different Sources
- Performance Analysis of Optical Link with Different Detectors
- Performance Analysis of Optical Amplifier

- Designing of point-to-point optical network using tools or software
- Designing of Metro network/WAN using tools or software
- Designing of WDM network using tools or software
- Designing of FSO using tools or software

Note: Small Project on implementation of indoor fiber optical network or free space optical network can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "Laboratory session batch wise". Computation/simulation-based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "Choice Based Credit and Grading System" manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECP 801	Major Project-II	--	12	--	--	6	--	6

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECP 801	Major Project-II	--	--	--	--	50	100	--	150

Objective: The primary objective is to meet the milestones formed in the overall project plan decided in Project - I. The idea presented in Project -I should be implemented in Project -II with results, conclusion and future work. The project will culminate in the production of a thesis by each individual student.

Guidelines:

Project Report Format:

At the end of the semester the student needs to prepare a project report which should be prepared as per the guidelines issued by the University of Mumbai. Along with the project report a CD containing: project documentation, Implementation code, required utilities, Software_s and user Manuals need to be attached.

Term Work:

Student has to submit weekly progress report to the internal guide and the internal guide has to keep a track on the progress of the project and also has to maintain the attendance report. This progress report can be used for awarding the term work marks. In case of industry projects, visit by internal guide will be preferred to get the status of project. Distribution of marks for term work shall be as follows:

a) Weekly Attendance on Project Day
b) Project work contributions as per objective
c) Project Report (Hard Bound)
d) Term End Presentation (Internal)

The final certification and acceptance of TW ensures the satisfactory performance on the above aspects.

Oral & Practical:

Oral & Practical examination of Project- II should be conducted by Internal and External examiners approved by University of Mumbai. Students have to give presentation and demonstration on the Project-II.